Model Name: P550HVN09.1

Issue Date : 2019/01/03

( )Preliminary Specifications
(＊)Final Specifications

Customer Signature	Date	AUO	Date
Approved By		Approval By PM Director	
Note		Reviewed By RD Director	
		Reviewed By Project Leader	
		Prepared By PM	

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Record of Revision

Version	Date	Page	Description
0.0	2018/01/30		1st release
0.1	2018/06/04	25	Modify the input current value
0.2	2019/01/03	9-10	Update the module drawing

1. General Description

This specification applies to the 55 inch Color TFT-LCD Module P550HVN09.1 This LCD module has a TFT active matrix type liquid crystal panel 1920x1080 pixels, and diagonal size of 55 inch. This module supports 1920x1080 mode. Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 10-bit (8bit + FRC) gray scale signal for each dot.

The P550HVN09.1 has been designed to apply the 10-bit selectable 2 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle.

*** General Information**

1.1. Display Characteristics

Items	Specification	Unit	Note
Active Screen Size	55	**inch**	
Display Area	1209.6 (H) x680.4 (V)	**mm**	
Outline Dimension	1231.4 (H) x 702.2 (V) x 10.1 (D Min.)	**mm**	D Min.: Front bezel to Back Bezel
Driver Element	a-Si TFT active matrix		
Bezel Opening	1213.6 (H) x 684.4 (V)	**mm**	
Display Colors	1073M	**Colors**	
Number of Pixels	1920x1080	**Pixel**	
Pixel Pitch	0.63 (H) x 0.63 (W)	**mm**	
Pixel Arrangement	RGB vertical stripe		
Display Operation Mode	Normally Black		
Surface Treatment	Anti-Glare, 3H		Haze=2%
Rotate Function	Unachievable		Note 1
Display Orientation	Portrait/Landscape Enabled		Note 2

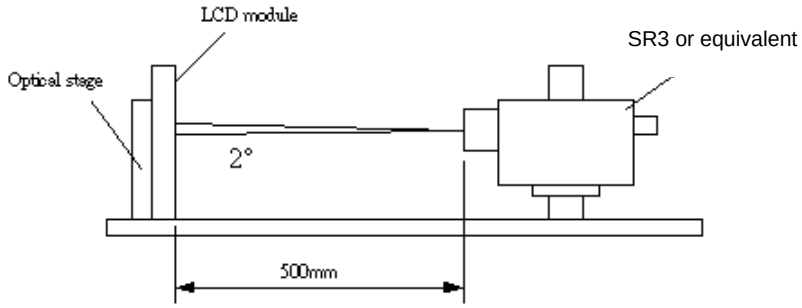
Note 1: Rotate Function refers to LCD display could be able to rotate. This function does not work in this model.

Note 2: Please refer to 1.3.1 Placement Suggestions.

1.2. Optical Characteristics

Optical characteristics are determined on the back-light of measured unit is 'ON' and stabilized after 45~60 minutes in a dark environment at 25°C. The values are specified at 50cm distance from the LCD surface at a viewing angle of φ and θ equal to 0°.

Fig.1 presents additional information concerning the measurement equipment and method.



Parameter		Symbol	Values			Unit	Notes
			Min.	Typ.	Max		
Contrast Ratio		CR	3200	4000	--		1
Surface Luminance (White)		L_{WH} (2D)	360	450	--	cd/m^2	2
Luminance Variation		$\delta_{WHITE(9P)}$	--	--	1.33		3
Response Time (G to G)		Tγ	--	8	--	ms	4
Color Gamut		NTSC		72		%	
Color Coordinates							
	Red	R_X	Typ.-0.03	0.648	Typ.+0.03		
		R_Y		0.337			
	Green	G_X		0.313			
		G_Y		0.614			
	Blue	B_X		0.152			
		B_Y		0.062			
	White	W_X		0.280			
		W_Y		0.290			
Viewing Angle							5
	x axis, right(φ=0°)	θ_r	--	89	--	degree	
	x axis, left(φ=180°)	θ_l	--	89	--	degree	
	y axis, up(φ=90°)	θ_u	--	89	--	degree	
	y axis, down (φ=270°)	θ_d	--	89	--	degree	

Note:

1. Contrast Ratio (CR) is defined mathematically as:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance of } L_{on5}}{\text{Surface Luminance of } L_{off5}}$$

2. Surface luminance is luminance value at point 5 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. LED input VDDB =24V, IDDB. = 2.54A, LWH=Lon5 where Lon5 is the luminance with all pixels displaying white at center 5 location.

3. The variation in surface luminance, δWHITE is defined (center of Screen) as:

$\delta_{WHITE(9P)}$ = Maximum(L_{on1}, L_{on2},…,L_{on9})/ Minimum(L_{on1}, L_{on2},…L_{on9})

4. Response time T_γ is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100% brightness matrix) and is based on F_v=60Hz to optimize.

Measured Response Time		Target				
		0%	25%	50%	75%	100%
Start	0%		0% to 25%	0% to 50%	0% to 75%	0% to 100%
	25%	25% to 0%		25% to 50%	25% to 75%	25% to 100%
	50%	50% to 0%	50% to 25%		50% to 75%	50% to 100%
	75%	75% to 0%	75% to 25%	75% to 50%		75% to 100%
	100%	100% to 0%	100% to 25%	100% to 50%	100% to 75%	

T_γ is determined by 10% to 90% brightness difference of rising or falling period. (As illustrated)

The response time is defined as the following figure and shall be measured by switching the input signal for "any level of gray(bright) " and "any level of gray(dark)".

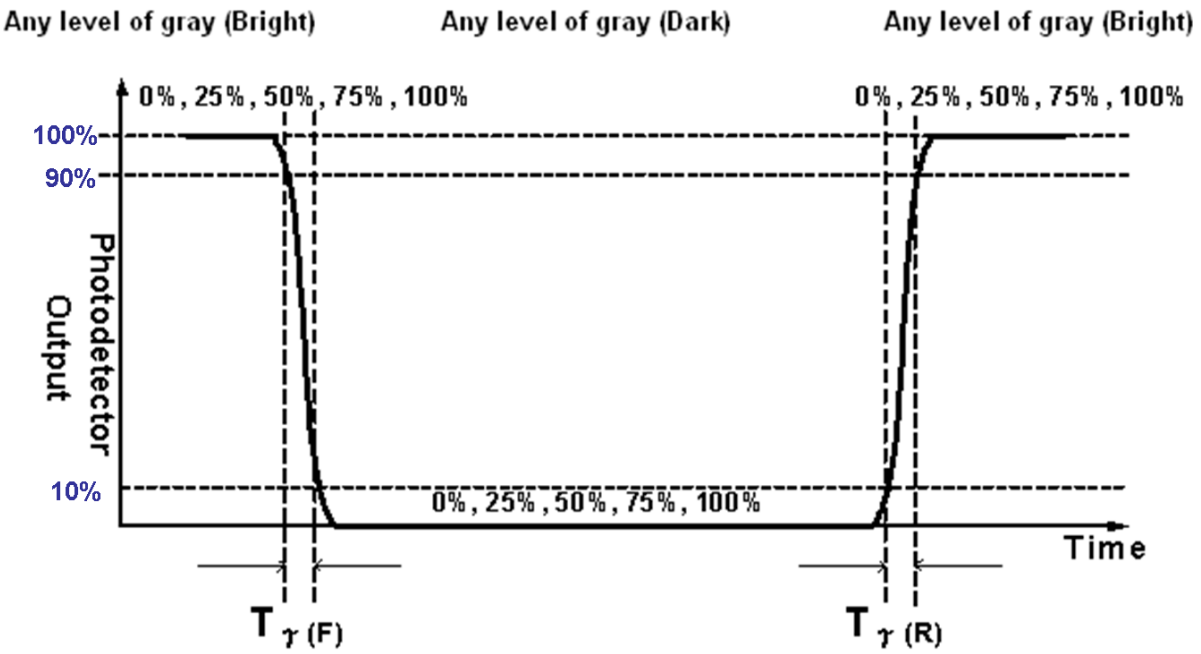
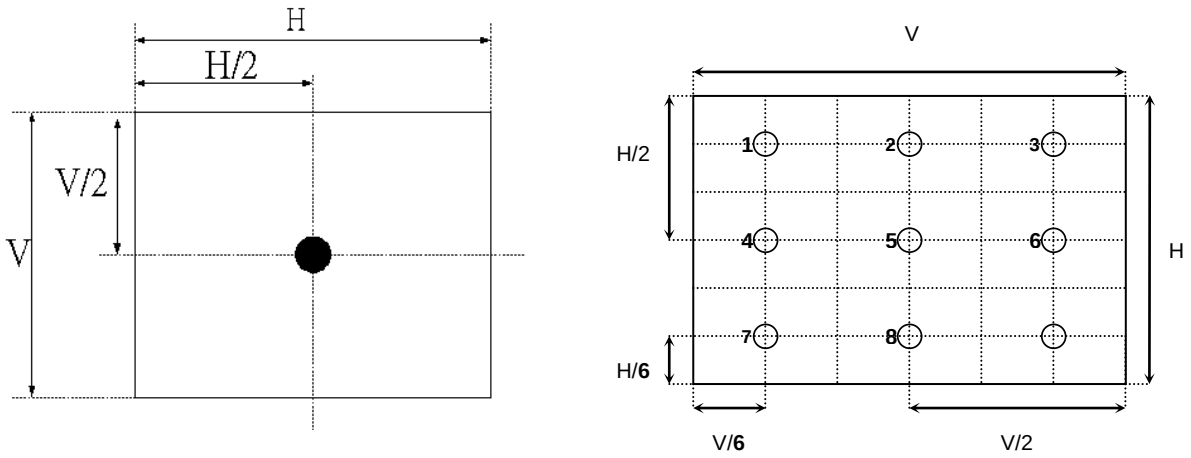
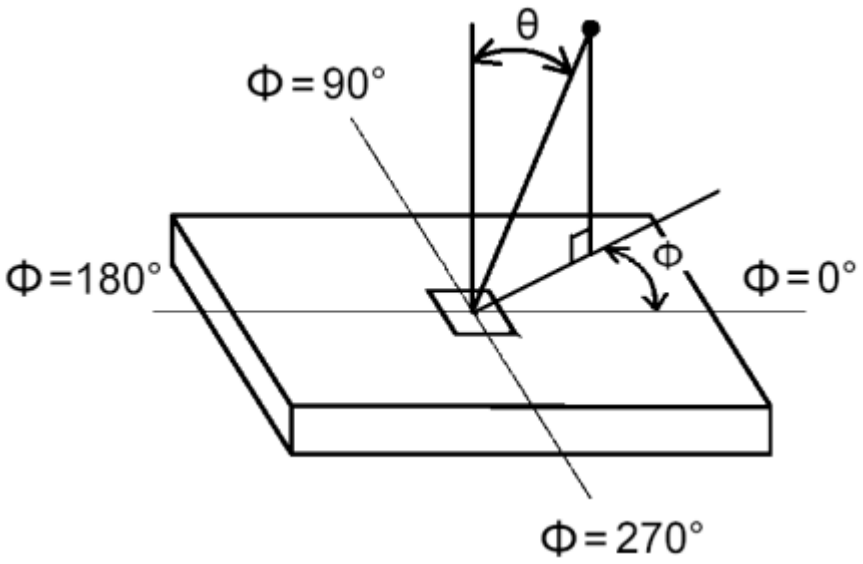


FIG. 2 Luminance



5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG3.

FIG.3 Viewing Angle



1.3. Mechanical Characteristics

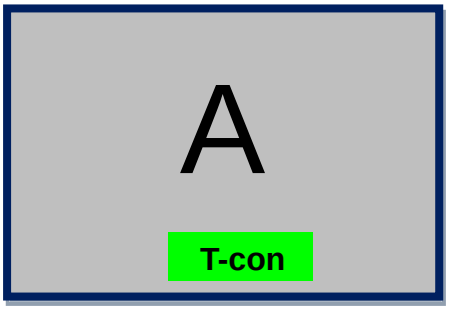
The contents provide general mechanical characteristics for the model P550HVN09.1 In addition the figures in the next page are detailed mechanical drawing of the LCD.

Item		Dimension	Unit	Note
Outline Dimension	Horizontal	1231.4	mm	
	Vertical	702.2	mm	
	Depth (Dmin)	10.1	mm	front bezel to back bezel
	Depth (Dmax)	44.45	mm	to stud
Weight	13200 (Typ)		g	w/ DB

1.3.1. *Placement Suggestions*

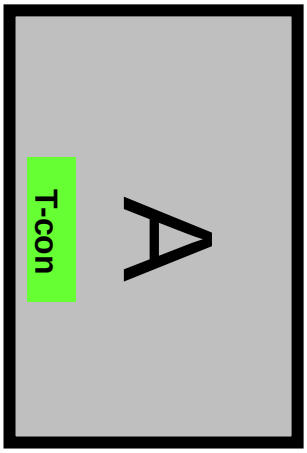
1. Landscape Mode: The default placement is T-Con Side on the lower side and the image is shown upright via viewing from the front.

(Front View)

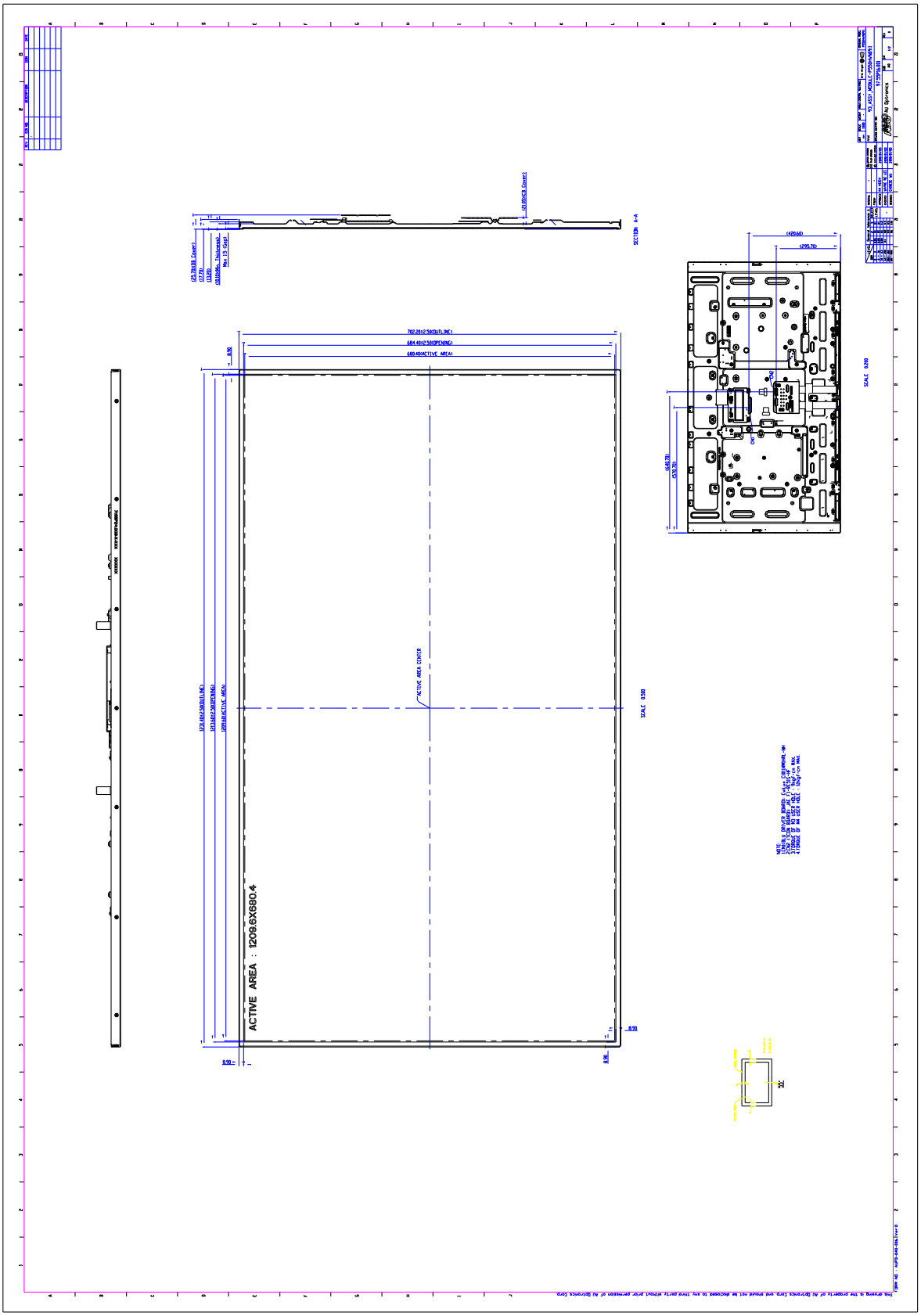


2. Portrait Mode: The default placement is that T-Con side has to be placed on the left side via viewing from the front.

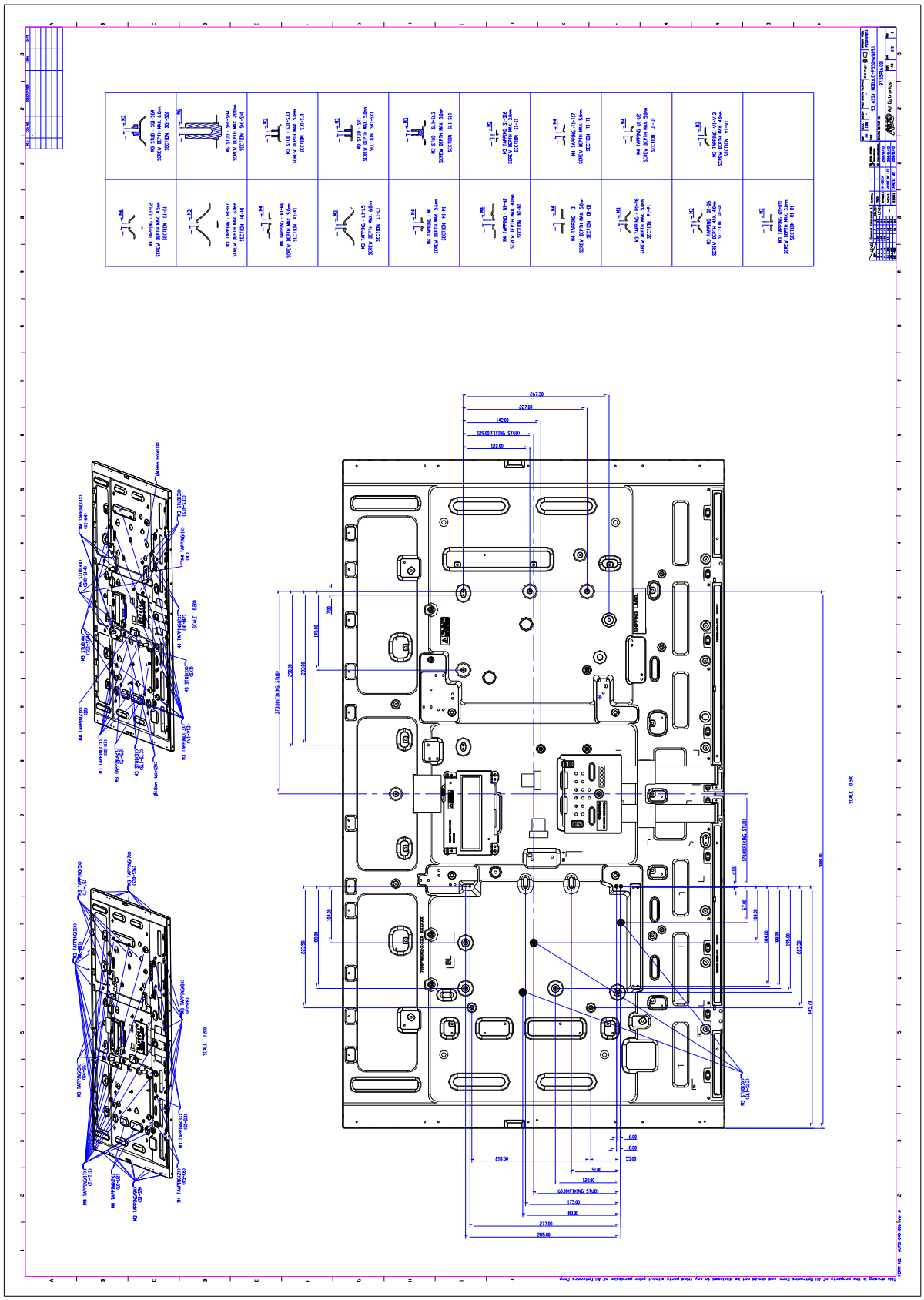
(Front View)



Front View



Back View



2. Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit

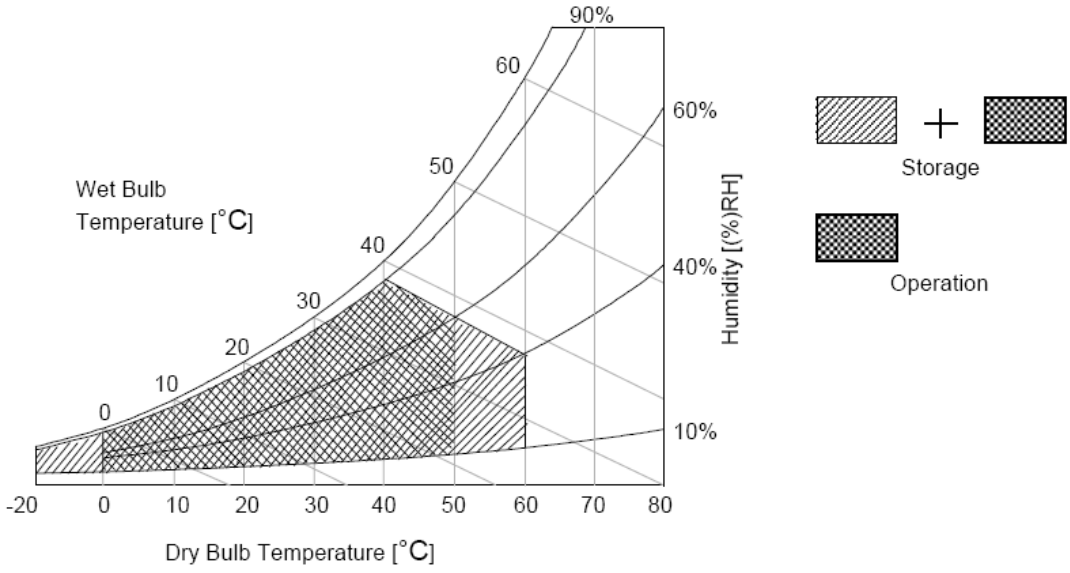
Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	Vcc	-0.3	14	[Volt]	Note 1
Input Voltage of Signal	Vin	-0.3	4	[Volt]	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	Note 3

Note 1: Duration:50 msec.

Note 2 : Maximum Wet-Bulb should be 39℃and No condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40℃or less. At temperatures greater than 40℃, the wet bulb temperature must not exceed 39℃.

Note 3: Surface temperature is measured at 50℃ Dry condition



3. Electrical Specification

The P550HVF12.0 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The other is to power Back Light Unit.

3.1. Electrical Characteristics

3.1.1. *DC Characteristics (Ta = 25 ± 2 ℃)*

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max		
LCD							
Power Supply Input Voltage		V_{DD}	10.8	12	13.2	V_{DC}	1
Power Supply Input Current	Black pattern	I_{DD}	--	0.48	0.64	A	2
	White pattern		--	1.26	1.63	A	
	H-strip pattern		--	0.85	1.14	A	
Power Consumption	Black pattern	P_C	--	5.76	7.68	Watt	
	White pattern		--	15.12	19.56	Watt	
	H-strip pattern		--	10.2	13.68	Watt	
Backlight Power Consumption		P_{BL}		61	66.3	Watt	
Inrush Current		I_{RUSH}	--	--	5.7	A	3
Life time (MTTF)			30000			Hour	4, 5

Note1. The ripple voltage should be fewer than 5% of VDD.

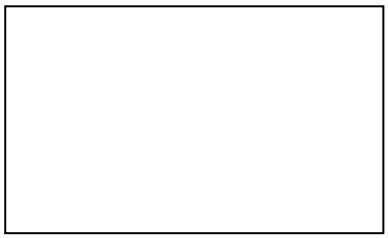
Note2. Test Condition:

(1) V_{DD} = 12.0V, (2) Fv = 60Hz, (3) Fclk= 74.25MHz, (4) Temperature = 25 ℃

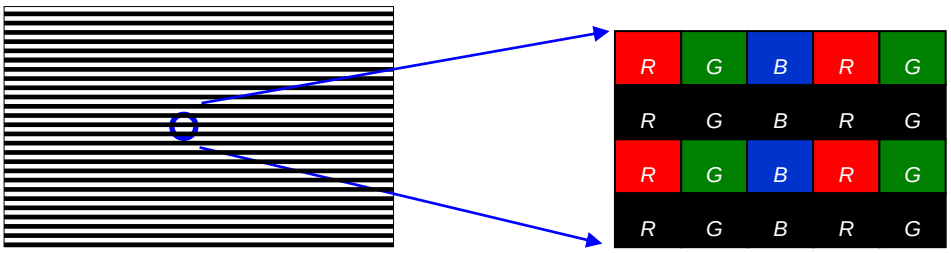
(5) Power dissipation check pattern. (Only for power design)

a. Black pattern

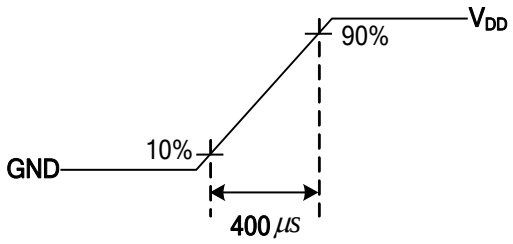
b. White pattern



c. H-Strip pattern



Note3. Measurement condition : Rising time = 400us



Note4. The relative humidity must not exceed 80% non-condensing at temperatures of 40℃ or less. At temperatures greater than 40℃, the wet bulb temperature must not exceed 39℃.

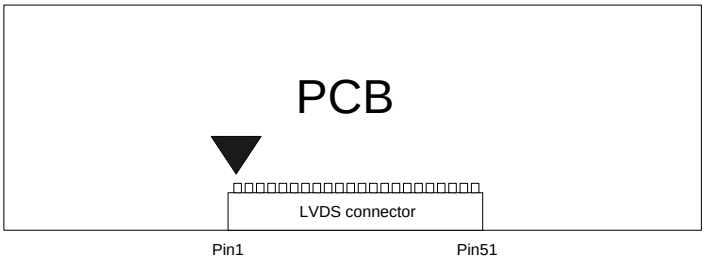
Note5. The lifetime (MTTF) is defined as the time which luminance of LED is 50% compared to its original value. [Operating condition: Continuous operating at Ta = 25±2℃, for single lamp/LED only]

3.2. Interface Connections

- LCD connector: JAE FI-RE51S-HF (LVDS connector) or compatible

PIN	Symbol	Description	Note	PIN	Symbol	Description	Note
1	N.C.	No connection	2	26	GND	Ground	
2	SCL	I2C Clock	3,4	27	GND	Ground	
3	WP	Write Protection	3,5	28	CH2_0-	LVDS Channel 2, Signal 0-	
4	SDA	I2C Data	3,4	29	CH2_0+	LVDS Channel 2, Signal 0+	
5	N.C.	No connection		30	CH2_1-	LVDS Channel 2, Signal 1-	
6	N.C.	No connection	2	31	CH2_1+	LVDS Channel 2, Signal 1+	
7	LVDS_SEL	LVDS data format selection	3,6	32	CH2_2-	LVDS Channel 2, Signal 2-	
8	N.C.	No connection	2	33	CH2_2+	LVDS Channel 2, Signal 2+	
9	N.C.	No connection	2	34	GND	Ground	
10	N.C.	No connection	2	35	CH2_CLK-	LVDS Channel 2, Clock -	
11	GND	Ground		36	CH2_CLK+	LVDS Channel 2, Clock +	
12	CH1_0-	LVDS Channel 1, Signal 0-		37	GND	Ground	
13	CH1_0+	LVDS Channel 1, Signal 0+		38	CH2_3-	LVDS Channel 2, Signal 3-	
14	CH1_1-	LVDS Channel 1, Signal 1-		39	CH2_3+	LVDS Channel 2, Signal 3+	
15	CH1_1+	LVDS Channel 1, Signal 1+		40	CH2_4-	LVDS Channel 2, Signal 4-	
16	CH1_2-	LVDS Channel 1, Signal 2-		41	CH2_4+	LVDS Channel 2, Signal 4+	
17	CH1_2+	LVDS Channel 1, Signal 2+		42	GND	Ground	
18	GND	Ground		43	N.C.	No connection	2
19	CH1_CLK-	LVDS Channel 1, Clock -		44	GND	Ground	
20	CH1_CLK+	LVDS Channel 1, Clock +		45	GND	Ground	
21	GND	Ground		46	GND	Ground	
22	CH1_3-	LVDS Channel 1, Signal 3-		47	N.C.	No connection	2
23	CH1_3+	LVDS Channel 1, Signal 3+		48	V_{DD}	Power Supply Input Voltage	
24	CH1_4-	LVDS Channel 1, Signal 4-		49	V_{DD}	Power Supply Input Voltage	
25	CH1_4+	LVDS Channel 1, Signal 4+		50	V_{DD}	Power Supply Input Voltage	
				51	V_{DD}	Power Supply Input Voltage	

Note1. Pin number start from the left side as the following figure.



Note2. Please leave this pin unoccupied. It can not be connected by any signal (Low/GND/High).

Note3. Input control signal threshold voltage definition

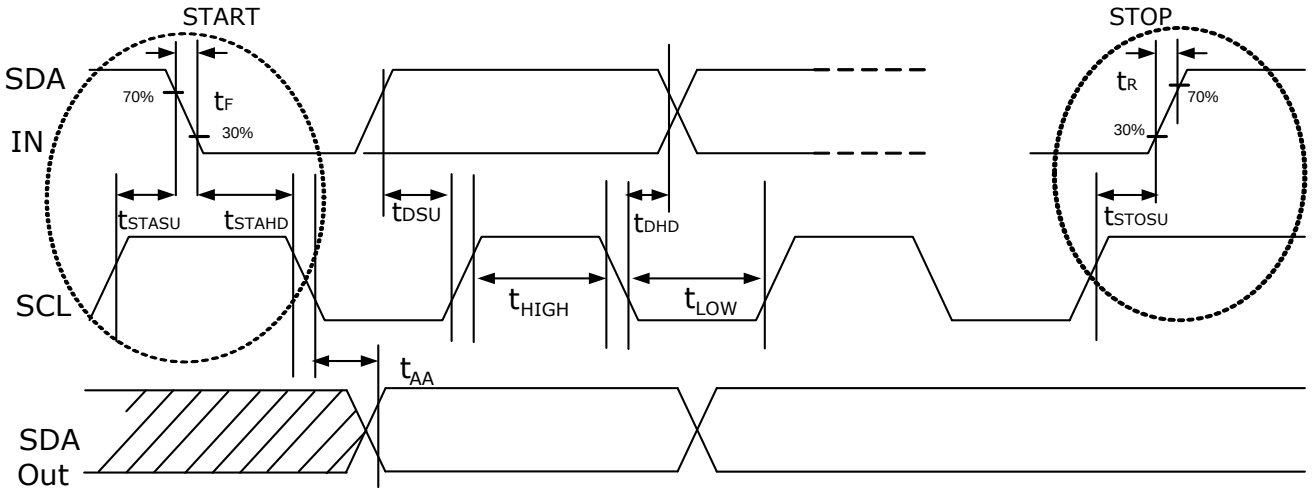
Item	Symbol	Min.	Typ.	Max.	Unit
Input High Threshold Voltage	VIH	2.7	-	3.6	V
Input Low Threshold Voltage	VIL	0	-	0.6	V

Note4. I2C Data and Clock

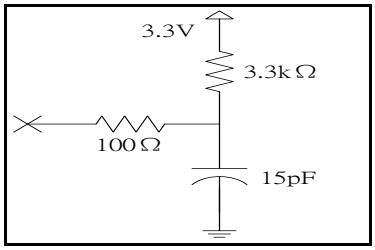
I2C Data and Clock timing

Parameter		Symbol	Min.	Typ.	Max	Unit
I2C	SCL clock frequency	fSCL	-	-	350	kHz
	Clock Pulse Width Low	tLOW	1.85	-	-	us
	Clock Pulse Width High	tHIGH	0.4	-	-	us
	Clock Low to Data Output Valid	tAA	1.76	-	-	us
	Start Setup Time	tSTASU	0.6	-	-	us
	Start Hold Time	tSTAHD	0.6	-	-	us
	Stop Setup Time	tSTOSU	0.6	-	-	us
	Data In Setup Time	tDSU	0.1	-	-	us
	Data In Hold Time	tDHD	0	-	-	us
	SCL/SDA Rise Time	tR	-	-	0.3	us
	SCL/SDA Fall Time	tF	-	-	0.3	us

I2C Read/Write Timing



Input equivalent impedance of SDA/SCL pin

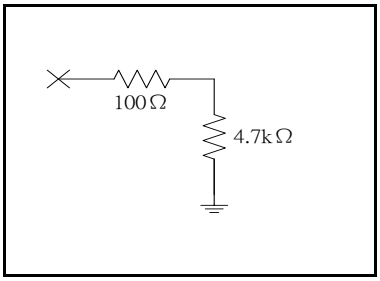


Note5. Write Protection

Mode selection

WP	Note
L or OPEN	Protection
H	Writable

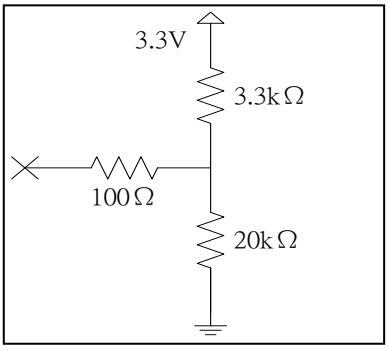
Input equivalent impedance of WP pin



Note6. LVDS data format selection

LVDS_SEL	Mode
H or OPEN	NS
L	Jeida

Input equivalent impedance of LVDS_SEL pin

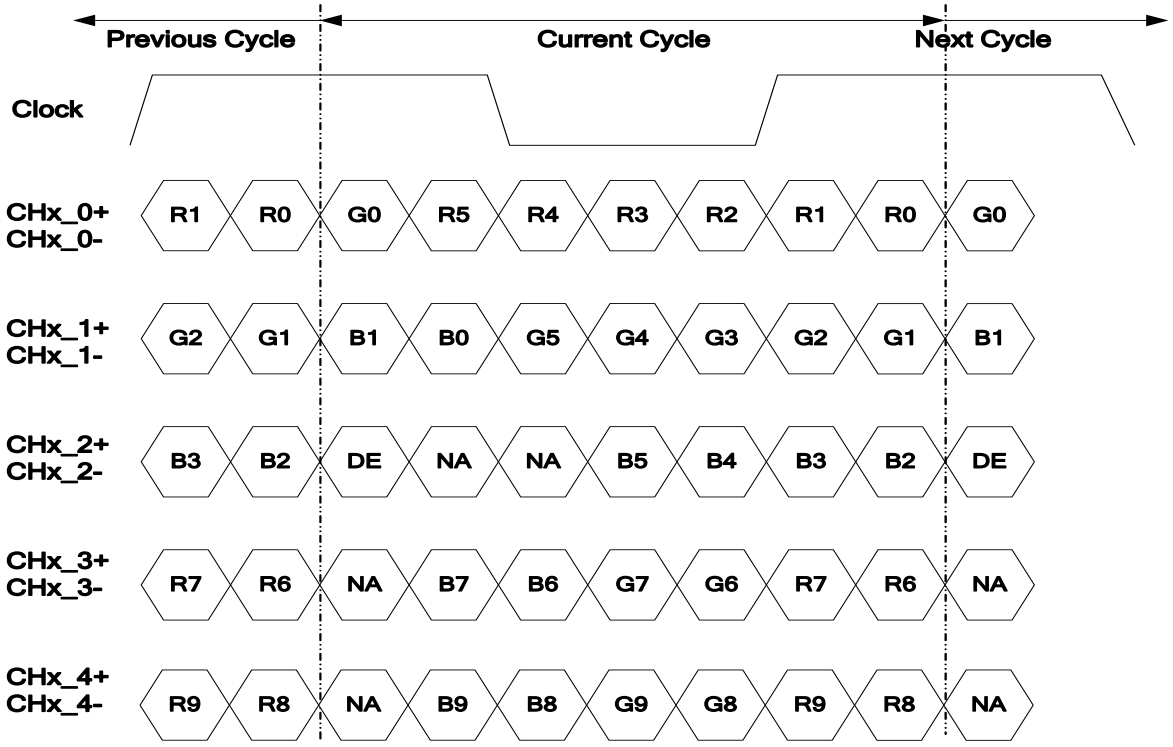


3.3. Input Data Format

3.3.1. *Data mapping*

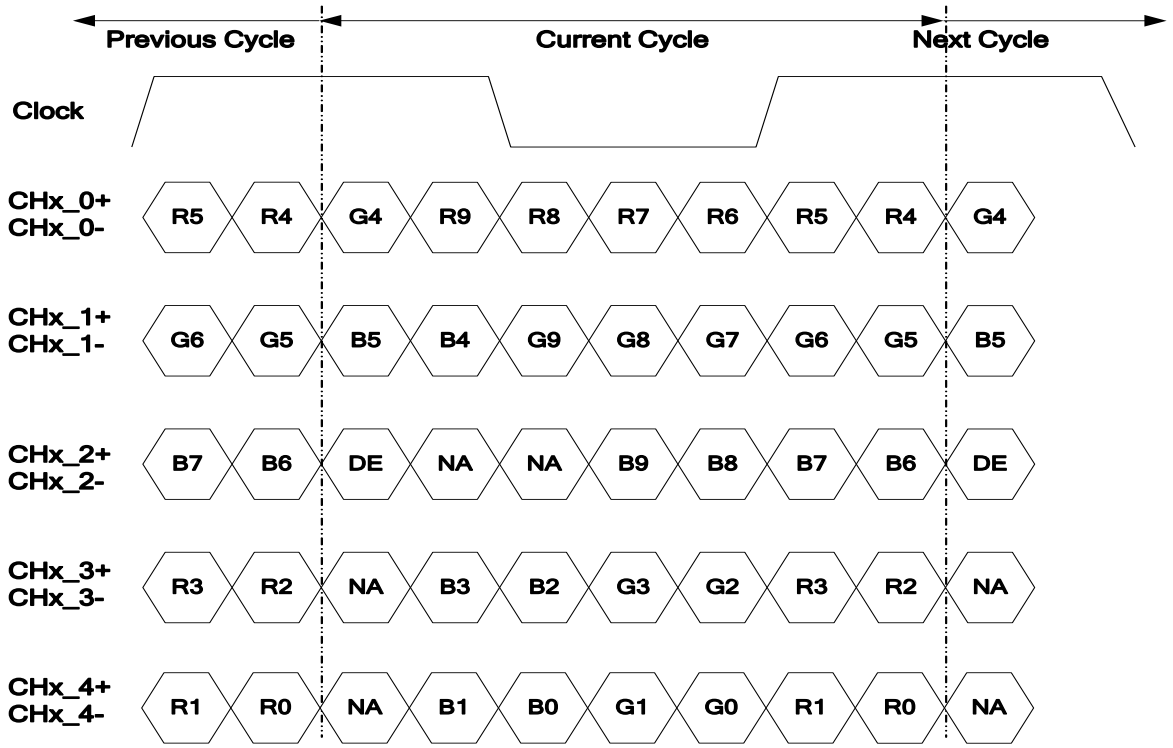
LVDS Option for 10bit

- LVDS Option = High/OPEN ➔NS



Note: x = 1, 2, 3, 4…

- LVDS Option = Low ➔JEIDA



Note: x = 1, 2, 3, 4…

3.3.2. *Color Input Data Reference*

The brightness of each primary color (red, green and blue) is based on the 10 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

LVDS Option for 10bit

Color		Input Color Data																													
		RED										GREEN										BLUE									
		MSB									LSB	MSB									LSB	MSB									LSB
		R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	G0	B9	B8	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	Blue(1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
R	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(001)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

	RED(1022)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

	GREEN(1022)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
B	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1

	BLUE(1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0
	BLUE(1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1

3.4. Signal Timing Specification

This is the signal timing required at the input of the user connector. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

Timing Table (DE only Mode)

Signal	Item	Symbol	Min.	Typ.	Max	Unit
Vertical Section	Period	Tv	1100	1125	1480	Th
	Active	Tdisp (v)		1080		
	Blanking	Tblk (v)	20	45	400	Th
Horizontal Section	Period	Th	1030	1100	1325	Tclk
	Active	Tdisp (h)		960		
	Blanking	Tblk (h)	70	140	365	Tclk
Clock	Frequency	Fclk=1/Tclk	53	74.25	82	MHz
Vertical Frequency	Frequency	Fv	47	60	63	Hz
Horizontal Frequency	Frequency	Fh	60	67.5	73	KHz

Notes:

(1) Display position is specific by the rise of DE signal only.

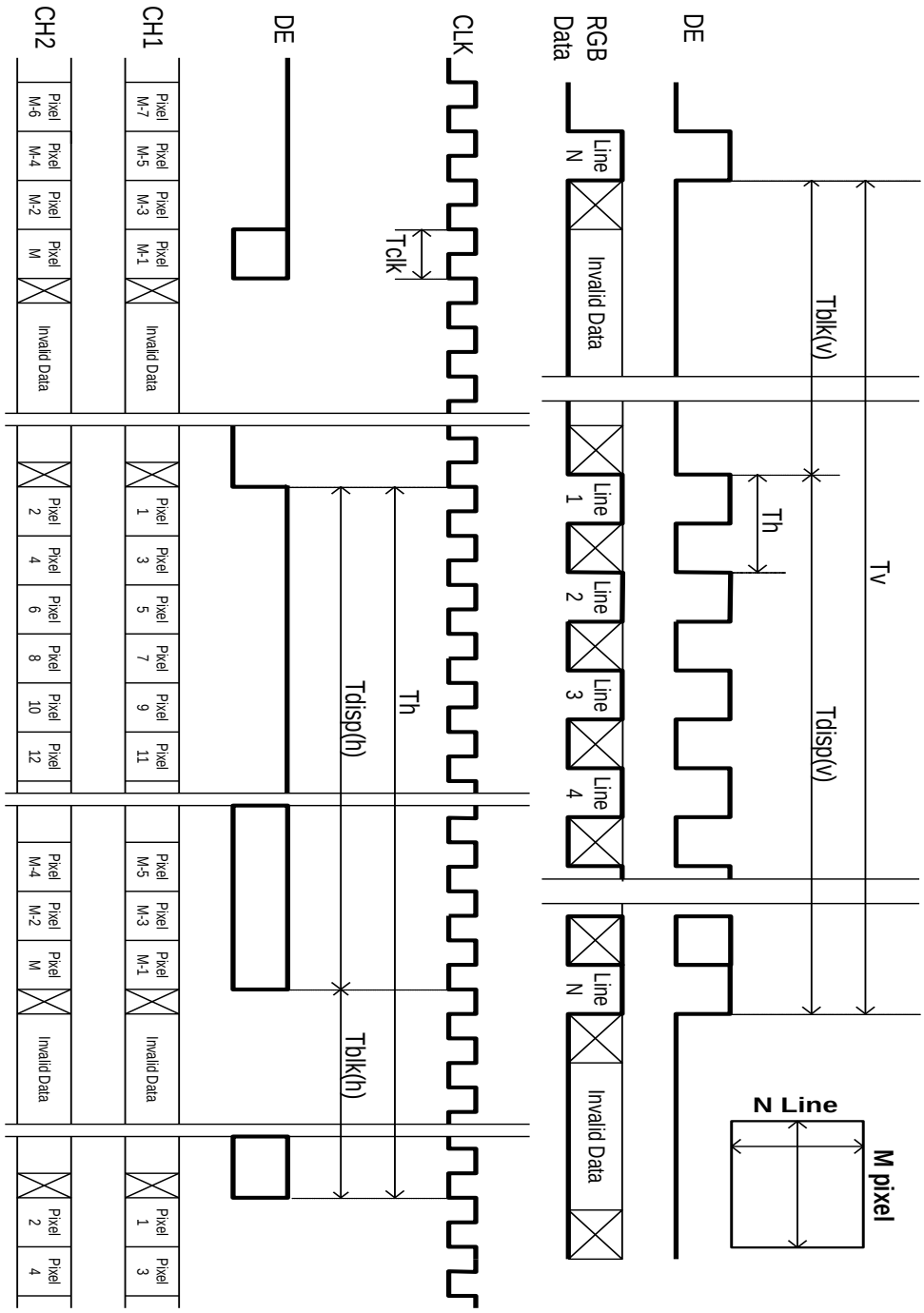
Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.

(2)Vertical display position is specified by the rise of DE after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of 1st DE is displayed at the top line of screen.

(3)If a period of DE "High" is less than 1920 DCLK or less than 1080 lines, the rest of the screen displays black.

(4)The display position does not fit to the screen if a period of DE "High" and the effective data period do not synchronize with each other.

Signal Timing Waveforms



Note1. Display position is specific by the rise of DE signal only.

Horizontal display position is specified by the rising edge of 1st DCLK after the rise of 1st DE, is displayed on the left edge of the screen.

Note2.Vertical display position is specified by the rise of DE after a “Low” level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of 1st DE is displayed at the top line of screen

Note3. If a period of DE “High” is less than 3840 DCLK or less than 2160 lines, the rest of the screen displays black.

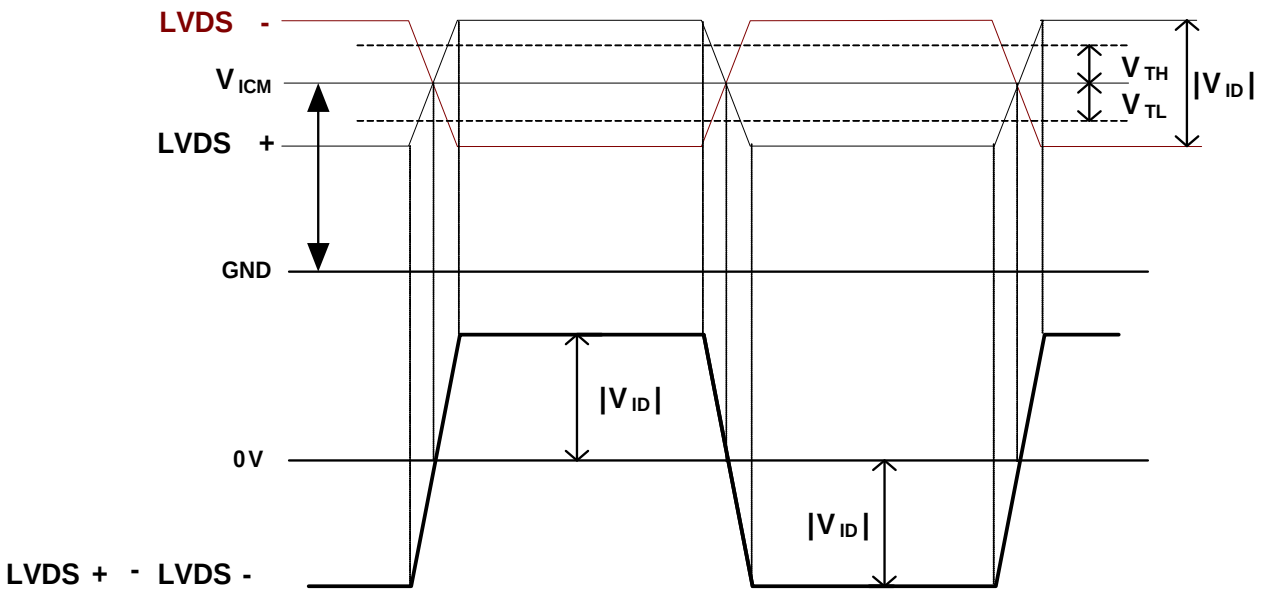
Note4. The display position does not fit to the screen if a period of DE “High” and the effective data period do not synchronize with each other.

3.5. Input interface characteristics

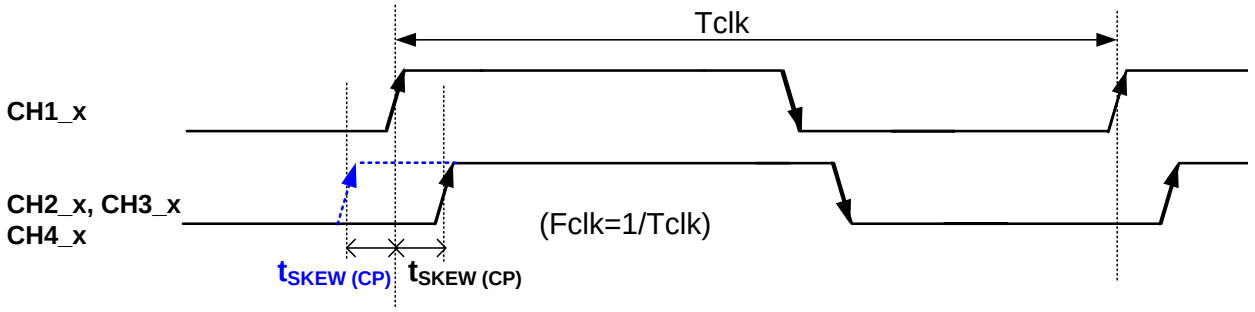
3.5.1. *LVDS*

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max		
LVDS Interface	Input Differential Voltage	$\lvert V_{ID} \rvert$	200	400	600	mV_{DC}	1
	Differential Input High Threshold Voltage	V_{TH}	+100	--	+300	mV_{DC}	1
	Differential Input Low Threshold Voltage	V_{TL}	-300	--	-100	mV_{DC}	1
	Input Common Mode Voltage	V_{ICM}	1.1	1.25	1.4	V_{DC}	1
	Input Channel Pair Skew Margin	$t_{SKEW\ (CP)}$	-500	--	+500	ps	2
	Input Channel Pair Skew Margin (only for M'Star MST7428BB)	$t_{SKEW\ (CP)}$	-400	--	+400	ps	2
	Receiver Clock : Spread Spectrum Modulation range	Fclk_ss	Fclk -3%	--	Fclk +3%	MHz	3
	Receiver Clock : Spread Spectrum Modulation frequency	Fss	30	--	200	KHz	3
	Receiver Data Input Margin Fclk = 85 MHz Fclk = 65 MHz	tRMG	 -0.4 -0.5	 -- --	 0.4 0.5	ns	4

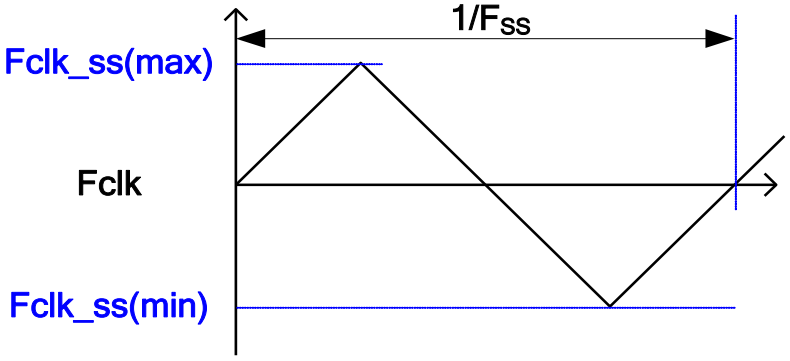
Note1. VICM = 1.25V



Note2. Input Channel Pair Skew Margin

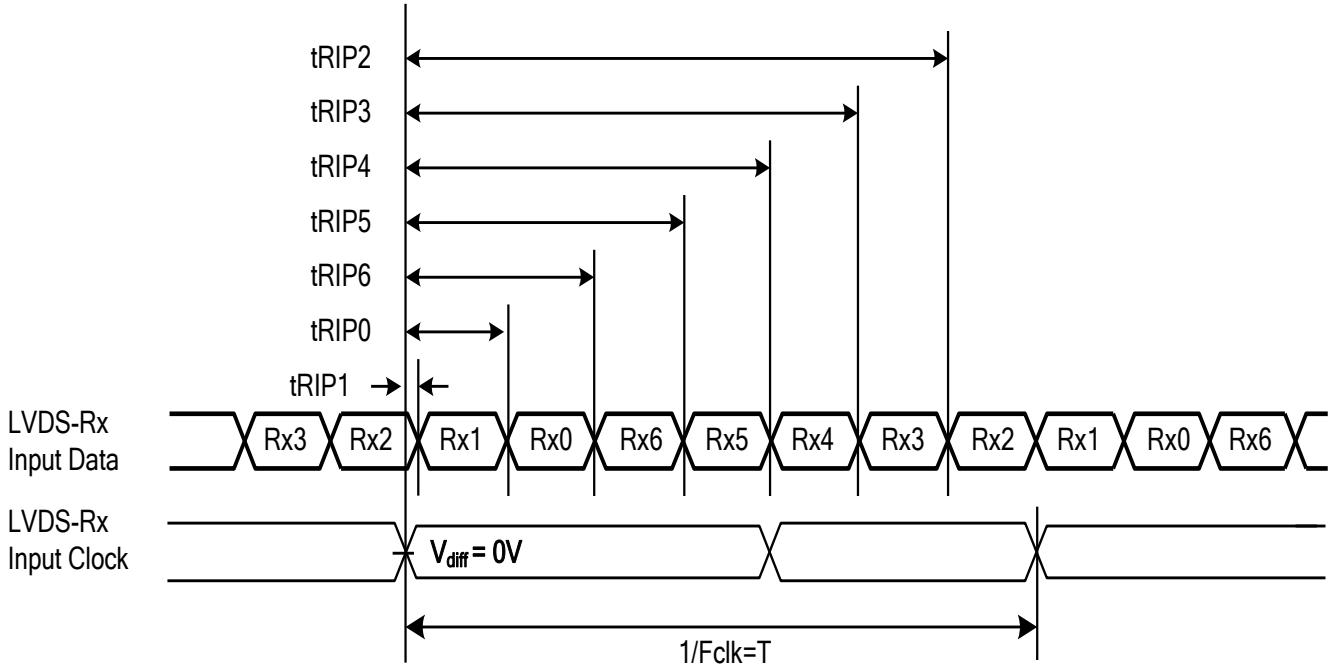


Note3. LVDS Receiver Clock SSCG (Spread spectrum clock generator) is defined as below figures.

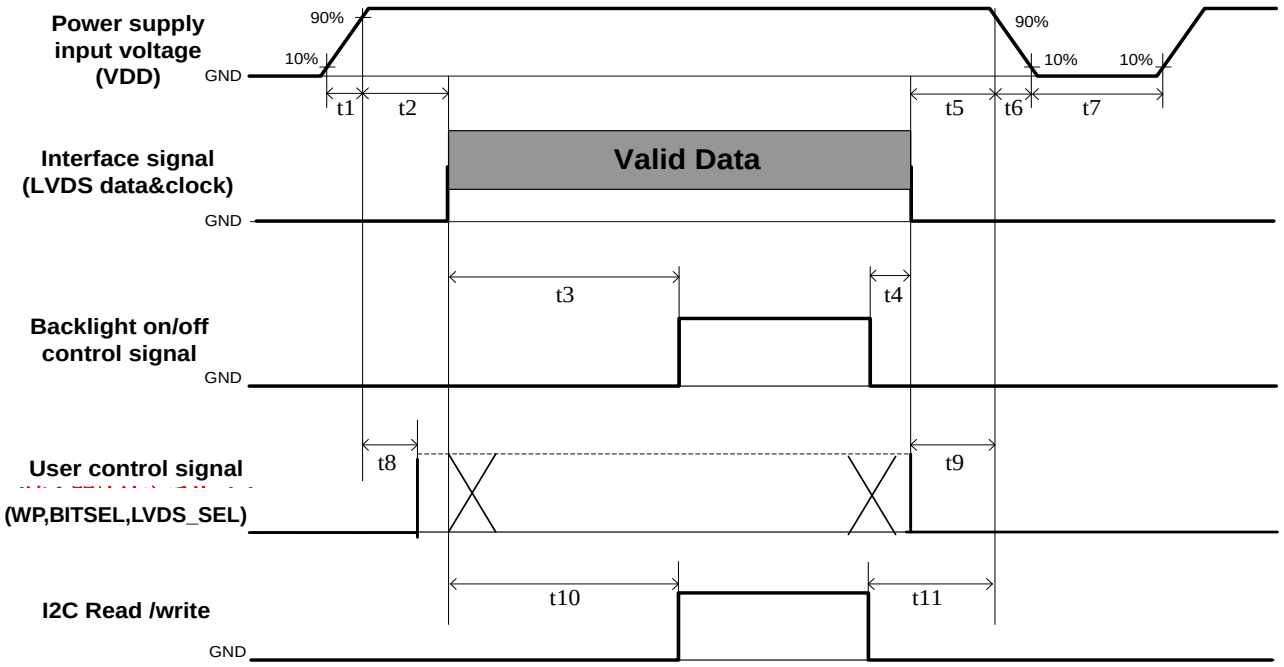


Note4. Receiver Data Input Margin

Parameter	Symbol	Rating			Unit	Note
		Min	Type	Max		
Input Clock Frequency	Fclk	Fclk (min)	--	Fclk (max)	MHz	**T=1/Fclk**
Input Data Position0	tRIP1	-|tRMG|	0	|tRMG|	ns	
Input Data Position1	tRIP0	T/7-|tRMG|	T/7	T/7+|tRMG|	ns	
Input Data Position2	tRIP6	2T/7-|tRMG|	2T/7	2T/7+|tRMG|	ns	
Input Data Position3	tRIP5	3T/7-|tRMG|	3T/7	3T/7+|tRMG|	ns	
Input Data Position4	tRIP4	4T/7-|tRMG|	4T/7	4T/7+|tRMG|	ns	
Input Data Position5	tRIP3	5T/7-|tRMG|	5T/7	5T/7+|tRMG|	ns	
Input Data Position6	tRIP2	6T/7-|tRMG|	6T/7	6T/7+|tRMG|	ns	



3.6. Power Sequence for LCD



Parameter	Values			Unit
	Min.	Type.	Max.	
t1	0.4	---	30	ms
t2	0.1	---	50	ms
t3	400	---	---	ms
t4	0[*1]	---	---	ms
t5	0	---	---	ms
t6	---	---	---[*2]	ms
t7	1000[*3]	---	---	ms
t8	20[*4]	---	50	ms
t9	0	---	---	ms
t10	400	---	---	ms
t11	150	---	---	ms

Note:

(1) t4=0 : concern for residual pattern before BLU turn off.

(2) t6 : voltage of VDD must decay smoothly after power-off. (customer system decide this value)

(3) t7 : When the power supply input voltage(VDD) is off, be sure to pull down the valid and invalid data to 0V.

(4) When user control signal is N.C. (no connection), opened in Transmitted end, t8 timing spec can be negligible.

3.7. Backlight Specification

3.7.1. *Electrical specification (Ta = 25 ± 2 ℃)*

	Item	Symbol		Condition	Spec			Unit	Note
					Min	Typ	Max		
1	Input Voltage	VDDB		-	22.8	24	25.2	VDC	-
2	Input Current	I_{DDB}		VDDB=24V		3.2	3.54	ADC	1
3	Input Power	P_{DDB}		VDDB=24V		76.8	85	W	1
4	Inrush Current	I_{RUSH}		VDDB=24V			4	ADC	2
5	On/Off control voltage	V_{BLON}	ON	VDDB=24V	2	-	5.5	VDC	-
			OFF		0	-	0.8		-
6	On/Off control current	I_{BLON}		VDDB=24V	-	-	1.5	mA	-
7	External PWM Control Voltage	V_EPWM	MAX	VDDB=24V	2	-	5.5	VDC	-
			MIN	VDDB=24V	0	-	0.8		-
8	External PWM Control Current	I_EPWM		VDDB=24V	-	-	2	mADC	-
9	External PWM Duty ratio	D_EPWM		VDDB=24V	10	-	100	%	3
10	External PWM Frequency	F_EPWM		VDDB=24V	90	20K	50K	Hz	-
11	DET status signal	DET	HI	VDDB=24V	Open Collector			VDC	4
			Lo		0	-	0.8	VDC	4
12	Input Impedance	Rin		VDDB=24V	300			Kohm	-

Note 1 : Dimming ratio= 100% (MAX)（Ta=25±5℃, Turn on for 45minutes）
Note 2: Measurement condition Rising time = 20ms (VDDB : 10%~90%);
Note 3: Dimming linearity only guarantee between 20 ~ 80% duty. Less than 10% dimming control is functional well and no backlight shutdown happened
Note 4: Normal : 0~0.8V ; Abnormal : Open collector

3.7.2. *Input Pin Assignment*

LED driver board connector:

CN1: S14B-PHA-SM3-TB (HF) (JST)

Pin No	Symbol	Description
1	VDDB	Operatng Voltage Supply, +24V DC regulated
2	VDDB	Operatng Voltage Supply, +24V DC regulated
3	VDDB	Operatng Voltage Supply, +24V DC regulated
4	VDDB	Operatng Voltage Supply, +24V DC regulated
5	VDDB	Operatng Voltage Supply, +24V DC regulated
6	BLGND	Ground and Current Return
7	BLGND	Ground and Current Return
8	BLGND	Ground and Current Return
9	BLGND	Ground and Current Return
10	BLGND	Ground and Current Return
11	DET	BLU status detection: Normal: 0~0.8V; Abnormal: Open collector (Recommend Pull high R > 10K, VDD=3.3V)
12	VBLON	BL On-Off: High/Open (2.0V~5.5V) for BL On, Low (GND) for off
13	NC	NA
14	External PWM(PDIM)	External PWM (0%~100%)

CN2: CN2: CI0112M1HRD-NH (Cvilux)

Pin No	Symbol	Description
1	VLED1 +	LB1+ High Voltage
2	VLED1 +	LB1+ High Voltage
3	EB1-1 -	LB1-string1 feedback
4	EB1-2 -	LB1-string2 feedback
5	EB1-3 -	LB1-string3 feedback
6	EB1-4 -	LB1-string4 feedback
7	FB2-4 -	LB2-string4 feedback
8	FB2-3 -	LB2-string3 feedback

9	FB2-2 -	LB2-string2 feedback
10	FB2-1 -	LB2-string1 feedback
11	VLED2 +	LB2+ High Voltage
12	VLED2 +	LB2+ High Voltage

Note1. DET status

DET	BLU status
0 ~ 0.8V	Normal
Open collector	Abnormal

Recommend pull high R > 10K ohm, pull high voltage VDD = 3.3V

Note2. Input control signal threshold voltage definition

Item	Symbol	Min.	Typ.	Max.	Unit
Input High Threshold Voltage	VIH	2	-	5.5	V
Input Low Threshold Voltage	VIL	0	-	0.8	V

Note3. VBLON

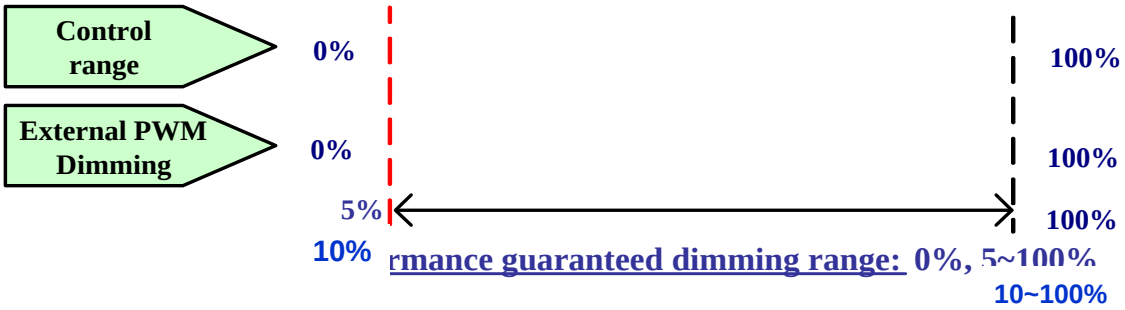
Mode selection

WP	Note
H or OPEN	BL On
L	BL Off

Note4. Please leave this pin unoccupied. It can not be connected by any signal (Low/GND/High).

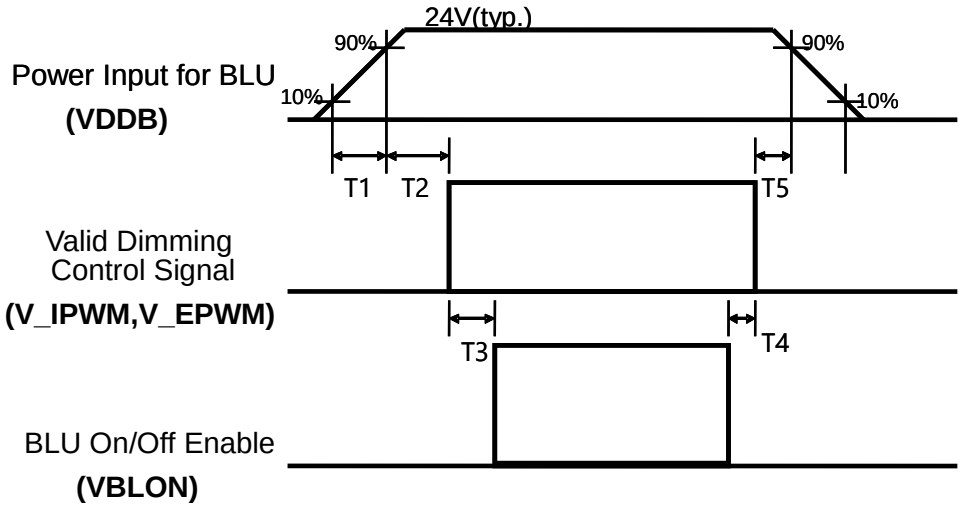
Note5. PDIM

PWM Dimming range:

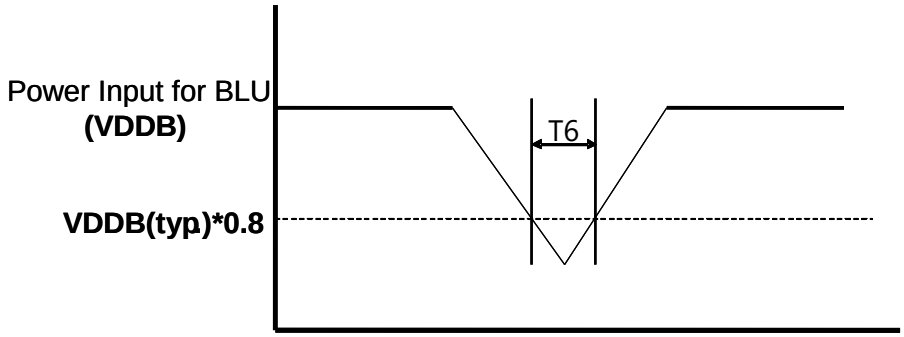


IF External PWM function less than 10% dimming ratio, Judge condition as below:
(1)Backlight module must be lighted ON normally.
(2)All protection function must work normally.
(3)Uniformity and flicker could not be guaranteed

3.7.3. *Power Sequence for Backlight*



Dip condition



Parameter	Value			Units
	Min	Typ	Max	
T1	20	-	-	ms
T2	250	-	-	ms
T3	200			ms
T4	0	-	-	ms
T5	0	-	-	ms
T6	-	-	1000	ms*1

Note: 1.T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time. Even though T1 is over the specified value, there is no problem if I2t spec of fuse is satisfied.
2. T8 describes VDDB dip condition and VDDB couldn't lower than 10% VDDB.

3.7.4. *LED Operating Life Time*

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max		
Backlight Operating Life Time(MTTF)		30000			Hour	1

Note:

1. The lifetime (MTTF) is defined as the time which luminance of LED is 50% compared to its original value. [Operating condition: Continuous operating at Ta = 25±2℃, for single lamp/LED only]

4. Reliability Test Items

	Test Item	Q'ty	Condition
1	High temperature storage test	3	60℃, 500hrs
2	Low temperature storage test	3	-20℃, 500hrs
3	High temperature operation test	3	50℃, 500hrs
4	Low temperature operation test	3	-5℃, 500hrs
5	Vibration test (With carton)	1(PKG)	Random wave (1.04Grms 2~200Hz) Duration : X,Y,Z 20min per axes
6	Drop test (With carton)	1(PKG)	Surround four flats drop height:15 cm Bottom flat drop height:25.4 cm twice (ASTMD4169)

5. International Standard

5.1. Safety

(1) **UL 60950-1; Standard for Safety of Information Technology Equipment Including electrical Business Equipment.**

(2) **IEC 60950-1; Standard for Safety of International Electrotechnical Commission**

(3) **EN 60950-1; European Committee for Electrotechnical Standardization (CENELEC), EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.**

5.2. EMC

(1) **ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National standards Institute(ANSI), 1992**

(2) **C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special committee on Radio Interference.**

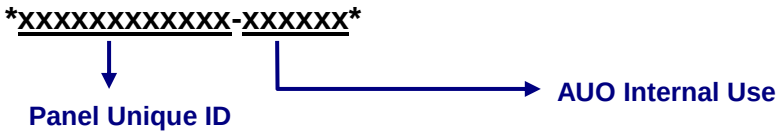
(3) **EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information**

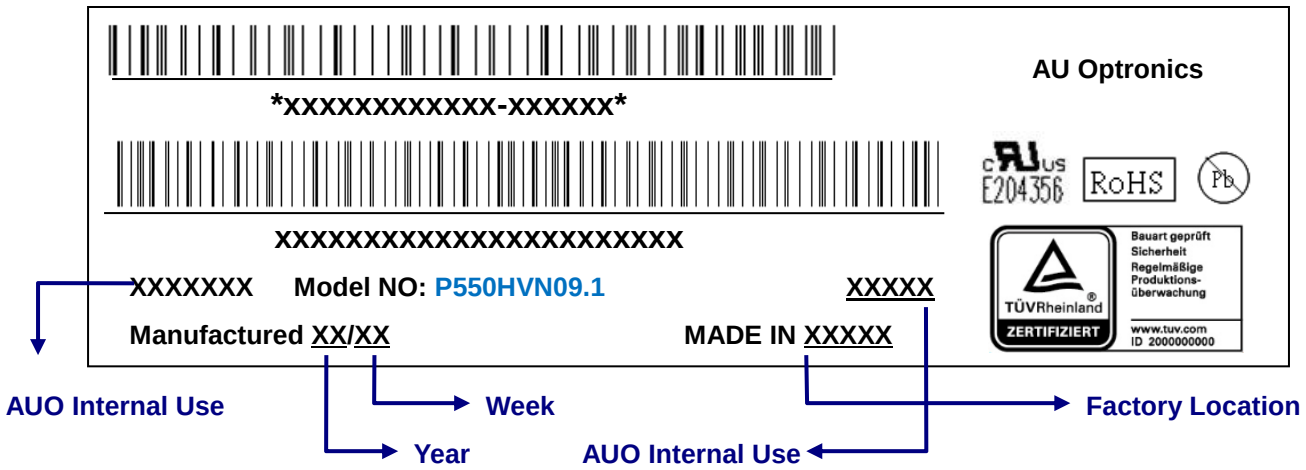
Technology Equipment." European Committee for Electrotechnical Standardization. (CENELEC), 1998

6. Packing

6.1. Definition of Label

A. Panel Label:





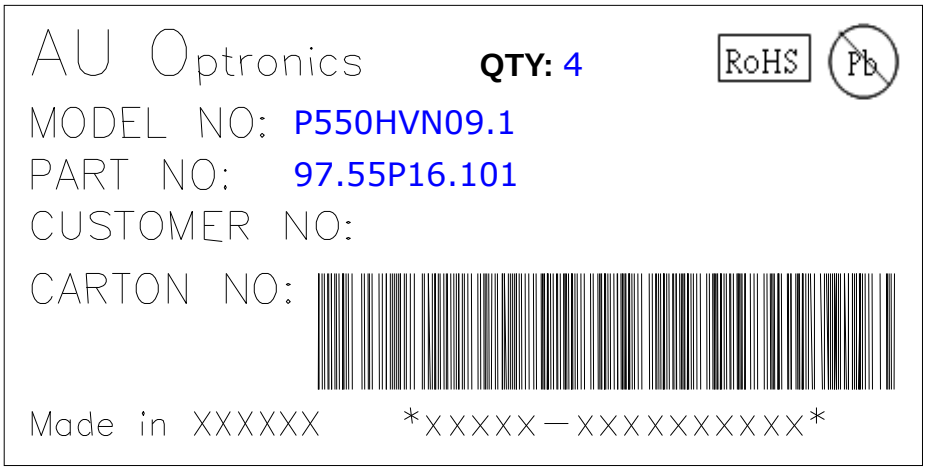
Green mark description

(1) **For Pb Free Product, AUO will add** Pb **for identification.**

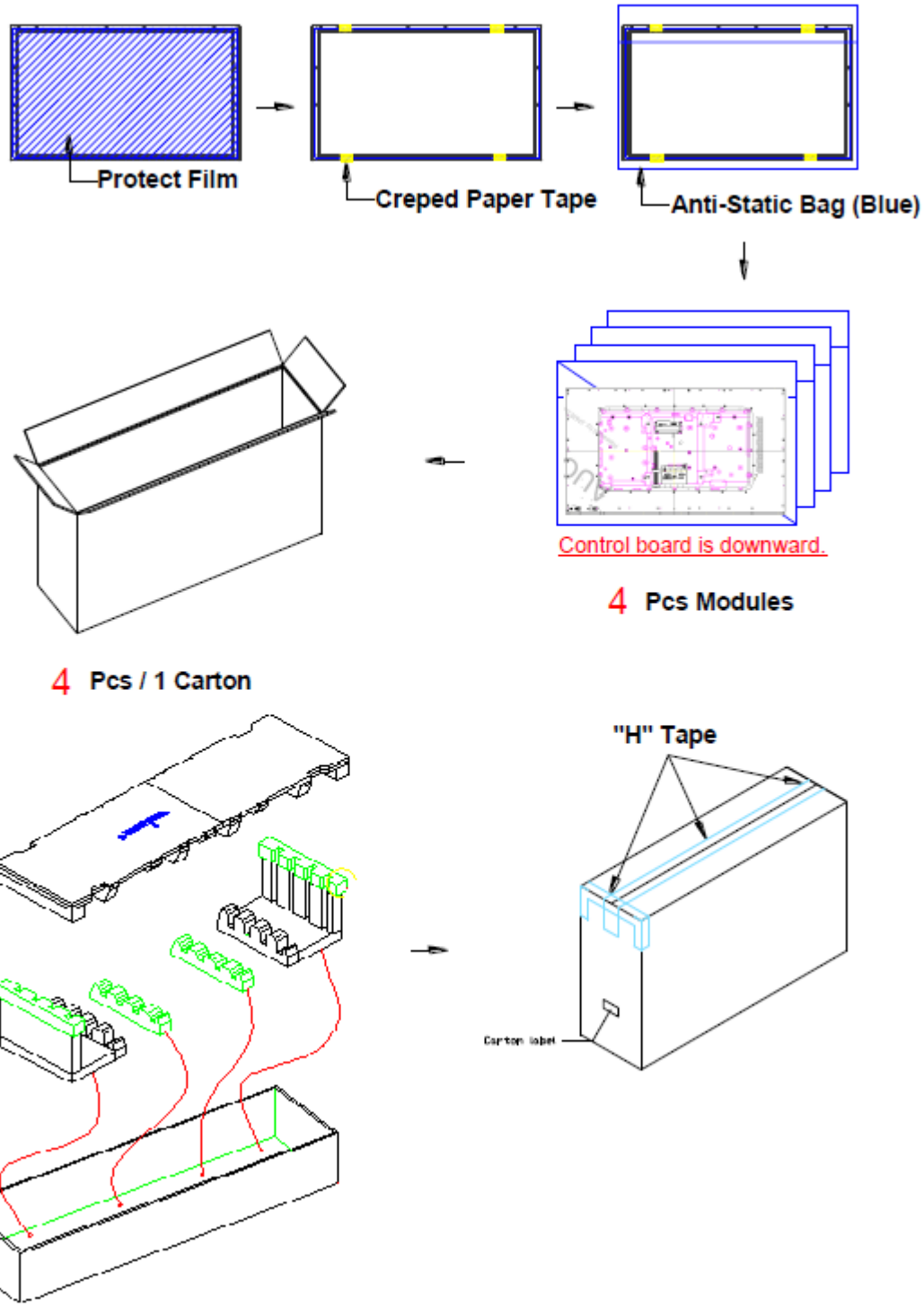
(2) **For RoHs compatible products, AUO will add RoHS for identification.**

Note: The green Mark will be present only when the green documents have been ready by AUO internal green team. (definition of green design follows the AUO green design checklist.)

B. Carton Label:

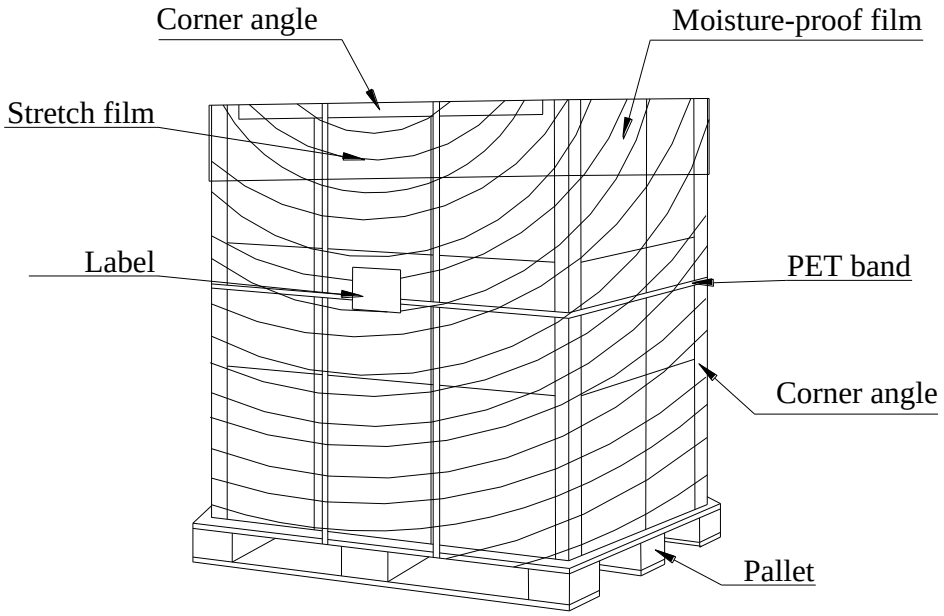


6.2. Packing Methods



6.3. Pallet and Shipment Information

	Item	Specification			Packing Remark
		Qty.	Dimension	Weight (kg)	
1	Packing Box	4 pcs/box	1305(L)*383(W)*800(H)	68.1	
2	Pallet	1	1315(L)*1150(W)*138(H)	17	
3	Boxes per Pallet	3 boxes/pallet			
4	Panels per Pallet	12pcs/pallet			
5	Pallet after packing	12	1315(L)*1150(W)*938(H)	221.3	



7. Precautions

Please pay attention to the followings when you use this TFT LCD module.

7.1. Mounting Precautions

(1) You must mount a module using holes arranged in four corners or four sides.

(2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.

(3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.

(4) You should adopt radiation structure to satisfy the temperature specification.

(5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter cause circuit broken by electro-chemical reaction.

(6) Do not touch, push or rub the exposed polarizer with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)

(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizer. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.

(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.

(9) Do not open the case because inside circuits do not have sufficient strength.

7.2. Operating Precautions

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)

(2) Response time depends on the temperature. (In lower temperature, it becomes longer..)

(3) Brightness depends on the temperature. (In lower temperature, it may become lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.

(4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.

(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.

(6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may

be important to minimize the interface.

7.3. Operating Condition for Public Information Display

The device listed in the product specification is designed and manufactured for PID (Public Information Display) application. To optimize module's lifetime and function, below operating usages are required.

(1) Normal operating condition

A. Operating temperature: 0~40℃

B. Operating humidity: 10~90%

C. Display pattern: dynamic pattern (Real display).

Note) Long-term static display would cause image sticking.

(1) Operation usage to protect against image sticking due to long-term static display.

A. Suitable operating time: 24 hours a day or less.

(* The moving picture can be allowed for 24 hours a day)

B. Liquid Crystal refresh time is required. Cycling display between 5 minutes' information (static) display and 10 seconds' moving image.

C. Periodically change background and character (image) color.

D. Avoid combination of background and character with large different luminance.

(2) Periodically adopt one of the following actions after long time display.

A. Running the screen saver (motion picture or black pattern)

B. Power off the system for a while

(3) LCD system is required to place in well-ventilated environment. Adapting active cooling system is highly recommended.

(4) Product reliability and functions are only guaranteed when the product is used under right operation usages. If product will be used in extreme conditions, such as high temperature/ humidity, display stationary patterns, or long operation time etc…, it is strongly recommended to contact AUO for filed application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at airports, transit stations, banks, stock market and controlling systems.

7.4. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wristband etc. And don't touch interface pin directly.

7.5. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

7.6. Storage

When storing modules as spares for a long time, the following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃and 35℃at normal humidity.

(2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

(3) Storage condition is guaranteed under packing conditions.

(4) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition.

7.7. Handling Precautions for Protection Film

(1) **The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.**

(2) **When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.**

(3) **You can remove the glue easily. When the glue remains on the bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.**

