

SPECIFICATION FOR APPROVAL

() Preliminary Specification

(•) Final Specification

Title

55.0" WUXGA TFT LCD

BUYER	GENERAL
MODEL	

SUPPLIER	LG DISPLAY Co., Ltd.
*MODEL	LD550DUN
SUFFIX	TMA4(RoHS Verified)

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Please return 1 copy for your c your signature and con		PD Product Design LG Display Co., L	



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RECORD OF REVISIONS

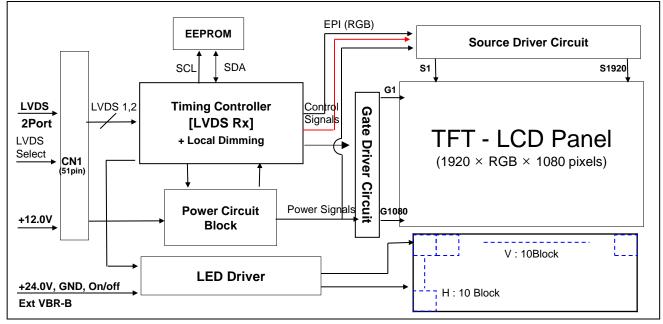
Revision No.	Revision Date	Page	Description
1.0	Nov. 26. 2018	-	Final CAS

1. General Description

The LD550DUN is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) Local Block backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 54.64 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 10-bit gray scale signal for each dot. Therefore, it can present a palette of more than 1.07Bilion colors.

It has been designed to apply the 10-bit 2-port LVDS interface.

It is intended to support commercial Display where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	54.64 inches(1387.83mm) diagonal
Outline Dimension	1210.51 x 681.22 x 53.3(D)
Pixel Pitch	0.630(H) X 0.630(V)
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	10Bit (D), 1.07 Billion colors
Luminance, White	500 cd/m2 (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Power Consumption	Total 203.6W (Typ.) [Logic=6.6W, Backlight=197.0W (@EXTVBR-B = 100%)]
Weight	12,500g (Typ.)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard Coating(2H), Anti-glare treatment of the front polarizer (Haze 28%)
Possible Display Type	Landscape and Portrait Enabled



2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

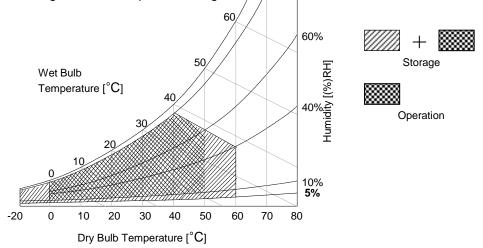
Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value		Unit	Note
		Symbol	Min	Max	Unit	Note
Dower Input Voltage	LCD Circuit	VLCD	-0.3	+14.0	VDC	
Power Input Voltage	Driver	VBL	-0.3	+ 27.0	Vdc	
	ON/OFF	VON/OFF	-0.3	+3.9	Vdc	1
Driver Control Voltage	Brightness	EXTVBR-B	-0.3	+3.9	VDC	1
	Status	Status	-0.3	+3.9	Vdc	
T-Con Option Selection	Voltage	VLOGIC	-0.3	+4.0	VDC	
Operating Temperature	Operating Temperature		0	+50	°C	2.2
Storage Temperature		Тѕт	-20	+60	°C	2,3
Panel Front Temperature		Tsur	-	+68	°C	4
Operating Ambient Humidity		Нор	10	90	%RH	0.0
Storage Humidity		Нѕт	5	90	%RH	2,3

Note.

1. Ambient temperature condition (Ta = 25 ± 2 °C)

- 2. Temperature and relative humidity range are shown in the figure below.
 - Wet bulb temperature should be Max 39°C, and no condensation of water.
- 3. Gravity mura can be guaranteed below 40°C condition.
- 4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may be degraded in case of improper thermal management in final product design. <u>90%</u>



3. Electrical Specifications

3-1. Electrical Characteristics

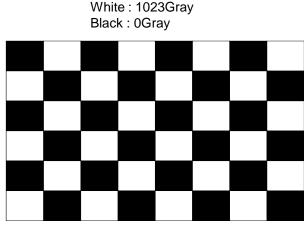
It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the LED backlight and LED Driver circuit.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter		Symbol		Value	Unit	Note	
		Symbol	Min	Тур	Max	Onit	Note
Circuit :							
Power Input Voltage		VLCD	10.8	12.0	13.2	Vdc	
		ILCD	-	550	715	mA	1
	Power Input Current		-	680	884	mA	2
T-CON Option	Input High Voltage	V _{IH}	2.7	-	3.6	VDC	
Selection Voltage	Input Low Voltage	V _{IL}	0	-	0.7	VDC	
Power Consumption		PLCD	-	6.6	8.58	Watt	1
Rush current		Irush	-	-	6	A	3

Notes: 1. The specified current and power consumption are under the V_{LCD}=12.0V, $25 \pm 2^{\circ}$ C, f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.

- 2. The current is specified at maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).
- 4. Ripple voltage level is recommended under \pm 5% of typical voltage



Mosaic Pattern(8 x 6)

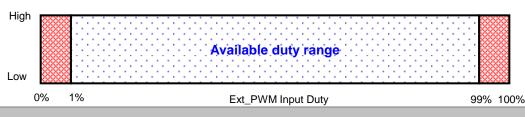


Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		Current of		Values			Notes	
Parameter			Symbol	Min	Тур	Max	Unit	notes
LED Driver :								
Power Supply Inpu	ut Voltage		VBL	21.6	24.0	26.4	Vdc	1
Power Supply Inpu	ut Current		IBL	-	8.2	9.6	A	Ext VвR-в = 100%
Power Supply Input Current (In-Rush)		Irush	-	-	12.5	A	VBL = 21.6V Ext VBR-B = 100% 3	
Power Consumption		PBL	-	197.0	230.3	W	Ext VвR-в = 100%	
	On/Off	On	V on	2.5	-	3.6	Vdc	
		Off	V off	-0.3	0.0	0.7	Vdc	
Input Voltage for Control System Signals	Brightness Adjust		ExtVbr-b	1	-	100	%	On Duty, 5
	PWM Frequ	PWM Frequency for NTSC & PAL			100		Hz	2
	NTSC & PA				120		Hz	2
	Pulse Duty	Pulse Duty Level (PWM)		2.5	-	3.6	Vdc	HIGH : on duty
	(PWM)			0.0	-	0.7	Vdc	LOW : off duty
LED :								
Life Time				50,000			Hrs	6

Notes :

- 1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes at $25\pm2^{\circ}$ C. The specified current and power consumption are under the typical supply Input voltage 24Vand VBR (ExtVBR-B: 100%), it is total power consumption.
- 2. LGD recommend that the PWM freq. is synchronized with Two times harmonic of V_sync signal of system. Though PWM frequency is over 120Hz (max 252Hz), function of LED Driver is not affected.
- 3. The duration of rush current is about 200ms. This duration is applied to LED on time.
- 4. Even though inrush current is over the specified value, there is no problem if I²T spec of fuse is satisfied.
- 5. Ext_PWM Signal have to input available duty range. Between 99% and 100% ExtVBR-B duty have to be avoided. (99% < ExtVBR-B < 100%) But ExtVBR-B 0% and 100% is possible.
- 6. The life time is determined as the time at which brightness of the LED is 50% compared to that of initial value at the typical LED current on condition of continuous operating at $25 \pm 2^{\circ}$ C, based on duty 100%.





3-2. Interface Connections

This LCD module employs two kinds of interface connection, a 51-pin connector is used for the module electronics and Master 14-pin and Slave 12-pin connectors are used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1): FI-RE51S-HF, Refer to below table.
- Mating Connector : FI-RE51HL

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	Reverse	'H' = Reverse, 'L' = Forward	27	Bit Select	H' or NC= 10bit(D) , 'L' = 8bit
2	NC	No Connection	28	RE0N	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection	29	RE0P	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection (Reserved for LGD)	30	RE1N	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection (Reserved for LGD)	31	RE1P	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection (Reserved for LGD)	32	RE2N	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' =JEIDA , 'L' = VESA	33	RE2P	SECOND LVDS Receiver Signal (C+)
8	SCL	LED Driver Defect Detecting	34	GND	Ground
9	SDA	LED Driver Defect Detecting	35	RECLKN	SECOND LVDS Receiver Clock Signal(-)
10	Local Dimming	'H' =Enable only	36	RECLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	RO0N	FIRST LVDS Receiver Signal (A-)	38	RE3N	SECOND LVDS Receiver Signal (D-)
13	RO0P	FIRST LVDS Receiver Signal (A+)	39	RE3P	SECOND LVDS Receiver Signal (D+)
14	RO1N	FIRST LVDS Receiver Signal (B-)	40	RE4N	SECOND LVDS Receiver Signal (E-)
15	RO1P	FIRST LVDS Receiver Signal (B+)	41	RE4P	SECOND LVDS Receiver Signal (E+)
16	RO2N	FIRST LVDS Receiver Signal (C-)	42	NC	No Connection
17	RO2P	FIRST LVDS Receiver Signal (C+)	43	NC	No Connection
18	GND	Ground	44	NC	No Connection
19	ROCLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	ROCLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	GND	Ground
22	RO3N	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	RO3P	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	RO4N	FIRST LVDS Receiver Signal (E-)	50	VLCD	Power Supply +12.0V
25	RO4P	FIRST LVDS Receiver Signal (E+)	51	VLCD	Power Supply +12.0V
26	NC	No Connection	-	-	-

Notes:

- 1. All GND(ground) pins should be connected together to the LCD module's metal frame.
- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the EIA 644 Standard.
- 4. Specific pins(pin No. **#2~#6**) are used for internal data process of the LCD module. These pins should be no connection.
- 5. It may be happened to Abnormal Display during the system interface signal is not
- 6. If Specific pin No. #7, #10, #27 is "NC", LCD Module may be happened to Abnormal Display
- * Even when you use the reverse function, LCM can't be installed Reversed
- 7. Specially added pins #8 & #9, for LED Driver Defect Detecting (Appendix VIII)



3-2-2. Backlight Module

Master

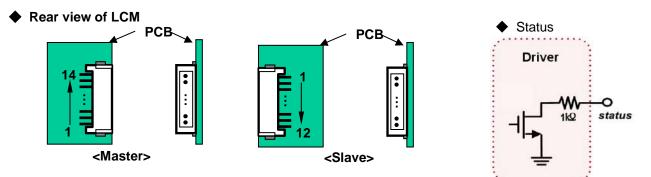
- -LED Driver Connector
- : 20022WR-14B2(Yeonho) / 20022WR-12B2(Yeonho)
- Mating Connector
- : 20022HS-14 / 20022HS-12

Table 5. LED DRIVER CONNECTOR PIN CONFIGULATION

Pin No	Symbol	Description	14PIN	12PIN	Note
1	VBL	Power Supply +24.0V	VBL	VBL	
2	VBL	Power Supply +24.0V	VBL	VBL	
3	VBL	Power Supply +24.0V	VBL	VBL	
4	VBL	Power Supply +24.0V	VBL	VBL	
5	VBL	Power Supply +24.0V	VBL	VBL	
6	GND	Backlight Ground	GND	GND	
7	GND	Backlight Ground	GND	GND	
8	GND	Backlight Ground	GND	GND	1
9	GND	Backlight Ground	GND	GND	
10	GND	Backlight Ground	GND	GND	
11	Status	Status	Status	NC	2
12	VON/OFF	Backlight ON/OFF control	VON/OFF	NC	
13	EXTVBR-B	External PWM	EXTVBR-B	-	3
14	NC	No connection	NC	-	1

Notes :1. GND should be connected to the LCD module's metal frame.

- 2. Normal : Low (under 0.7V) / Abnormal :Open
- 3. High : on duty / Low : off duty, Pin#13 can be opened. (if Pin #13 is open , EXTVBR-B is 100%)
- 4. Each impedance of pin #12 and #13 is over 50 $[\mbox{K}\Omega]$.





3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

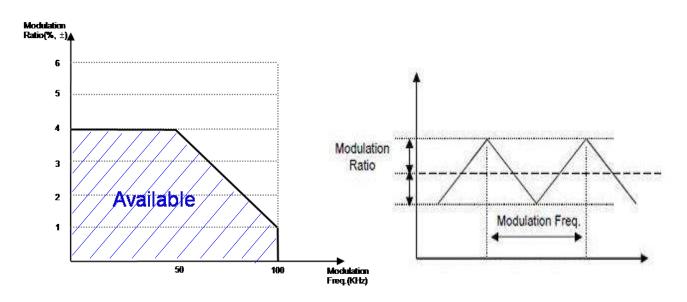
	Table 6.	TIMING TABL	E for NTSC &	A PAL (DE Onl	v Mode)
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ITE	м	Symbol	Min	Тур	Мах	Unit	notes
	Display Period		960	960	960	tCLK	1920 / 2
Horizontal	Blank	tнв	100	140	240	tCLK	1
	Total	tHP	1060	1100	1200	tCLK	
	Display Period	tvv	1080	1080	1080	Lines	
Vertical	Blank	tvв	20	45	300	Lines	1
	Total	tvp	1100	1125	1380	Lines	

ITE	М	Symbol	Min	Тур	Max	Unit	notes
	DCLK	fclk	63.00	74.25	78.00	MHz	
Frequency	Horizontal	fн	57.3	67.5	70	KHz	2
	Vertical	f∨	47	60	63	Hz	2

- Note: 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode). If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.
 - 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency
 - Spread Spectrum Rate (SSR) for 50KHz ~ 100kHz Modulation Frequency(FMOD) is calculated by (7 – 0.06*Fmod), where Modulation Frequency (FMOD) unit is KHz.
 LVDS Receiver Spread spectrum Clock is defined as below figure
 - * Timing should be set based on clock frequency.





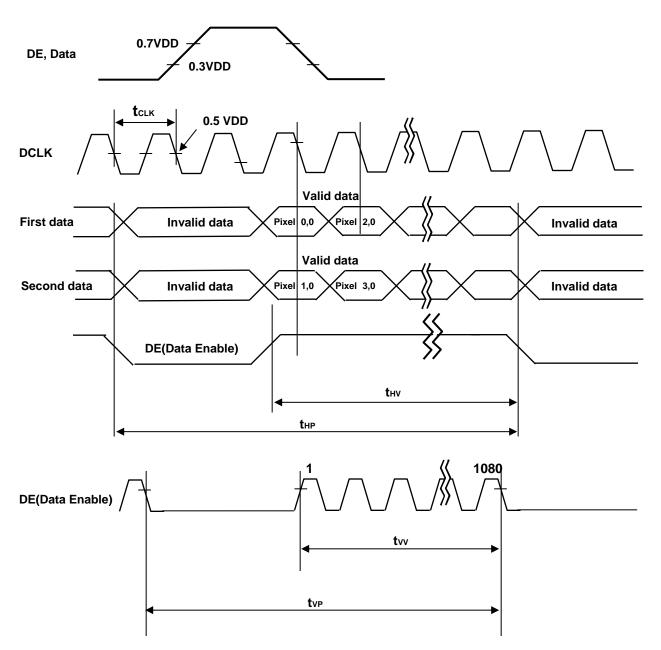
* Please pay attention to the followings when you set Spread Spectrum Rate(SSR) and Modulation Frequency(FMOD)

- 1. Please set proper Spread Spectrum Rate(SSR) and Modulation Frequency (FMOD) of TV system LVDS output.
- Please check FOS after you set Spread Spectrum Rate(SSR) and Modulation Frequency(FMOD) to avoid abnormal display. Especially, harmonic noise can appear when you use Spread Spectrum under FMOD 30 KHz.



3-4. LVDS Signal Specification

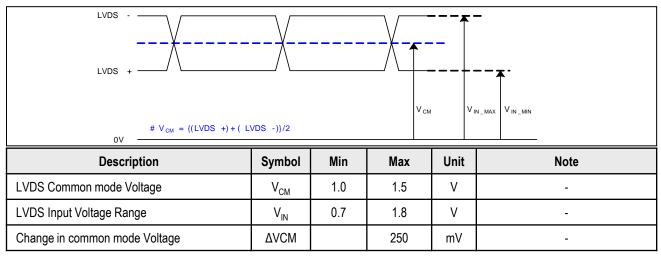
3-4-1. LVDS Input Signal Timing Diagram



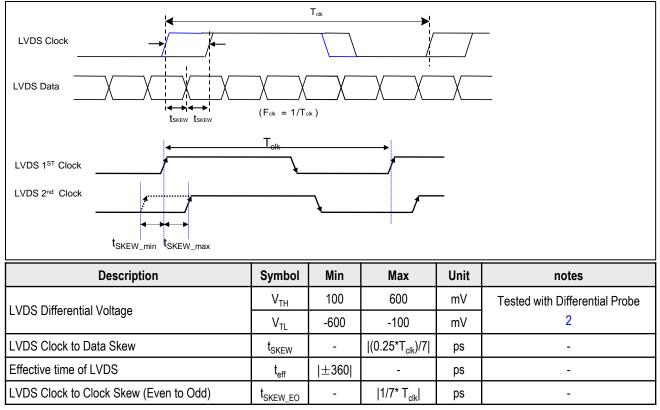


3-4-2. LVDS Input Signal Characteristics

1) DC Specification



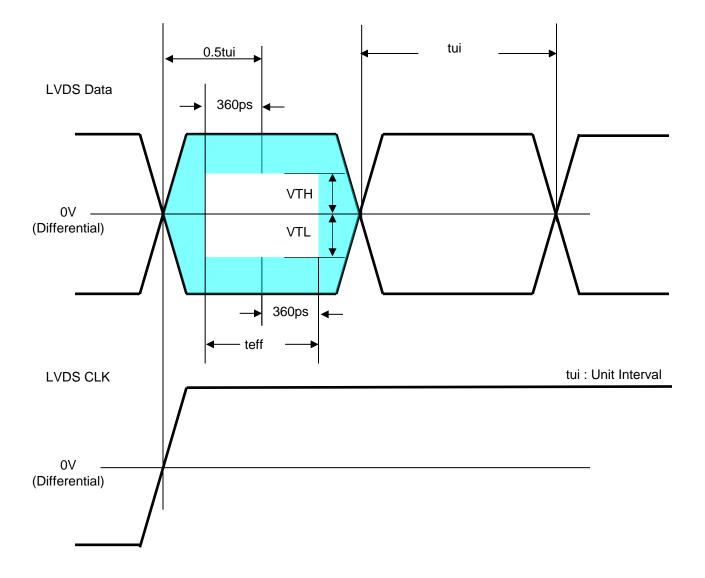
2) AC Specification



Note 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

2. LVDS Differential Voltage is defined within $\mathbf{t}_{\rm eff}$





* This accumulated waveform is tested with differential probe



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Product Specification

3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 10-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 8 provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

														Inp	out	Со	lor	Da	ita												
Co	olor					RE	Ð									GR	EEN									BL	UE				
		MSB							L	SB		MS	В							L	SB	MSE	3							LS	В
	1	R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	GO	B9	B8	B7	B6	B5	B4	B3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0		0	0	0	0	0		0	0	0		0	0	0	.0 	.0	0	.0 	.0 	.0	0
	Red (1023)	1	1	1 •••		1	1		1 	1 	1 	0		. 0					0			0	0	0	.0	.0	.0	.0	.0	.0	0
	Green(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1 	1 	. 1	1 	1 	. 1	1 	. 1	0	0	0	.0	0	.0	.0	.0	0	0
Basic	Blue (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (0001)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED			•••	•••	• • • •			••••		• • •	••••				•••	••••	•••• • •	• • •	• • •	• • •			•••	•••		•••	•••	•••	•••	•••	••••
	RED (1022)	1	1	1	1	1	1	1	1	1	0	0			0		 0	0	 0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (1023)	1	1	1	1	1	1	1	1	1	1	0			0	0	 0	 0	 0	 0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(0001)	0	0	 0	0	0	0	 0	 0	 0	 0	0	 0				 0	 0	 0	 0	1	· · · · 0	0	 0	 0	 0	 0	 0	 0	 0	 0
GREEN			•••	•••	••••			••••	•••	•••	• • • •		•••	•••	•••	••••	····	•••	•••	•••	• • • •		•••	•••	•••	•••	•••	•••	•••	•••	•••
	GREEN(1022)		 0	 0	0	0	0	0	 0	 0	 0	1			1		 1	 1	1	 1	 0	 0	0	0	 0	 0	 0	 0	 0	 0	 0
	GREEN(1023)	 0	 0	 0	 0	0	 0	 0	 0	 0	 0	 1	 1	 1	 1		 1	 1	 1	 1	 1	· · · · 0	0	 0	 0	 0	 0	 0	 0	 0	 0
	BLUE (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (0001)		 0	•••	 0	 0	 0	 0	 0	• • •	 0		 0	•••	• • •		 0	 0	 0	 0	• • • •	· · · ·	0			 0		 0	 0	 0	 1
BLUE			···	•••	••••							̈.	••••		•••			•••		•••		ŀ			·	·		···			
DLUE			••••	•••	••••				•••	•••		 	•••	•••	•••	· · · · •	•••					·					ст. 		 4		
	BLUE (1022)		0	•••	••••		0	0	0		0	•••	•••	• • •	0	•••	0	0	0	0			1							<u>.</u>	
	BLUE (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1



3-6. Power Sequence

3-6-1. LCD Driving circuit

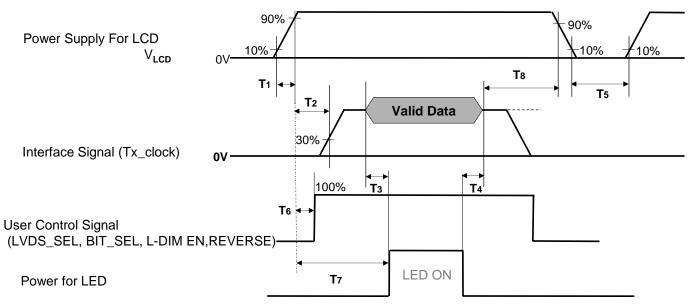


Table 8. POWER SEQUENCE

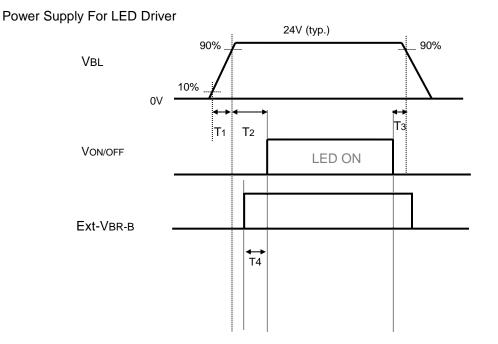
Deremeter		Unit	Netes		
Parameter	Min	Max	Unit	Notes	
T1	0.5	-	20	ms	1
T2	0	-	-	ms	2
T3	200	-	-	ms	3
T4	200	-	-	ms	3
T5	1.0	-	-	s	4
T6	0	-	T2	ms	5
T7	0.5	-	-	s	6
Т8	100	-	-	ms	7

notes: 1. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied.

- 2. If T2 is satisfied with specification after removing LVDS Cable, there is no problem.
- 3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
- 4. T5 should be measured after the Module has been fully discharged between power off and on period.
- 5. If the on time of signals (Interface signal and user control signals) precedes the on time of Power (V_{LCD}), it will be been and a been read a been will be been and the been and t
- it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.
- 6. If there is no abnormal display, no problem.
- 7. It is recommendation specification that T8 has to be 100ms as a minimum value.
- * Please avoid floating state of interface signal at invalid period.
- * When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.



3-6-2. Sequence for LED Driver



3-6-3. Deep condition for LED Driver

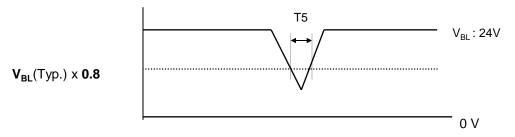


Table 9. Power Sequence for LED DRIVER

Deremeter		Values		Linita	Domoriko
Parameter	Min	Тур	Max	Units	Remarks
T1	20	-	-	ms	1
T2	500	-	-	ms	
Т3	10	-	-	ms	
T4	0	-	-	ms	
T5	-	-	10	ms	V _{BL} (Тур) x 0.8

Notes : 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time. Even though T1 is over the specified value, there is no problem if I²T spec of fuse is satisfied.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25\pm2^{\circ}$ C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °.

FIG. 1 shows additional information concerning the measurement equipment and method.

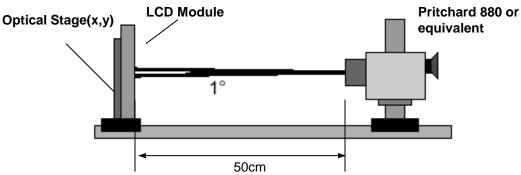


FIG. 1 Optical Characteristic Measurement Equipment and Method

Parar Contrast Ratio	neter	Symbol					
Contrast Ratio			Min	Тур	Max	Unit	Note
		CR	800	1100	-		1
Dynamic Contras	Ratio	DCR	20000	30000	-		8
Surface Luminand	ce, white	L _{WH}	400	500	-	cd/m ²	2
Luminance Variat	ion	δ _{WHITE} 13P	80	90	-		3
Response Time	Gray-to-Gray	G to G	-	8	12	ms	4
	RED	Rx		0.647			
	RED	Ry		0.331]		
	GREEN	Gx	Тур	0.313	Тур		
Color Coordinates	GREEN	Gy	-0.03	0.609	+0.03		
[CIE1931]	BLUE	Bx		0.154			
	BLUE	Ву		0.056			
	WHITE	Wx	Тур	0.279	Тур		
	VVIIIE	Wy	-0.02	0.292	+0.02		
Color Temperature	e			10,000		К	
Color Gamut				72		%	
Half Luminance	Angle						
Horiz	zontal	Θr,I	45	-	-	degree	5
Verti	cal	Θu,d	45	-	-	uegree	5
Viewing Angle (C	R>10)						
x ax	is, right(∳=0°)	θr	89	-	-		
x ax	is, left (φ=180°)	θΙ	89	-	-	degree	6
y ax	is, up (∳=90°)	θυ	89	-	-	uegree	0
y ax	is, down (φ=270°)	θd	89	-	-		
Gray Scale			-	-	-		7

Table 10. OPTICAL CHARACTERISTICS

Ta= 25±2°C, V_{LCD}=12.0V, fv=60Hz, Dclk=74.25MHz, Ext VBR-B=100%



- Notes :1. Contrast Ratio(CR) is defined mathematically as :
 - CR(Contrast Ratio) = Maximum CRn (n=1, 2, 3, 4, 5)
 - CRn = Surface Luminance at position n with all white pixels
 - Surface Luminance at position n with all black pixels
 - n = the Position number(1, 2, 3, 4, 5). For more information, see FIG 2.
 - 2. Surface luminance are determined after the unit has been 'ON' and 60min after lighting the backlight in a dark environment at $25\pm2^{\circ}$ C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
 - 3. δ WHITE(13P) = Minimum(Lon1,Lon2, Lon3,..., Lon13) / Maximum(Lon1,Lon2, Lon3,..., Lon13)*100(%)

Where Lon1 to Lon9 are the luminance with all pixels displaying white at 9 locations . For more information, see the FIG. 2-2.

- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4. Half Luminance Angle is the angle over the middle brightness 50%
- 6. Half Luminance Angle is a angle of half luminance compare to center luminance at 255 gray. For more information, see the FIG. 4
- 7. Gray scale specification
- Gamma Value is approximately 2.2. For more information, see the Table 11.
- 8. Dynamic Contrast Ratio(DCR) is defined mathematically as : Lw / Lk
 - -. Lw : White Center Pattern
 - -. Lk : Black Center Pattern

Use the corner-box pattern for local dimming measurements , For more information, see the FIG. 5

Table 11. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ.)
LO	0.07
L63	0.27
L127	1.04
L191	2.49
L255	4.68
L319	7.66
L383	11.5
L447	16.1
L511	21.6
L575	28.1
L639	35.4
L703	43.7
L767	53.0
L831	63.2
L895	74.5
L959	86.7
L1023	100



Measuring point for surface luminance & measuring point for luminance variation.

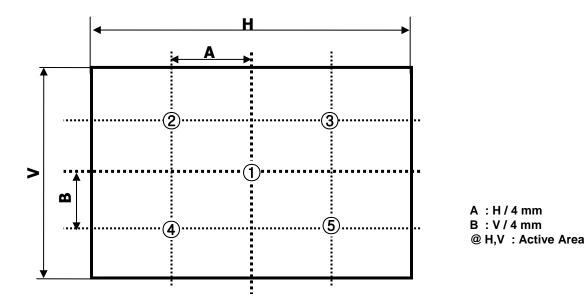


FIG. 2 5 Points for Luminance Measure

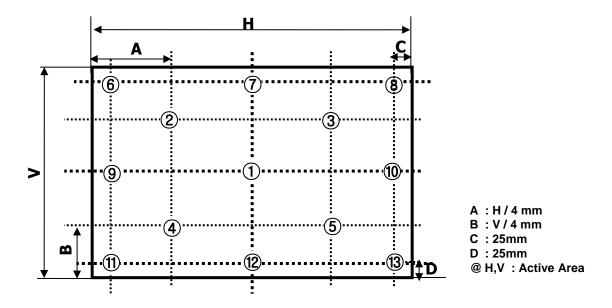
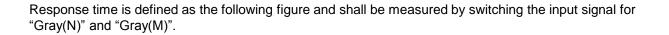
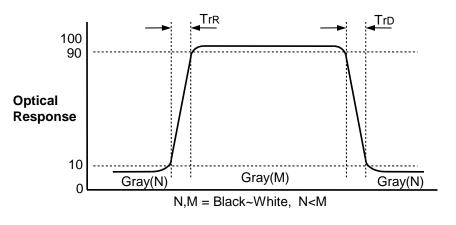


FIG. 2-2 13 Points for Luminance Measure









Dimension of viewing angle range

Half Luminance Angle : The angle of 50% luminance compare to normal direction luminance

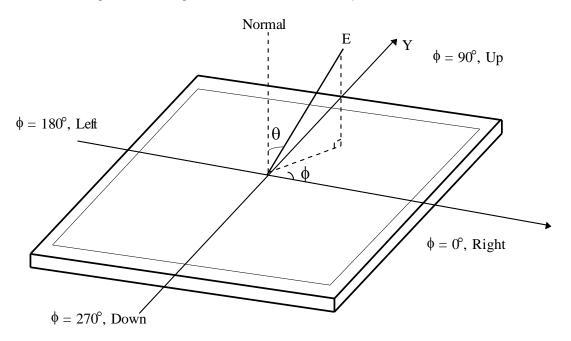


FIG. 4 Viewing Angle

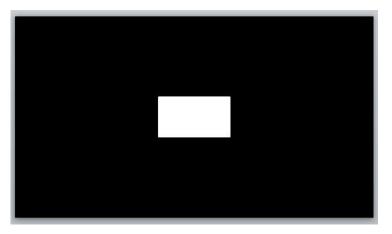


Use the corner-box pattern for local dimming measurements.

We measure the center contrast of a 1/5 size white box compared to four 1/10 size boxes placed in each corner.

Measure the luminance Lw of the white-center pattern. Measure the luminance Lk of the black-center pattern.

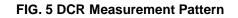
Calculate DCR = Lw / Lk.



White center measurement is made on 20% linear box ; Lw (1/5 size of H and V of display)



Blaxk center measurement is made with 10% linear boxes in corners ; Lk (1/10 size of H and V of display)





5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

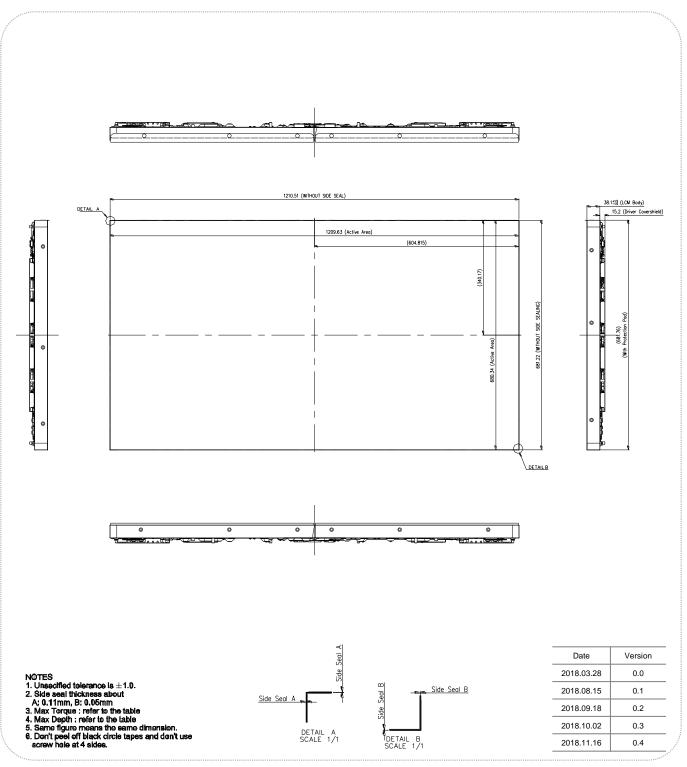
Table 12. MECHANICAL CHARACTERISTICS

Item		Value		
	Horizontal	1210.51 mm		
Outline Dimension	Vertical	681.22 mm		
	Depth	53.3 mm		
Active Dieplay Area	Horizontal	1209.63 mm		
Active Display Area	Vertical	680.34 mm		
Weight	12.5kg (Ty	/p.), 12.9kg (Max.)		

Note : Please refer to a mechanic drawing in terms of tolerance at the next page.

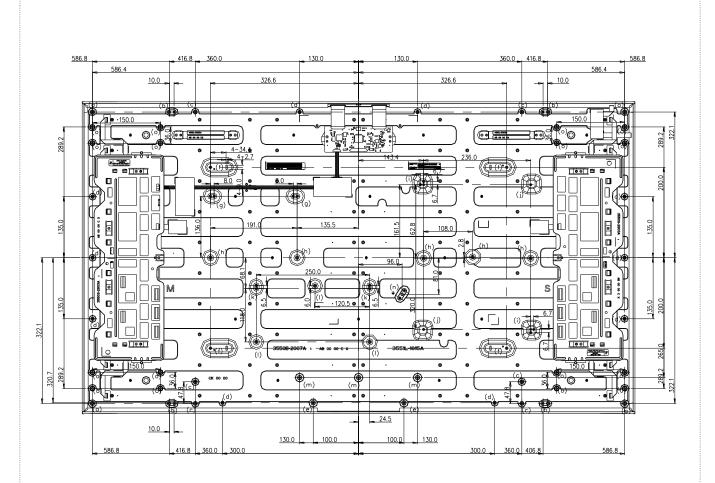


[FRONT VIEW]





<REAR VIEW>



ltern	tap	Height from Body	Depth	torque	note
(a)	M 4.0	4.0mm	Max 3.7	Max 10kgf.cm	Emboes
(b)	M 4.0	4.0mm	Max 3.7	Max 10kgf.cm	Emboes
(c)	M 4.0	8.5mm	Max 5.5	Max 10kgf.cm	Pernnut
(d)	M 3.0	15.0mm	Max 7.0	Max 6kgf.cm	Pemnut
(e)	M 3.0	4.5mm	Max 5.4	Max 6kgf.cm	Emboes
(f)	M 4.0	10.3mm	Max 9.5	Max 10kgf.cm	Emboes
(g)	M 3.0	11.0mm	Max 10.0	Max 6kgf.cm	Emboes
(h)	M 3.0	11.0mm	Max 10.0	Max 6kgf.cm	Emboes
(i)	M 3.0	11.0mm	Max 10.0	Max 6kgf.cm	Emboes
Ø	M 3.0	11.0mm	Max 10.0	Max 6kgf.cm	Emboes
(k)	M 3.0	9.7mm	Max 9.0	Max 6kgf.cm	Emboes
(1)	M 3.0	9.7mm	Max 9.0	Max 6kgf.cm	Emboes
(m)	M 3.0	4.5mm	Max 5.4	Max 6kgf.cm	Emboes
(n)	M 4.0	6.0mm	Max 5.5	Max 10kgf.cm	Emboes
(0)	M 4.0	8.5mm	Max 5.5	Max 10kgf.cm	Pernnut

Date	Version
2018.03.28	0.0
2018.08.15	0.1
2018.09.18	0.2
2018.10.02	0.3
2018.11.16	0.4



6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C 90%, 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 500h
4	Low temperature operation test	Ta= 0°C 500h
5	Humidity condition Operation	Ta= 40 °C ,90%RH
6	Altitude operating storage / shipment	0 - 16,400 ft 0 - 40,000 ft

Note : Before and after Reliability test, LCM should be operated with normal function.



7. International Standards

7-1. Safety

- a) UL 60950-1, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1 : General Requirements.
 b) CAN/CSA-C22.2 No. 60950-1-07, Canadian Standards Association.
- b) CAN/CSA-C22.2 No. 60950-1-07, Canadian Standards Association. Information Technology Equipment - Safety - Part 1 : General Requirements.
- c) EN 60950-1, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment - Safety - Part 1 : General Requirements.
- d) IEC 60950-1, The International Electrotechnical Commission (IEC). Information Technology Equipment - Safety - Part 1 : General Requirements

7-2. Environment

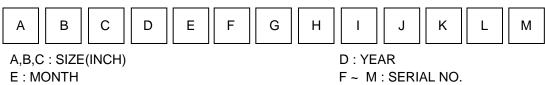
a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011



8. Packing

8-1. Information of LCM Label

a) Lot Mark



Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	А	В	С	D	Е	F	G	Н	J	К

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one box : 10 pcs
- b) Pallet Assy Size : 1440mm(W) X 1140mm(D) X 950mm(H)



9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : V=±200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change.Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it can causes conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) The conductive material and signal cables are kept away from LED driver inductor to prevent abnormal display, sound noise and temperature rising.



9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

9-7. Appropriate Condition for commercial Display

- Generally large-sized LCD modules are designed for consumer applications (TV).

Accordingly, a long-term display like in commercial Display (PD) application, can cause uneven display including image sticking. To optimize module's lifetime and function, several operating usages are required.

- 1. Normal operating condition
 - Temperature: 0 ~ 40 °C
 - Operating Ambient Humidity : 10 ~ 90 %
 - Display pattern: dynamic pattern (Real display)
 - Note) Long-term static display can cause image sticking.
 - -. Be careful for expose the module to sunlight. It causes degradation of polarizer and color filter.
- 2. Operating usages under abnormal condition
- a. Ambient condition
 - Well-ventilated place is recommended to set up PD system.
- b. Power and screen save
- Periodical power-off or screen save is needed after long-term display.





- 3. Operating usages to protect against image sticking due to long-term static display
 - a. Suitable operating time: under 24 hours a day.
- b. Static information display recommended to use with moving image.
 - Cycling display between 5 minutes' information(static) display and 10 seconds' moving image.
- c. Background and character (image) color change
 - Use different colors for background and character, respectively.
 - Change colors themselves periodically.
- d. Avoid combination of background and character with large different luminance.
- 1) Abnormal condition just means conditions except normal condition.
- 2) Black image or moving image is strongly recommended as a screen save.
- 4. Lifetime in this spec. is guaranteed only when PD is used according to operating usages.
- 5. Module should be turned clockwise based on front view when used in portrait mode.



APPENDIX-I-1

Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="L or NC")

Host System	or	Compatible				
30 Bit						Timing
RED0	33			FI-RE51	с_UE	Controller
RED1	34				5-111	Controller
RED2	35					
RED3	36		40	12		
RED4	37	TA1-TA1+	39	13	100 Ω ≶	-RO0P
RED5	38	TB1-/TB1+	_38	14	100 Ω ≶	RO1N
RED6			_37	15	10052 >	RO1P
RED7	61	TC1-/TC1+	36	16	100 Ω ≶	RO2N
RED8			35 _33	17		RO2P
RED9	5	TCLK1-		19	100 Ω ≶	ROCLKN
GREEN0	40	TCLK1+	30	20		ROCLKP
GREEN1	41	TD1-/TD1+	29	22	100 Ω ≶	
GREEN2	42		28	23		RO3P
GREEN3		TE1-/TE1+	27	24	100 Ω ≶	RO4N RO4P
GREEN4	45			25		KU4F
GREEN5			24	28		
GREEN6	62	TA2-/TA2+	_23	20	100 Ω ≶	RE0P
GREEN7	63		22	30	100 Ω ≶	RE1N
GREEN8	6	TB2-/TB2+	21	31	$100\Omega \leq$	RE1P
GREEN9	8	TC2-/TC2+	20 19	32	100 Ω ≶	RE2N
BLUE0			19	33	10052 >	RE2P
BLUE1	49	TCLK2-	16	35	100 Ω ≶	RECLKN
BLUE2	50	TCLK2+	14	36		
BLUE3	52	TD2-/TD2+	13	38	100 Ω ≶	RE3N RE3P
BLUE4	53		12	39		RE3P RE4N
BLUE5	54	TE2-/TE2+	11	40	100 Ω ≶	RE4P
BLUE6	64			41		
BLUE7	1			- 7		VESA / JEIDA
BLUE8	9					
BLUE9	11					
Hsync	55					
Vsync	57					
Data Enable	58					
CLOCK	12		GND		LCM Modul	le

- Note: 1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input.
 - 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
 - 3. '9' means MSB and '0' means LSB at R,G,B pixel data.



APPENDIX-I-2

Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="H")

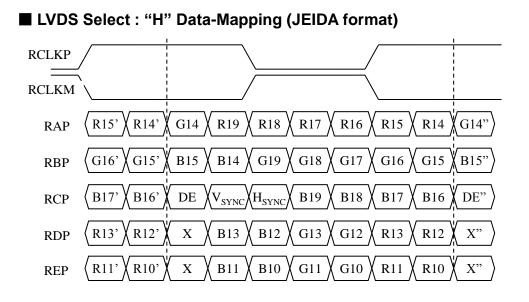
Host System 30 Bit		C63LVD103 Compatible				
RED0	4		FL	RE51S		Timing Controller
RED1	5		1 1-		-111	oontroller
RED2	59					
RED3	61		40	12	~ ~	- ROON
RED4	 33	TA1-TA1+	39	13	100 Ω ≶	- ROOP
RED5	 34		_38	14	4000 <	RO1N
RED6	35	TB1-/TB1+	_37	15	100 Ω ≶	- RO1P
RED7	36	TC1-/TC1+	_36	16 17	100 Ω ≶	RO2N
RED8	37		35	17	10032 >	RO2P
RED9	38	TCLK1-	<u>33</u> <u>32</u>	20	100 Ω ≶	ROCLKN
GREEN0	6	TCLK1+		20		- ROCLKP
GREEN1	8	TD1-/TD1+	29	22	100 Ω ≶	RO3N
GREEN2	 62	101/1011	28	24		RO3P
GREEN3	63	TE1-/TE1+	_27	25	100 Ω ≷	RO4N
GREEN4	40					RO4P
GREEN5	41		24	28		REON
GREEN6	42	TA2-/TA2+	23	_ 29	100 Ω ≶	REOP
GREEN7	44		22	30	· · · · · · · · · · · · · · · · · · ·	RE1N
GREEN8	 45	TB2-/TB2+	_21	31	100 Ω ≶	- RE1P
GREEN9	46		20	- 32	400 0 <	RE2N
BLUE0	9	TC2-/TC2+	19	- 33	100 Ω ≶	RE2P
BLUE1	11	TCLK2-	<u> 17 </u>	- 35	100 Ω ≶	RECLKN
BLUE2	64	TCLK2+		- 36	10052 >	RECLKP
BLUE3	1	TD2-/TD2+		- 38	100 Ω ≶	RE3N
BLUE4	48	10271024	12	- 39 - 40		RE3P
BLUE5	49	TE2-/TE2+	11	- 40	100 Ω ≶	RE4N RE4P
BLUE6	50					RE4P
BLUE7	52			- 7		VESA / JEIDA
BLUE8	53					
BLUE9	54					
Hsync	55					
Vsync	57					·
Data Enable	 58					
CLOCK	12		<		LCM Module	
OLOOK	14		VCC			

- Note :1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input.
 - 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
 - 3. '9' means MSB and '0' means LSB at R,G,B pixel data.

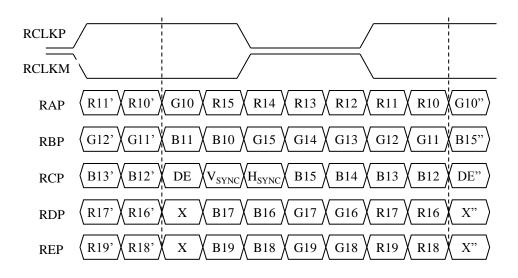


APPENDIX- II

LVDS Data-Mapping info. (10bit)



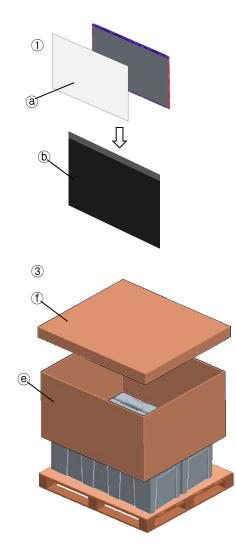
LVDS Select : "L" Data-Mapping (VESA format)

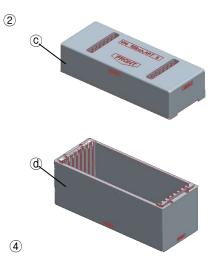


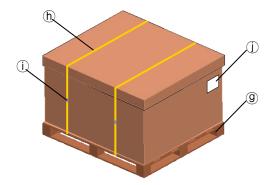


APPENDIX-III

■ Pallet Ass'y







No.	Description	Material		
a	Protect Film	PP+PE		
b	BAG	AL		
C	PackingTop	EPS		
đ	Packing,Bottom	EPS		
e	Angle Packing	Double Wall		
ſ	Angle Cover	Single Wall		
9	Pallet	Plywood		
h	Band	PP		
í	Clip	Steel		
J	Label	Paper		



APPENDIX-IV

LCM Label



Production site
 LG Display (Guangzhou) Co., LTD
 notes 1.The origin of LCM Label will be changed according to the production site.



APPENDIX- V ■ Pallet Label

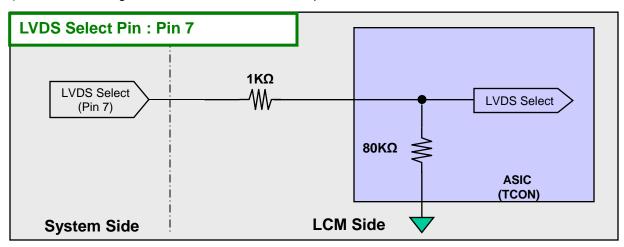




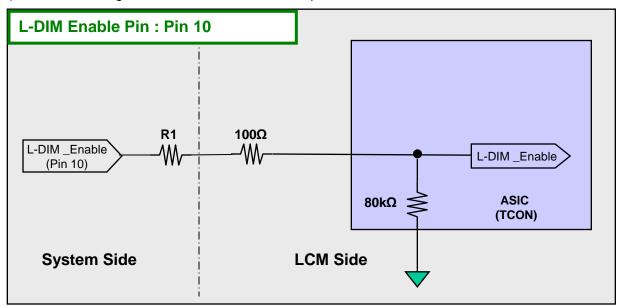
APPENDIX- VI

Option Pin Circuit Block Diagram

1) Circuit Block Diagram of LVDS Format Selection pin



2) Circuit Block Diagram of L-DIM Enable Selection pin

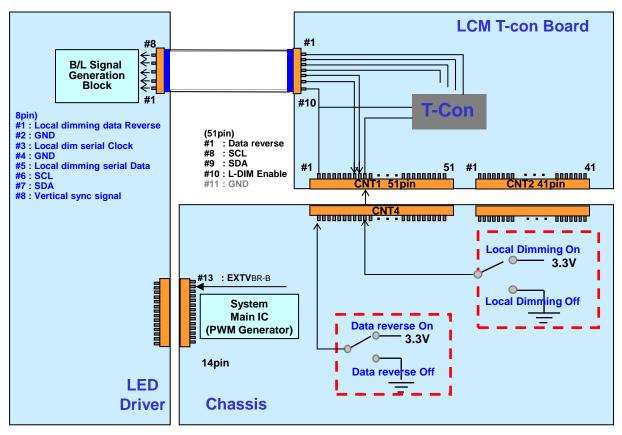




APPENDIX- VII

EXTVBR-B & Local Dimming Design Guide

- 1) When L-Dim Enable is "L", Vertical Sync Signal = System Dimming with 100Hz or 120Hz frequency.
- 2) Local Dimming signals are synchronized with V-Sync Freq. of System in T-Con Board.
- 3) EXTVBR-B Specification (VCC = 3.3V) @ Local Dimming , Reverse Specification
 - a) High Voltage Range : 2.5 V ~ 3.6 V
 - b) Low Voltage Range $: 0.0 \text{ V} \sim 0.7 \text{ V}$



<With Driver Model>

		VCC
EXTVBR-B	100 Hz for PAL	VCC*0.9
Frequency	120 Hz for NTSC	Rising Time
Rising Time	MAX 10.0 μs	Falling Time
Falling Time	MAX 10.0 μs	

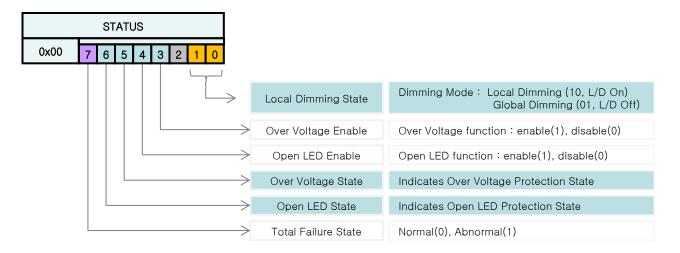


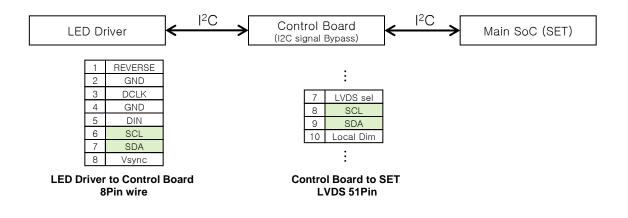
APPENDIX- VIII-1

Defect Detecting Mode (LED Driver)

- 1) It is possible to get defect information by I²C when LCM is defected
- 2) Failure mode that can be detected
 - Open LED, Over Voltage
 - Protection Operation status, Local Dimming Operation status

▶ I²C Data format (2Byte)



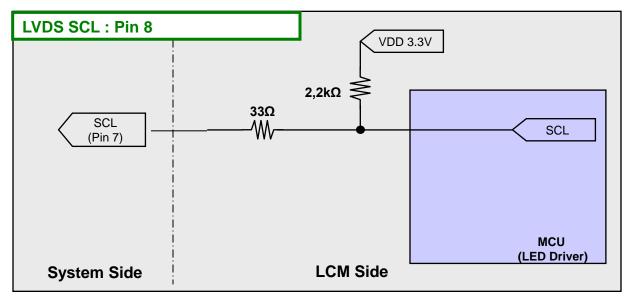




APPENDIX- VIII-2

SCL, SDA Pin Circuit Diagram

1) Circuit Block Diagram of LVDS SCL pin



1) Circuit Block Diagram of LVDS SDA pin

