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DV150X0M-N16 Product Specification Rev.0

FUZHOU BOE OPTOELECTRONICS TECHNOLOGY Co., LTD

SPEC. NUMBER NO. S8-64-8D-332 PRODUCT GROUP TFT-LCD

Rev.0

ISSUE DATE 2023.3.17 PAGE 1 OF 33

DBG-RD-商用器件-2022011-O



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2023.3.17

REVISION HISTORY

()Preliminary specification ($\sqrt{}$)Final specification

Rev.	Page	Description of changes	Date	Prepared
	P21	Correct Mask Parameter	2023.1.9	Fang Xin
	P22	Update Quality Content	2023.3.7	Chen Guangquan
D0	P5	Add note 1	2023.3.7	ALL
PO	P6	Add Note 2	2023.3.7	ALL
	P32/P33	Update APPENDIX	2023.3.7	Li Long
	-	Add Spec.Num	2023.3.14	Chen Xiaoan
	P5	Correct Back-Light Remark	2023.3.16	Li long
	P23	Correct Packing Order Figure	2023.3.16	Li long
	P24	Correct Packing Note	2023.3.16	Li long
0	-	Add Spec.Num (Final)	2023.3.17	Chen Xiaoan
	P22	Update MDL ID Label	2023.6.30	Chen Xiaoan
	P24	Update Box Label	2023.6.30	Chen Xiaoan

SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	2 OF 33
		A 4/040 V 007

DBG-RD-商用器件-2022011-O

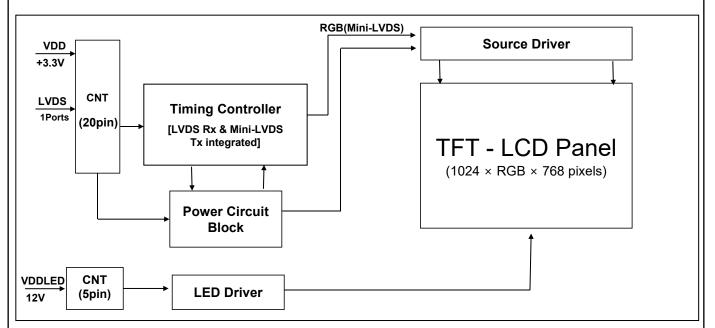
	BOI		PRODUCT GROUP	REV	ISSUE DATE
			Customer SPEC	0	2023.3.17
			Contents		
	No		ITEM		Page
		REVIS	SIONS HISTORY		2
		CONT	ENTS		3
	1	GENE	RAL DESCRIPTION		4
		1.1 Int	roduction		
		1.2 Fea	atures		
		1.3 Ap	plications		
		1.4 Ge	neral Specification		
	2	ABSO	LUTE MAXIMUM RATINGS		6
	3	ELEC	FRICAL SPECIFICATIONS		7
		3.1 TF	T LCD Open Cell		
		3.2 Ba	cklight Unit		
	4	INTEF	FACE CONNECTION		9
		4.1 Op	en Cell Input Signal & Power		
		4.2 LV	DS Interface		
		4.3 LV	DS Rx Interface Timing Parameter		
		4.4 LV	DS Rx Interface Eye Diagram		
		4.5 LV	DS Receiver Differential Input		
	5	SIGNA	AL TIMING SPECIFICATIONS		13
		5.1 Tin	ning Parameters (DE only mode)		
		5.2 Sig	nal Timing Waveform		
		5.3 Inp	out Signals, Basic Display Colors and Gray Scale of	of Colors	
		5.4 Po	wer Sequence		
	6	OPTIC	CAL SPECIFICATIONS		18
	7	MECH	IANICAL CHARACTERISTICS		20
	8	RELIA	BILITY TEST		21
	9	PROD	CUT SERIAL NUMBER		22
	10	PACK	ING INFORMATION		23
	11	PRECA	AUTIONS		25
	12	APPEN	NDIX		30
S	SPEC. NUMI	BER	SPEC. TITLE		PAGE
١	NO. S8-64-8	D-332	DV150X0M-N16 Product Specification		3 OF 33
			00044_0		Δ4(210 X 297

DBG-RD-商用器件-2022011-O

1.0 GENERAL DESCRIPTION

1.1 Introduction

DV150X0M-N16 is a color active matrix TFT LCD MDL using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This MDL has a 15 inch diagonally measured active area with XGA resolutions (1024 horizontal by 768 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.7M colors. The TFT-LCD MDL panel is adapted for a low reflection and higher color type.



1.2 Features

- LVDS interface with 1 pixel / clock
- High-speed response
- Low color shift image quality
- 6-bit +FRC color depth, display 16.7M colors
- Wide viewing angle
- DE (Data Enable) only mode
- ADS technology is applied for high display quality
- RoHS compliant
- 7*24hrs usage support

SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	4 OF 33
		A4(210 V 207)

DBG-RD-商用器件-2022011-O

BOE	PRODUCT GROUP	REV	ISSUE DATE
	Customer SPEC	0	2023.3.17
1.3 Application			

- Display Terminals for Control System
- Landscape and Portrait Display
- Mainly Applies to Game Consoles

1.4 General Specification

< Table 1. General Specifications >

Parameter	Specification	Unit	Remarks
Active area	304.128(H) × 228.096(V)	mm	
Number of pixels	1024(H) ×768(V)	pixels	
Pixel pitch	0.297(H) ×0.297(V)	mm	
Pixel arrangement	Pixels RGB Vertical stripe		
Display colors	16.7M	colors	6bits+FRC
Display mode	Normally Black		
Dimensional outline	326.5(H) × 253.5(V) × 10.4 (Typ.)	mm	Detail refer to drawing
Weight	730	g	
Power Consumption	13.64	Watt	Тур.
Bezel width (L/R/U/D)	10.19/10.19/11.7/11.7	mm	
Surface Treatment	Haze 25%, 3H		
Back-light	E-LED Light bar Type		54ea LED/BLU
LED life	50000	hrs	
Display Direction	landscape & Portrait		

	-	
SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	5 OF 33
 DBG-RD-商用器件-20	22011-O	A4(210 X 297)

[VSS=GND=0V]

2.0 ABSOLUTE MAXIMUM RATINGS

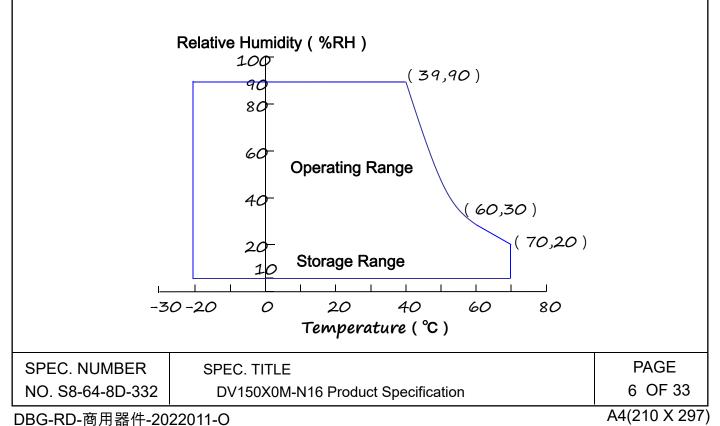
The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

Parameter	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	VDD	-0.3	4	V	Ta = 25 °C
Operating Temperature	T _{OP}	-20	+70	°C	
Storage Temperature	T _{SUR}	-20	+70	°C	Note 1
Storage Temperature	T _{st}	-20	+70	°C	Note 2
Operating Ambient Humidity	Нор	10	90	%RH	
Storage Humidity	Hst	10	90	%RH	

< Table 2. Open Cell Electrical Specifications >

Note 1 : Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water.

Note 2 : When used near the limit conditions of temperature and humidity, the life will be reduced.



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3.0 ELECTRICAL SPECIFICATIONS

3.1 TFT LCD Open Cell

< Table 3. Open Cell Electrical Specifications >

[Ta =25±2 °C]

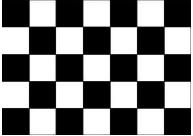
	Parameter			Values		Unit	Remark
	Parameter	Symbol	Min	Тур	Max	Unit	Reillark
Power Supply Input Voltage		VDD	3.0	3.3	3.6	V	Note 1
Power Supp	ly Current	IDD	-	462	810	mA	NOLE I
Power Supp	ly Ripple Voltage	VRP	-	-	300	mV	
Rush Currer	nt	IRUSH	-	2	3	Α	Note 2
	Differential Input High Thr eshold Voltage	VLVTH	-	-	+100	mV	VLVC=1.2V
LVDS Interface	Differential Input Low Threshold Voltage	VLVTL	-100	-	-	mV	VLVC-1.2V
	Common Input Voltage	VLVC	0.7	-	1.6	V	
CMOS	Input High Threshold Voltage	VIH	0.7VDD	-	VDD	V	
Interface	Input Low Threshold Voltage	VIL	0	-	0.3VDD	V	
		PD		1.52	2.92	W	
Power Consumption		PBL		12.12	15.12		Note 3
		Ptotal		13.64	18.04		

Note 1 : The supply voltage is measured and specified at the interface connector of LCM.

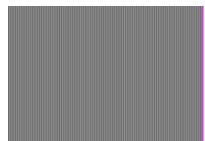
The current draw and power consumption specified is for VDD=3.3V,

Test Pattern of power supply current

a) Typ : Mosaic 7X5 (L0/L255)



b) Max : Vline Subline (L255))



Note 2 : The duration of rush current is about 2ms and rising time of Power Input is 1ms(min) Note 3 : Calculated value for reference (Input pins*VPIN ×IPIN) excluding inverter loss.

SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	7 OF 33
		A 4/040 V 007)

DBG-RD-商用器件-2022011-O



2023.3.17

3.0 ELECTRICAL SPECIFICATIONS

3.2 Backlight Unit

< Table 4. Backlight Unit Electrical Specifications >

[Ta =25±2 °C]

		Min.	Тур.	Max.	Unit	Remarks	
LED Driver P tage	ower Supply Vol	H_{VDD}	10.8	12	12.6	V	
	Power Supply Irrent	I _{HVDD}	900	1010	1200	mA	
EN Control	Backlight on	V_{ENH}	3	3.3	3.6	V	EN logic high vo Itage
Level	Backlight off	V _{ENL}	0	0	0.6	V	EN logic low vol tage
PWM Contr	PWM High Le vel	V_PML	3	3.3	3.6	V	
ol Level	PWM Low Lev el	V _{PML}	0	0	0.6	V	
PWM Control	Frequency	F _{PWM}	0.12	-	20	KHz	Refer to customer comments
Duty Ratio		-	5	-	100	%	
LED Life-Time		N/A	50,000	-	-	Hour	Note4
LED Light Bar Input Voltage P er Input Pin		VPIN	26.1	27.9	29.7	V	
LED Light Bar Input Current Per Input Pin		IPIN	-	65	-	mA	Note2
LED Power Co	onsumption	PBL	-	12.12	15.12	W	Note3

LED bar consists of 54LED packages,6 strings(parallel)*9packages(serial)

Note1: There are one light bar ,and the specified current is input LED chip 100% duty current

Note2: The sense current of each input pin is 65mA

Note3: PBL=6 Input pins*VPIN ×IPIN

Note4: The lifetime is determined as the time at which luminance of LED become 50% of the initial brightness or not normal lighting at IPIN=65mA on condition of continuous operating at 25 ±2 °C

SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	8 OF 33

DBG-RD-商用器件-2022011-O

Customer SPEC

4.0 INTERFACE CONNECTION

4.1 Open Cell Input Signal & Power

- LVDS Connector : MSB240420HEA or Compatible.

Pin No	Symbol	Description	Pin No	Symbol	Description
1	VDD	Power Supply,3.3V(typ.)	11	RIN2-	-LVDS differential data input 2
2	VDD	Power Supply,3.3V(typ.)	12	RIN2+	+LVDS differential data input 2
3	VSS	Ground	13	VSS	Ground
4	NC	No Connection	14	CLKIN-	-LVDS differential clock input CL K
5	RIN0-	-LVDS differential data input 0	15	CLKIN+	+LVDS differential clock input CL K
6	RIN0+	+LVDS differential data input 0	16	VSS	Ground
7	VSS	Ground	17	RIN3-	-LVDS differential data input 3
8	RIN1-	-LVDS differential data input 1	18	RIN3+	+LVDS differential data input 3
9	RIN1+	+LVDS differential data input 1	19	VSS	Ground
10	VSS	Ground	20	NC	No Connection

< Table 4. Open Cell Input Connector Pin Configuration >

SPEC. NUMBER NO. S8-64-8D-332 SPEC. TITLE DV150X0M-N16 Product Specification PAGE 9 OF 33

Β	Ο	Ε

4.2 LED Light Bar

Pin No

1

2

3

4

5

BIST Pattern

2 seconds

Pattern 1

(black)

< Table 5. LED Light Bar>

2 seconds

Pattern 3

(full red)

REV

0

Description

No Connection

PWM Dimming

Ground

12V

3.3V-On / 0V-Off

2 seconds

Pattern 4

(full green)

-LED connector: CI4205M1HR0-NH (瀚荃) or Compatible

Symbol

NC

Dimming

Enable

GND

VCC

Notes : 1. NC(Not Connected) : This pins are only used for BOE internal operations. 2. Input Level of LVDS signal is based on the EIA-644 Standard.

2 seconds

Pattern 2

(white)

2023.3.17

SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	10 OF 33
	00011 0	Δ4(210 Χ 297)

A4(210)

2 seconds

Pattern 5

(full blue)

2023.3.17

4.2 LVDS Interface

- LVDS Receiver : Timing Controller (LVDS Rx merged) / LVDS Data : Pixel Data

< Table 6. Open Cell Input Connector Pin Configuration >

	Input	Trans	mitter	Inter	rface	HT236F01-100 (CN 11)	Remark
	Signal	Pin No.	Pin No.	System (Tx)	TFT-LCD (Rx)	Pin No.	
	OR0	51					
	OR1	52					
	OR2	54			DTL 0 0		
	OR3	55	48 47	OUT0- OUT0+	RXO0- RXO0+	1 2	
	OR4	56	47	0010+	KAO0+	2	
	OR5	3					
	OG0	4	1				
	OG1	6					
	OG2	7	1				
	OG3	11	1				
	OG4	12	46	OUT1-	RXO1- RXO1+	3 4	
	OG5	14	45	OUT1+	KX01+	4	
	OB0	15	1				
	OB1	19	1				
L V	OB2	20					
D	OB3	22	1				
S	OB4	23	1				
	OB5	24	42	OUT2- OUT2+	RXO2- RXO2+	5 6	
	Hsync	27	41				
	Vsync	28					
	DE	30					
	MCLK	31	40 39	CLK OUT- CLK OUT+	RXO CLK- RXO CLK+	8 9	
	OR6	50					
	OR7	2	1				
	OG6	8	1		RXO3-		
	OG7	10	38	OUT3-	RXO3+	10	
	OB6	16	37	OUT3+		11	
	OB7	18	1				
	RSVD	25					
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NO. S8-64-8D-332

SPEC. TITLE DV150X0M-N16 Product Specification PAGE 11 OF 33

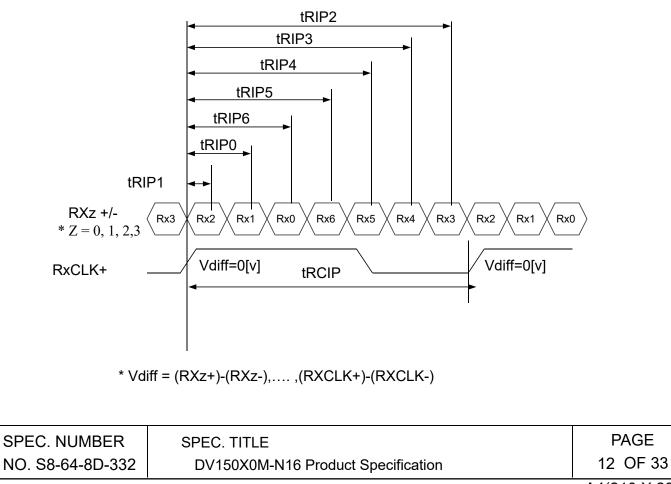
DBG-RD-商用器件-2022011-O

4.3 LVDS Rx Interface Timing Parameter

The specification of the LVDS Rx interface timing parameter is shown in Table 6.

<Table 7. LVDS Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit	Remark
CLKIN Period	tRCIP	10.31	13.47	15.87	nsec	
Input Data 0	tRIP1	0.5 ×tRCIP/7-0.4	0.5 ×tRCIP/7	0.5 ×tRCIP/7 +0.4	nsec	
Input Data 1	tRIP0	1.5 ×tRCIP/7 -0.4	1.5 ×tRCIP/7	1.5 ×tRCIP/7 +0.4	nsec	
Input Data 2	tRIP6	2.5 ×tRCIP/7-0.4	2.5 ×tRCIP/7	2.5 ×tRCIP/7+0.4	nsec	
Input Data 3	tRIP5	3.5 ×tRCIP/7-0.4	3.5 ×tRCIP/7	3.5 ×tRCIP/7+0.4	nsec	
Input Data 4	tRIP4	4.5 ×tRCIP/7-0.4	4.5 ×tRCIP/7	4.5 ×tRCIP/7+0.4	nsec	
Input Data 5	tRIP3	5.5 ×tRCIP/7-0.4	5.5 ×tRCIP/7	5.5 ×tRCIP/7+0.4	nsec	
Input Data 6	tRIP2	6.5 ×tRCIP/7-0.4	6.5 ×tRCIP/7	6.5 ×tRCIP/7+0.4	nsec	



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2023.3.17

5.0 SIGNAL TIMING SPECIFICATION

5.1 Timing Parameters (DE only mode)

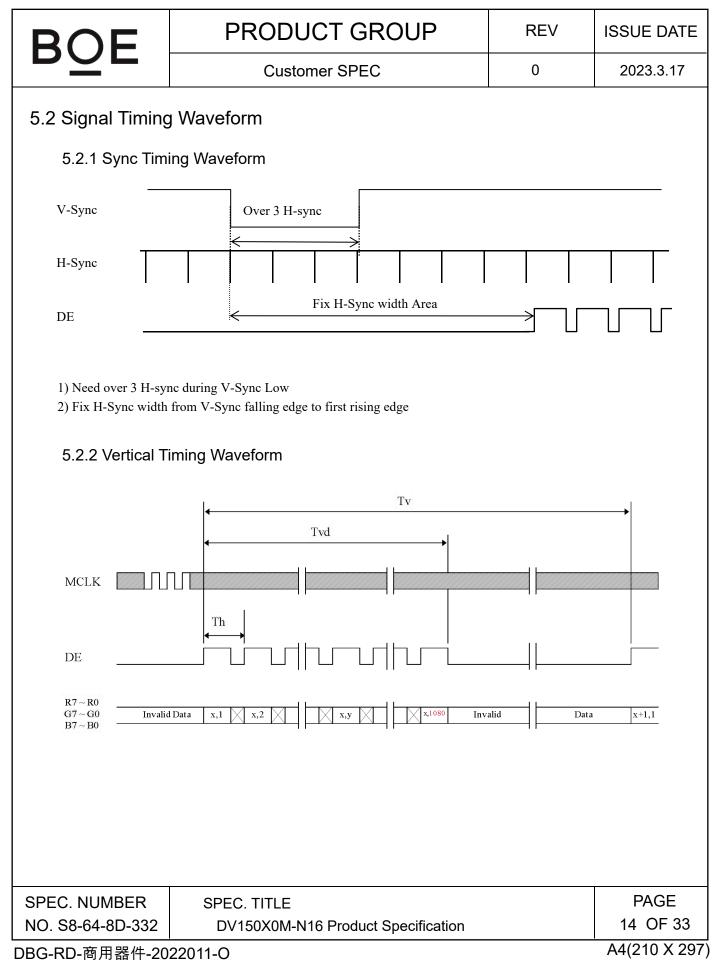
< Table 8. Timing Table >

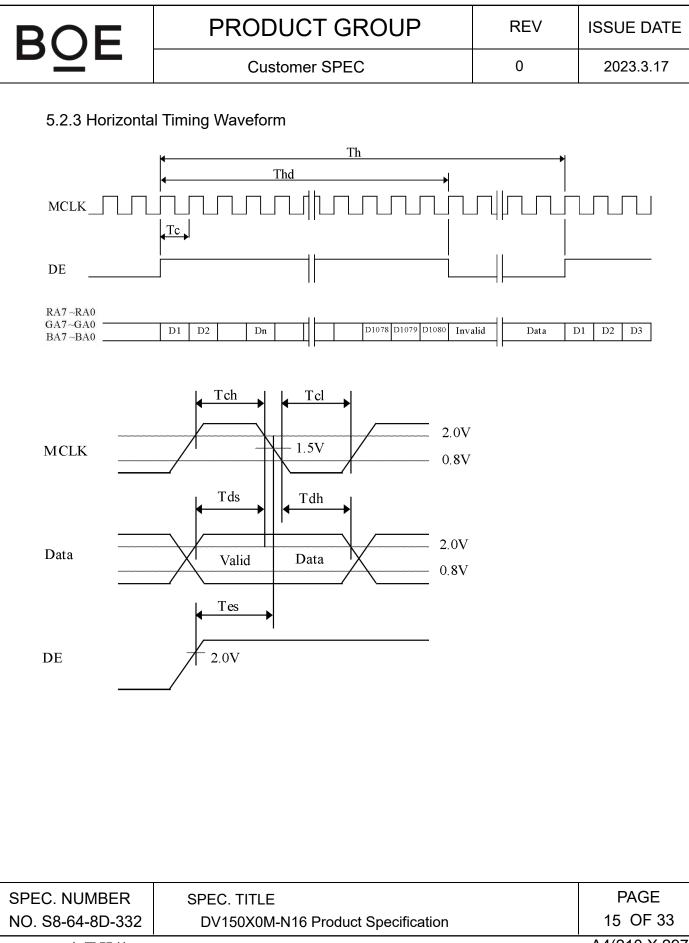
	Item		ols	Min	Тур	Max	Unit
	Frequency	1/To	c	52	58	71	MHz
Clock	High Time	Tch	l	-	4/7Tc	-	
	Low Time	Tcl		-	3/7Tc	-	
]	Frame Period			48	60	63	Hz
Нс	rizontal Active	Valid	t _{HV}	-	1024	-	t _{CLK}
[Display Term		t _{HP}	1200	1344	1400	t _{CLK}
Vertical Active		Valid	t _{vv}	-	768	-	t _{HP}
[Display Term	Total	t _{VP}	788	806	845	t _{HP}

Notes: This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal o peration. < Table 9. LVDS Input SSCG>

Symbol		Parameter	Condition	Min	Тур	Max	Unit
F	LVDS In	put frequency	-	25	-	100	MHz
T _{LVSK}	LVDS cf	nannel to channel skew	F=58MHz V _{IC} =1.2V V _{ID} =±200mV	-600	-	+600	ps
F _{LVMOD}	Modulati during S	ng frequency of input clock SC	F=58MHz	10	-	300	KHz
F _{LVDEV}		n deviation of input clock fr during SSC	V _{IC} =1.2V V _{ID} =±200mV	-3	-	+3	%
T _{CY-CY}	- _{CY} Cycle to Cycle jitter			-	-	200	ps
SPEC. NUI NO. S8-64-		SPEC. TITLE DV150X0M-N16 Proc	luct Specification		PAGE 3 OF 33		

DBG-RD-商用器件-2022011-O





DBG-RD-商用器件-2022011-O

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2023.3.17

5.3 Input Signals, Basic Display Colors and Gray Scale of Colors

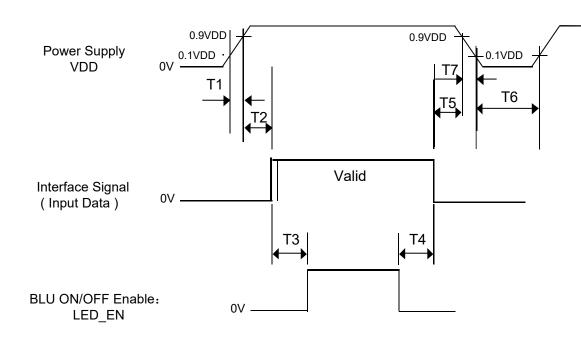
< Table 10. Input Signal and Display Color Table >

Color & Gray Scale										Inp	ut	Dat	ta S	Sig	nal										
Color & G	ray Scale			R	ed	Da	ta					Gr	eer	ו Da	ata					В	lue	Da	Ita		
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Colors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\bigtriangleup	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	\bigtriangleup					1							,	1								1			
of Red	\bigtriangledown	_	1			ļ	1						,						1			ļ	1		-
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\bigtriangledown	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
of Green	Δ	_				<u> </u>																<u> </u>			
		_		~					0	4	4	4	, ,		4	0	4	_						0	
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green Black	0	0	0 0	0 0	0	0 0	0	0	1 0	1 0	1 0	1 0	1 0	1	1 0	1 0	0	0 0	0 0	0	0 0	0	0 0	0
		0	0	0	0	0	0	0	0 0	0	0	0	0	0	0 0	0	0	0	0	0	0	0	0	0	0
	 Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Gray Scale		0	0	0	0	•	0	0	0	0	0	0	0		0	0	0	0	U	0		<u> </u>	0	I	0
-	\sim	-																							
of Blue	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
ŀ	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ŀ		0		0					1					0			1	0			0				1
Croy Scole	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
Gray Scale		Ť	_ <u> </u>	J	<u> </u>	1			<u> </u>	Ť	5	5	<u> </u>					Ť		Ŭ		<u>↓ ∪</u> ↑	<u> </u>		Ŭ
of White	$\overline{\nabla}$					I																l.			
•	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
		1	1	1	1	1	1	1	0	1		1	1		1	1	0	1	1	1	1	1	1	1	0
	White			1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
		1	1																		1 '				
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BOE	PRODUCT GROUP	REV	ISSUE DATE
DZL	Customer SPEC	0	2023.3.17

5.4 Power Sequence

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



< Table 11. Sequence Table >

Parameter		Units				
Farameter	Min	Тур	Max	Units		
T1	0.5	-	10	ms		
T2	0	-	50	ms		
Т3	500	-	-	ms		
T4	500	-	-	ms		
T5	0	-	30	ms		
T6	1	-	-	S		

Notes: 1. Back Light must be turn on after power for logic and interface signal are valid.

2. Even though T1 is out of SPEC, it is still ok if the inrush current of VDD is below the limit.

3. When VDD<0.9VDD(Typ.),Power off.

4. T7 decreases smoothly, if there were rebounding voltage, it must smaller than 0.5 volts.

SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	17 OF 33
	00011 0	A4(210 X 297)

6.0 OPTICAL SPECIFICATIONS

The test of optical specifications shall be measured in a dark room (ambient luminance≤1 lux and temperature=25±2°C) with the equipment of Luminance meter system (Goniometer system and PR788) and test unit shall be located at an approximate distance 180cm from the LCD surface at a viewing angle of θ and Φ equal to 0°. We refer to $\theta_{\emptyset=0}$ (= θ_3) as the 3 o'clock direction (the "right"), $\theta_{\emptyset=90}$ (= θ_{12}) as the 12 o'clock direction ("upward"), $\theta_{\emptyset=180}$ (= θ_9) as the 9 o'clock direction ("left") and $\theta_{\emptyset=270}$ (= θ_6) as the 6 o'clock direction ("bottom"). While scanning θ and/or \emptyset , the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 12.0V at 25°C. Optimum viewing angle direction is 6 'clock.

< Table 12. Optical Table >

 $[VDD = 5.0V, Frame rate = 60Hz, Ta = 25\pm2 °C]$

Parame	eter	Symbol	Condition	Min	Тур	Max	Unit	Remark	
	Llouizontol	Θ ₃		85	89		Deg.		
Viewing	Horizontal	Θ9	CR > 10	85	89		Deg.	Note 1	
Angle	Vertical	Θ12	CR > 10	85	89		Deg.	Note I	
	ventical	Θ6		85	89		Deg.		
Center Luminan	ce of White	Lc	Θ = 0° ILED=65m	540	600		nit	Note 2 Equ.PR7 88	
Uniformity	9 Points	ΔΥ9	A	75%	80%			Note 3	
Contrast	ratio	CR	Θ = 0°	700:1	1000:1	-		Note 4	
	White	Wx			0.287				
	VVIIILE	Wy	Θ = 0°		0.310				
	Red	Rx			0.638				
Reproduction		Ry		TYP. - 0.03	0.352	TYP. +0.03			
of color		Gx	(Center)		0.306			Note 5	
	Oreen	Gy	Normal Viewing		0.628				
	Blue	Bx	Angle		0.145				
	Blue	Ву			0.055			_	
Col	or Gamut			67	72	-	%		
Response Time	Tr+	Tf		-	30	35	ms	Note 6	
Gamma S	Scale			2.0	2.2	2.4			
SPEC. NUMBER NO. S8-64-8D-33		C. TITLE	V16 Product S	Specificat	ion			PAGE 18 OF 3	

DBG-RD-商用器件-2022011-O

Note :

- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determi ned for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respe ct to the optical axis which is normal to the LCD surface.
- 2. Definition of Luminance of White (Lc): Measure the luminance of gray level 255 at center point, LC=L(5), (see Figure 1 shown in Appendix 12.0).
- 3. The White luminance uniformity on LCD surface is then expressed as : ΔY =Minimum Luminance e of 9 points / Maximum Luminance of 9 points.(see Figure 1 shown in Appendix 12.0)
- 4. Contrast measurements shall be made at viewing angle of $\theta = 0^{\circ}$ and at the center of the LCD sur face. Luminance shall be measured with all pixels in the view field set first to white, then to the d ark (black) state. (See Figure 2 shown in Appendix) Luminance Contrast Ratio (CR) is defined m athematically.

CR = Luminance when displaying a white raster Luminance when displaying a black raster

- 5. The color chromaticity coordinates specified in Table 9.shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel. The BLU is used by BOE.
- 6. Response time Tg is the average time required for display transition by switching the input signal as below table and is based on Frame rate fV =60Hz to optimize.

Each time in below table is defined as Figure 3 and shall be measured by switching the

Meas	ured								10	Target	É							1
Resp	onse	0	15	21	47	62	79	95	111	127	143	159	175	191	207	223	239	255
	0	~					-	8 3					× (-		10		
2	15			/		949	0	÷	c					2	5 39	1		<u></u>
2	31			1	/			<u> </u>					Ĩ.			1		
	47			/	/	/												
3	63			Ĩ.	/	/	1											
	79					1												
	95					-												
Start	127										~		a	a 9	3 0			
Start	143	·						6 Q				~			6			
1	159	2 2		3		2							~		8 8			2
5	175			1		19							~	1				
191 207											~							
						1					1		1			/		
	223														/	/	/	
	239					Ì.		0 0								/		
	255																/	/
7. Definition of Transmittance (T%) : Module is with white(L255) signal input Transmittance = Luminance of LCD Module Luminance of BLU × 100 %																		
SPEC. NUMBER SPEC. TITLE PAGE																		
NO. S8-64-8D-332 DV150X0M-N16 Product Specification 19 OF 33																		
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7.0 MECHANICAL CHARACTERISTICS

7.1 Dimensional Requirements

Figure 4(located in Appendix 12.0) shows mechanical outlines for the model DV150X0M-N16 . Other parameters are shown in Table 13.

Parameter	Specification	Unit
Dimensional outline	$326.5(H) \times 253.5(V) \times 10.4(D)$	mm
Weight	730	gram
Active area	304.128(H) × 228.096(V)	mm
Pixel pitch	0.297(H) ×0.297(V)	mm
Number of pixels	$1024(H) \times 768(V) (1 \text{ pixel} = R + G + B \text{ dots})$	pixels

< Table 13. Dimensional Parameters >

7.2 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.

SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	20 OF 33
		A 4/040 V 007

BOE	PRODUCT GROUP	REV	ISSUE DATE
DGr	Customer SPEC	0	2023.3.17

8.0 RELIABILITY TEST

The Reliability test items and its conditions are shown in below.

< Table 14. Reliability Test Parameters >

No	Test Items	Conditions
1	High temperature storage test	Ta = 70 °C, 240 hrs
2	Low temperature storage test	Ta = -20 °C, 240 hrs
3	High temperature & high humidity storage test	Ta = 60 °C, 90%RH, 240hrs
4	High temperature operation test	Ta = 70 °C, 240 hrs
5	Low temperature operation test	Ta = -20°C, 240hrs
6	High temperature & high humidity operation test	Ta = 50 °C, 80%RH, 240hrs
7	Thermal shock	Ta =-20°C~60°C ,per 30min,100cycle,Storage
8	ESD	Contact ±8kv : Class B ; Air ±14kv : Class B
9	Packing VIB/Drop	Highway condition(0.82Grms, 1-200Hz, Random +Z/ X/Y1/0.5/0.5hr)/One side fixed drop
10	VIB	10~300Hz, 1.5G, Sine, ±X, ±Y, ±Z Sweep (30min)
11	Shock	50G,20ms,Half-sine wave, $(\pm X,\pm Y,\pm Z)$, Storage

• This test condition is based on BOE Model.

SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	21 OF 33
	A4(210 X 297)	

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9.0 PROD	CUT SI					XX	BO Rohs Co CROHS CO MADE IN CO	E CO Neplient Lus HINA			
MDL ID N Digit Code	-	2	3	4	5	6	7	8	9	10	11
Description	GE		Grade	Line	Ye	ear	Month	N	lodel Exte	nsion Coc	le
Code Digit	12	13	14	15	16	17			18		
Description			Seria	al No				扫码不上	显示,BOB	E厂内用	
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NO. S8-64-8D-332 DV150X0M-N16 Product Specification 22 OF 33 DBG-RD-商用器件-2022011-O A4(210 X 2)											

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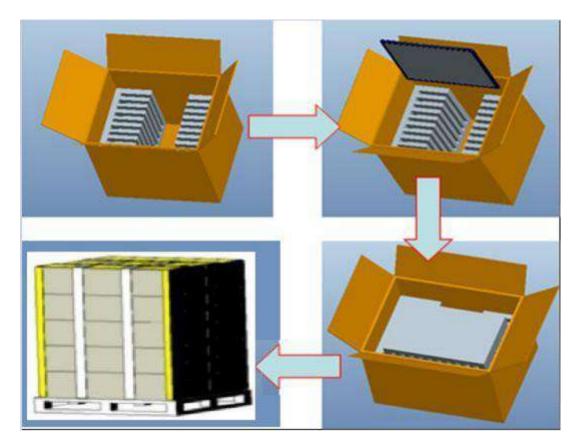
10.0 PACKING INFORMATION

BOE provides the standard shipping container for customers, unless customer specifies th eir packing information. The standard packing method and Barcode information are shown in below.

10.1 Packing Order

Put 1 EPE bottom into the inner box.

Put each module into a PE bag. Insert 10 Pcs MDL into each box



-Put the boxes on the Pallet

Put 1 EPE cover in and seal the box.

30boxes/Pallet:6boxes per layer, total 5 layers

- -Place paper corners and wrap film around the boxes
- -Pack with 4 packing belts,

SPEC. NUMBER NO. S8-64-8D-332

SPEC. TITLE DV150X0M-N16 Product Specification PAGE 23 OF 33

DBG-RD-商用器件-2022011-O

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D			С	ustom	er SPE	EC				0		2023.	3.17
10.2 Packing Note • Box Dimension : 390mm(L)×319mm(W)×328mm(H) • Package Quantity in one Box : 10pcs • Weight: 8.45±0.5kg/ Box													
10.3 Box I	_abel												
 Label Size : 100 mm (L) × 50 mm (W) Contents Model : DV150X0M-N16 Q`ty : Module 10 Q`ty in one box Serial No. : Box Serial No. Date : Packing Date 													
BC	?E				Co.,LTE			00			内容 , i FG-CC	<mark>说明如下</mark>)DE	
MODEL: SERIAL N		xxxx-xxx xxxxxxxx	XXX3			xxx : xxx	((X.XX.)	2 (x 4) CC	Ð	3 4	Box Pa	量), 编码规 acking E 料号(客F	期
		во хххххх		₹形码 x ⑤	ХХХ	X (6)	c W	BUS	-		DDE 后匹	
	1	2	2		A	5	(7			
Digit Code	1 x	2 x x	3 x	X	4 x	5 x	6 x	x	X	7 x	X	x	
Des. 1. Model Code GBN 2. Grade 3. Line 4. Year(2016:16, 2017:17,) 5. Month(1, 2, 3,, 9, X, Y, Z) 6. Revision Code 7. Serial Number													
SPEC. NUMBERSPEC. TITLEPAGENO. S8-64-8D-332DV150X0M-N16 Product Specification24 OF 33													
 DBG-RD-商用											/) X 297

11.0 PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD Module.

11.1 Mounting Precautions

- Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- You must mount a module using specified mounting holes (Details refer to the drawings)
- You should consider the mounting structure so that uneven force (ex. Twisted stress, Concentrated stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- Do not apply mechanical stress or static pressure on module; Abnormal display cause by pressing some parts of module during assembly process, do not belong to product failure, the press should be agreed by two sides.
- Determine the optimum mounting angle, refer to the viewing angle range in the specification for each model.
- Do not apply mechanical stress or static pressure on module , and avoid impact, vibration and falling.
- Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- Protection film for polarizer on the module should be slowly peeled off before display.
- Be careful to prevent water & chemicals contact the module surface.
- You should adopt radiation structure to satisfy the temperature specification.
- Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane & alcohol is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene, because they cause chemical damage to the polarizer.
- Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading..

NO. S8-64-8D-332	DV150X0M-N16 Product Specification	25 OF 33
SPEC. NUMBER	SPEC. TITLE	PAGE

0

BOE

- This module has its circuitry PCB's on the rear side and Driver IC, should be handled carefully in order not to be stressed.
- Avoid impose stress on PCB and Driver IC during assembly process ,Do not drawing, bending, COF package & wire
- Do not disassemble the module.

11.2 Operating Precautions

- Do not connector or disconnect the cable to/from the Module at the "Power On" Condition.
- When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the module would be damaged.
- Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- Do not allow to adjust the adjustable resistance or switch
- The electrochemical reaction caused by DC voltage will lead to LCD module degradation, so DC drive should be avoided.
- The LCD modules use C-MOS drivers, so customers are recommended that any unused input terminal would be connected to Vdd or Vss, do not input any signals before power is turn on, and ground you body, work/assembly area, assembly equipment to protect against static electricity.
- Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module may be damaged.
- Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- Design the length of cable to connect between the connector for back-light and the converter as shorter as possible and the shorter cable shall be connected directly, The long cable between back-light and Converter may cause the Luminance of LED to lower and need a higher startup voltage
- The cables should be as short as possible between System Board and PCB interface.
- Connectors are precision devices to transmit electrical signals, and operators should plug in parallel
- Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.

SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	26 OF 33

0

2023.3.17

11.3 Electrostatic Discharge Precautions

- Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.
- Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc.
- Do not close to static electricity to avoid product damage.
- Do not touch interface pin directly.

11.4 Precautions for Strong Light Exposure

• Do not leave the module operation or storage in Strong light . Strong light exposure causes degradation of polarizer and color filter.

11.5 Precautions for Storage

A. Atmosphere Requirement

ITEM	UNIT	MIN	ТҮР	MAX			
Storage Temperature	(°C)	5	25	40			
Storage Humidity	(%rH)	40	50	75			
Storage Life	6 months						
Storage Condition	facility.Prevent product and water.The product neeBe careful for content	m should be equipped s from being exposed ed to keep away from c ondensation at sudden on is guaranteed under	to the direct sunlight, organic solvent and c temperature change.	moisture corrosive gas.			

B. Package Requirement

- The product should be placed in a sealed polythene bag.
- Product Should be placed on the pallet, Which is away from the floor, Be cautions not to pile the product up.
- The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.
- As the original protective film, do not use the adhesive protective film to avoid change of Pol color and characteristic.

SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	27 OF 33

BOE

11.6 Precautions for protection film

- Remove the protective film slowly, keeping the removing direction approximate 30-degree not vertical from panel surface, If possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.
- People who peeled off the protection film should wear anti-static strap and grounded well.

11.7 Appropriate Condition for Commercial Display

Generally LCD modules are designed for consumer applications . Accordingly, long-term display like in Commercial Wide temperature Display application, can cause uneven display including image sticking. To optimize module's lifetime and function, several operating usages are required.

1. Normal operating condition

- Temperature: 20±15°C
- Operating Ambient Humidity : 55±20%
- Display pattern: dynamic pattern (Real display)
- Well-ventilated place is recommended to set up Commercial Display system
- 2. Special operating condition
 - a. Ambient condition
 - Well-ventilated place is recommended to set up Commercial Display system.
 - b. Power and screen save
 - Periodical power-off or screen save is needed after long-term display.

c. As the low temperature, the response time is greatly delayed. As the high temperatures (higher than the operating temperature) the LCD module may turn black screen. The above phenomenon cannot explain the failure of the display. When the temperature returns to the normal operating temperature, the LCD module will return to normal display.

d. When expose to drastic fluctuation of temperature (hot to cold or cold to hot) ,the LCD module may be affected; Specifically, drastic temperature fluctuation from cold to hot ,produces dew on the LCD module 's surface which may affect the operation of the polarizer and LCD module e. Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module may be damaged.

f. Products exposed to low temperature environment for a long time, need to carry out necessary protection , low temperature environment is usually refrigerators , vending machine Etc...

g. Long time and large angle forword use or unconventional use , It is strongly recommended to contact BOE for filed application engineering advice

SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	28 OF 33

h. Product reliability and functions are only guaranteed when the product is used under right operation usages. If product will be used in extreme conditions such as high temperature, high humidity, high altitude, special display images, running time, long time operation, outdoor operation, etc. It is strongly recommended to contact BOE for filed application engineering advice. Otherwise, its reliability and function may not be guaranteed.

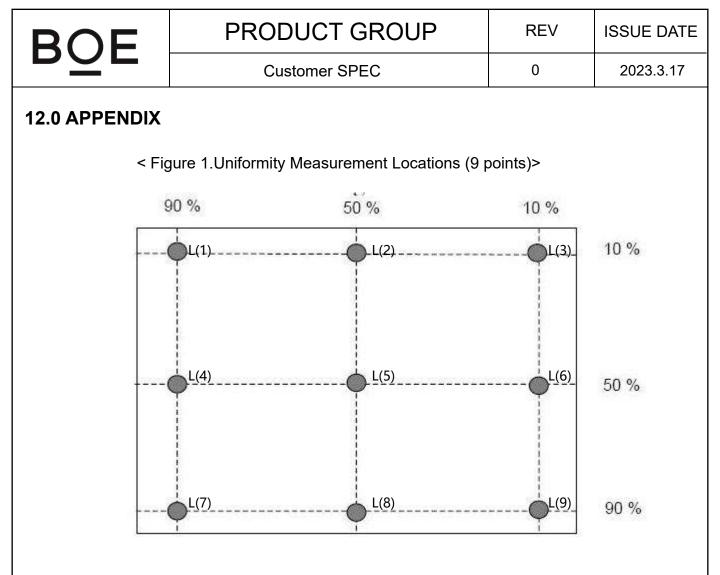
- 3. Operating usages to protect against image sticking due to long-term static display.
 - a. Static information display recommended to use with moving image.
 - Cycling display between 5 minutes' information(static) display and 10 seconds' moving image.
 - b. Background and character (image) color change
 - Use different colors for background and character, respectively.
 - Change colors themselves periodically.
 - c. Avoid combination of background and character with large different luminance.
 - 1) Abnormal condition just means conditions except normal condition.
 - 2) Black image or moving image is strongly recommended as a screen save
- 4. Lifetime in this spec. is guaranteed only when Commercial Display is used according to operating usages.

11.8 Other Precautions

A. LC Leak

- If the liquid crystal material leaks from the panel, it is recommended to wash the LC with acetone or ethanol and then burn it.
- If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- If LC in mouth, mouth need to be washed, drink plenty of water to induce vomiting and follow medical advice.
- If LC touch eyes, eyes need to be washed with running water at least 15 minutes.
- B. Rework
- When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	29 OF 33
		A4(210 X 297)



The White luminance uniformity on LCD surface is then expressed as : $\Delta Y9 = Minimum$ Luminance of five points / Maximum Luminance of 9 points

SPEC. NUMBER	SPEC. TITLE	PAGE
NO. S8-64-8D-332	DV150X0M-N16 Product Specification	30 OF 33
		A4(210 X 297)

DBG-RD-商用器件-2022011-O

