



Tentative Specification
<b>Preliminary Specification</b>
Approval Specification

# MODEL NO.: G150XNE SUFFIX: L01

Customer:	
APPROVED BY	SIGNATURE
Name / Title Note	
Please return 1 copy for you signature and comments.	ır confirmation with your

Approved By	Checked By	Prepared By
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Version 2.1 26 December 2023 1 / 41



# **CONTENTS**

1. GENERAL DESCRIPTION	5
1.1 OVERVIEW	5
1.2 FEATURE	5
1.3 APPLICATION	5
1.4 GENERAL SPECIFICATIONS	5
1.5 MECHANICAL SPECIFICATIONS	6
2. ABSOLUTE MAXIMUM RATINGS	7
2.1 ABSOLUTE RATINGS OF ENVIRONMENT	7
2.2 ELECTRICAL ABSOLUTE RATINGS	8
2.2.1 TFT LCD MODULE	8
2.2.2 BACKLIGHT UNIT	8
3. ELECTRICAL CHARACTERISTICS	9
3.1 TFT LCD MODULE	
3.2 BACKLIGHT UNIT	10
4. BLOCK DIAGRAM	12
4.1 TFT LCD MODULE	
5. INPUT TERMINAL PIN ASSIGNMENT	
5.1 TFT LCD MODULE	
5.2 BACKLIGHT UNIT(Converter connector pin)	
5.3 COLOR DATA INPUT ASSIGNMENT	
6. INTERFACE TIMING	
6.1 INPUT SIGNAL TIMING SPECIFICATIONS	
6.2 POWER ON/OFF SEQUENCE	
6.3 SCANNING DIRECTION	
7. OPTICAL CHARACTERISTICS	
7.1 TEST CONDITIONS	
7.2 OPTICAL SPECIFICATIONS	
8. RELIABILITY TEST CRITERIA	
9. PACKAGING	
9.1 PACKING SPECIFICATIONS	
9.2 PACKING METHOD	
9.3 UN-PACKING METHOD	
10. DEFINITION OF LABELS	
10.1 INX MODULE LABEL	
11. PRECAUTIONS	
11.1 ASSEMBLY AND HANDLING PRECAUTIONS	
11.2 STORAGE PRECAUTIONS	30



11.3 OTHER PRECAUTIONS31  12. MECHANICAL CHARACTERISTICS32	#1 /HJ / U - B	
12. MECHANICAL CHARACTERISTICS32	11.3 OTHER PRECAUTIONS	. 31
	12. MECHANICAL CHARACTERISTICS	. 32
Appendix. SYSTEM COVER DESIGN NOTICE34		



## **REVISION HISTORY**

Version	Date	Page	Description
Ver 2.0	28 Jun 2021	All	Approval Specification for Mstar-Tcon was first issued.
Ver 2.1	25 Dec 2023	10	3.2 BACKLIGHT UNIT modify EN Control Level (Backlight off max) from 0.15V to 0.3V
		11	Modify Note1 light bar parallel and series
		18	Modify TIMING DIAGRAM of LVDS
		29	Add UL No.E207943 in INX Module Label



#### 1. GENERAL DESCRIPTION

#### 1.1 OVERVIEW

G150XNE-L01 is a 15.0" TFT Liquid Crystal Display IA module with LED Backlight units and 20 pins LVDS interface. This module supports 1024 x 768 XGA mode and can display 16.7M/262k colors.

The PSWG is to establish a set of displays with standard mechanical dimensions and select electrical interface requirements for an industry standard 15.0" XGA LCD panel and the LED driving device for Backlight is built in PCBA.

#### **1.2 FEATURE**

- XGA (1024 x 768 pixels) resolution
- DE (Data Enable) only mode
- LVDS Interface with 1pixel/clock
- PSWG (Panel Standardization Working Group)
- Wide operating temperature.
- RoHS compliance

#### 1.3 APPLICATION

- -TFT LCD Monitor
- Factory Application
- Amusement

#### 1.4 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Active Area	304.1 (H) x 228.1(V) (15.0" diagonal)	mm	(1)
Driver Element	a-Si TFT active matrix	-	-
Pixel Number	1024 x R.G.B x 768	pixel	-
Pixel Pitch	0.297(H) x 0.297(W)	mm	-
Pixel Arrangement	RGB vertical Stripe	-	-
Display Colors	16.7M / 262K	color	-
Display Mode	Normally Black	-	-
Surface Treatment	Hard Coating (3H), Anti-Glare	-	-
Module Power Consumption	9.62	W	Тур.



#### 1.5 MECHANICAL SPECIFICATIONS

Item		Min.	Тур.	Max.	Unit	Note
	Horizontal(H)	326.0	326.5	327.0	mm	
Module Size	Vertical(V)	253.0	253.5	254.0	mm	(1)
	Depth(D)	8.6	9.1	9.6	mm	
Dozel Area	Horizontal	307.1	307.4	307.7	mm	-
Bezel Area	Vertical	231.0	231.3	231.6	mm	
Active Area	Horizontal	-	304.1	-	mm	
Active Area	Vertical	-	228.1	-	mm	
We	ight	-	960	1000	g	

Note (1)Please refer to the attached drawings for more information of front and back outline dimensions.



#### 2. ABSOLUTE MAXIMUM RATINGS

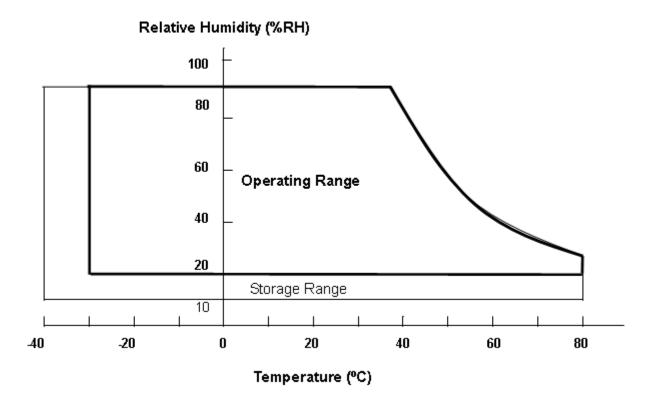
#### 2.1 ABSOLUTE RATINGS OF ENVIRONMENT

ltom	Cumbal	Va	lue	l loit	Note	
Item	Symbol	Min.	Max.	Unit		
Operating Ambient Temperature	TOP	-30	+80	$^{\circ}\!\mathbb{C}$	(1)(2)	
Storage Temperature	T <sub>ST</sub>	-40	+80	$^{\circ}\!\mathbb{C}$	(1)(2)	

Note (1)

- (a) 90 %RH Max.
- (b) Wet-bulb temperature should be 39 °C Max.
- (c) No condensation.

Note (2) Panel surface temperature should be  $0^{\circ}$ C min. and  $65^{\circ}$ C max under Vcc=5.0V, fr =60Hz, typical LED string current,  $25^{\circ}$ C ambient temperature, and no humidity control . Any condition of ambient operating temperature ,the surface of active area should be keeping not higher than  $65^{\circ}$ C.





#### 2.2 ELECTRICAL ABSOLUTE RATINGS

#### 2.2.1 TFT LCD MODULE

Item	Symbol	, Value		Unit	Note	
item	Symbol	Min.	Max.	Offic	Note	
Power Supply Voltage	VCC	-0.3	4	V	(1)	
Logic Input Voltage	Vin	-0.3	4	V	(1)	

#### 2.2.2 BACKLIGHT UNIT

Item	Symbol	Value		Unit	Note	
item	Symbol	Min.	Max.	Offic	Note	
Converter Voltage	Vi	-0.3	18	V	(1), (2)	
Enable Voltage	EN		5.5	V		
Backlight Adjust	Dimming		5.5	V		

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for LED (Refer to 3.2 for further information).



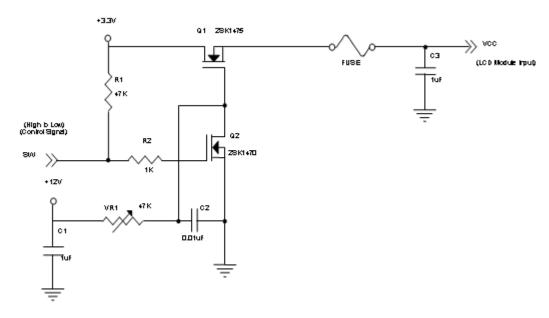
#### 3. ELECTRICAL CHARACTERISTICS

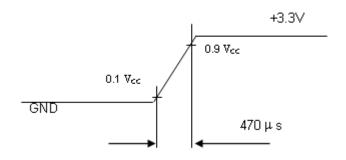
#### 3.1 TFT LCD MODULE

Daramatar	Cumbal	Symbol Value			Unit	Note	
Parameter	Symbol	Min.	Тур.	Max.	Unit	note	
Power Supply Vo	Itage	Vcc	3.0	3.3	3.6	V	-
Ripple Voltag	е	$V_{RP}$	ı	ı	100	mVp-p	
Inrush Current		I <sub>INRUSH</sub>	ı	ı	2.0	Α	(2)
Power Supply Current	White	lcc	ı	550	660	mA	(3)a
Fower Supply Current	Black	ICC	-	440	530	mA	(3)b
LVDS differential inpu	ıt voltage	V <sub>id</sub>	200	-	600	mV	
LVDS common input voltage		Vic	1.0	1.2	1.4	V	
Differential Input Voltage for	"H" Level	ViH	1	-	100	mV	-
LVDS Receiver Threshold	"L" Level	VıL	-100	-	-	mV	-
Terminating Res	istor	R⊤	-	100	-	Ohm	-

Note (1)The module should be always operated within above ranges.

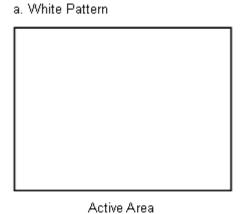
Note (2) Measurement Conditions:







Note (3) The specified power supply current is under the conditions at  $V_{DD}$  =3.3V, Ta = 25  $\pm$  2  $^{\circ}$ C, DC Current and  $f_{v}$  = 60 Hz, whereas a power dissipation check pattern below is displayed.



b. Black Pattern



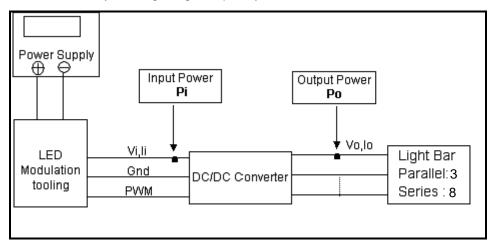
Active Area

#### 3.2 BACKLIGHT UNIT

Parameter		Symbol		Value		Unit	Note
Parame	etei	Symbol	Min.	Тур.	Max.	Oill	Note
Converter Inp	ut Voltage	$V_{i}$	10.8	12.0	13.2	$V_{DC}$	(Duty 100%)
Converter Input F	Ripple Voltage	ViRP	-	-	500	mV	
Converter Inp	ut Current	li	0.5	0.65	0.8	Adc	@ Vi = 12V (Duty 100%)
Converter Inru	l <sub>iRUSH</sub>	-	-	5.0	А	<pre>@ Vi rising     time=10ms     (Vi=12V)</pre>	
Input Power Co	onsumption	Pi	-	7.8	9.6	W	(1)
EN Control Level	Backlight on	ENLED	2.0	3.3	5.0	V	
EN Control Level	Backlight off	(BLON)	0	-	0.3	V	
PWM Control Level	PWM High Level	Dimming	2.0	-	5.0	V	
Pyvivi Control Level	PWM Low Level	(E_PWM)	0	-	0.15	V	
PWN Noise	Range	VNoise	•	-	0.1	V	
PWM Control	Frequency	f <sub>PWM</sub>	190	200	20k	Hz	(2)
DIAMA Director of Co	ntral Duty Datia		5	-	100	%	(2), @ 190Hz <f<sub>PWM&lt;1kHz</f<sub>
PWM Dimming Co	ntroi Duty Ratio	-	20	-	100	A time (Vi=  W (  V  V  V  V  V  Hz  (2)  190Hz <fr></fr> (4)  % 1kHz≤f	(2), @ 1kHz≦f <sub>PWM</sub> <20kHz
LED Life	Time	L <sub>LED</sub>	50,000	70,000	-	Hrs	(3)



Note (1)LED current is measured by utilizing a high frequency current meter as shown below:



- Note (2) At 190 ~1kHz PWM control frequency, duty ratio range is restricted from 5% to 100%.

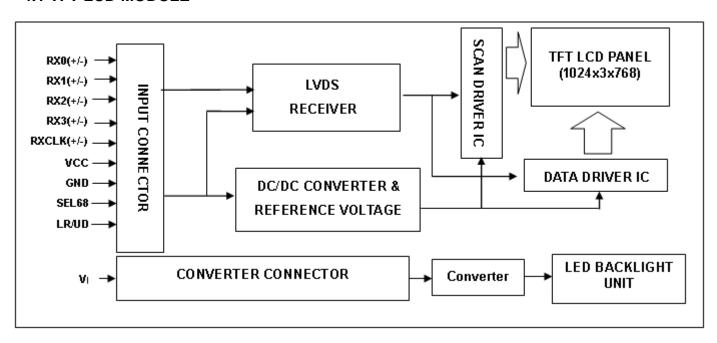
  1K ~20kHz PWM control frequency, duty ratio range is restricted from 20% to 100%.

  If PWM control frequency is applied in the range from 1KHz to 20KHZ, The "non-linear" phenomenon on the Backlight Unit may be found. So It's a **suggestion** that PWM control frequency should be **less** than 1KHz.
- Note (3) The lifetime of LED is estimated data and defined as the time when it continues to operate under the conditions at Ta =  $25 \pm 2$  °C and Duty 100% until the brightness becomes  $\leq 50\%$  of its original value. Operating LED at high temperature condition will reduce life time and lead to color shift.



#### 4. BLOCK DIAGRAM

#### 4.1 TFT LCD MODULE



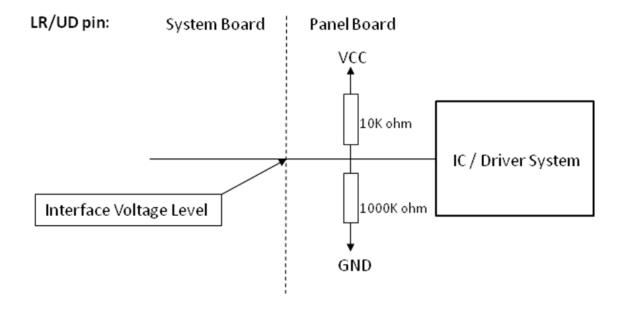


#### 5. INPUT TERMINAL PIN ASSIGNMENT

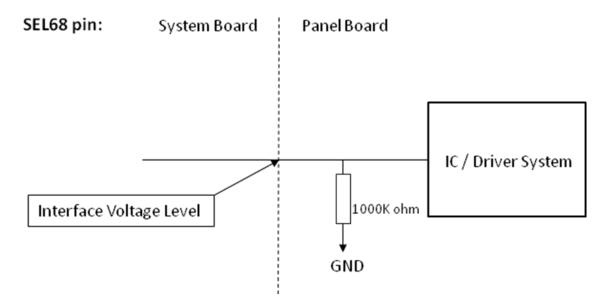
#### **5.1 TFT LCD MODULE**

Pin No.	Symbol	Function	Polarity	Note
1	VCC	Power Supply +3.3V(typical)		
2	VCC	Power Supply +3.3V(typical)		
3	NC	No Connection (Reserve for INX test)		Note (4)
4	LR/UD	Reverse Scan Control		Note (3)
		H or NC = Normal Mode.		
		L = Horizonta/ Vertical Reverse Scan.		
5	RX0-	LVDS Differential Data Input	Negative	
6	RX0+	LVDS Differential Data Input	Positive	
7	GND	Ground		
8	RX1-	LVDS Differential Data Input	Negative	
9	RX1+	LVDS Differential Data Input	Positive	
10	NC	No Connection (Reserve for INX test)		Note (4)
11	RX2-	LVDS Differential Data Input	Negative	
12	RX2+	LVDS Differential Data Input	Positive	
13	GND	Ground		
14	RXCLK-	LVDS Differential Data Input	Negative	
15	RXCLK+	LVDS Differential Data Input	Positive	
16	GND	Ground		
17	RX3-	LVDS Differential Data Input	Negative	
18	RX3+	LVDS Differential Data Input	Positive	
19	NC	No Connection (Reserve for INX test)		Note (4)
20	SEL68	LVDS 6/8 bit select function control,		Note (3)
		High → 6bit Input Mode		
		Low or NC → 8bit Input Mode		

- Note (1) Connector Part No.: STM MSB240420HDA or equivalent.
- Note (2) User's connector Part No.: Hirose DF14-20S-1.25C or equivalent.
- Note (3) "Low" stands for 0V. "High" stands for 3.3V. "NC" stands for "No Connection".
- Note (4) Pin3, Pin10, Pin19 input signals should be set to no connection or ground, this module would operate normally.







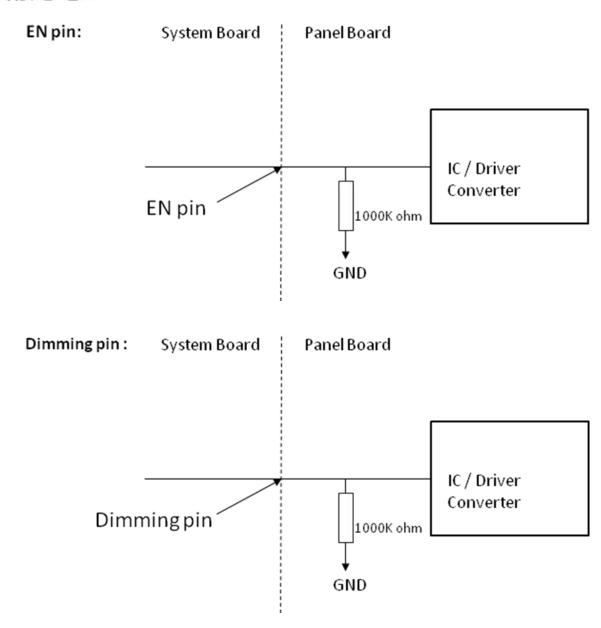
### 5.2 BACKLIGHT UNIT(Converter connector pin)

Pin	Symbol	Description	Remark
1	Vi	Converter input voltage	12V
2	V <sub>GND</sub>	Converter ground	Ground
3	EN	Enable pin	3.3V
4	Dimming	Backlight Adjust	PWM Dimming (Hi: 3.3V <sub>DC</sub> , Lo: 0V <sub>DC</sub> )
5	NC	Not Connect	

Note (1)Connector Part No.: CI4205M2HRP-NH (Cvilux) or equivalent.

Note (2)User's connector Part No.: MOLEX 51146-0500 or equivalent.







#### **5.3 COLOR DATA INPUT ASSIGNMENT**

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input the brighter the color. The table below provides the assignment of color versus data input.

												D	ata	Sig	nal										
	Color				Re								Gre								BI				
	T	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2		G0	B7	B6	B5	B4	В3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Red	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Reu	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Green	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
GICCII	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Blue	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
Diag	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note (1)0: Low Level Voltage, 1: High Level Voltage



#### 6. INTERFACE TIMING

#### **6.1 INPUT SIGNAL TIMING SPECIFICATIONS**

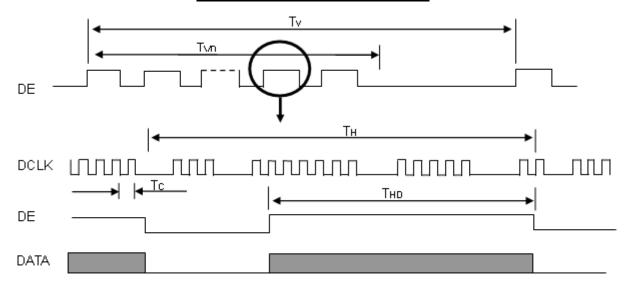
The input signal timing specifications are shown as the following table and timing diagram.

Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Note
	Frequency	Fr	53.35	65	80	MHz	-
	Period	Tc	12.5	15.38	18.75	ns	
	Input cycle to cycle jitter	T <sub>rcl</sub>			200	ns	(a)
LVDS Clock	Input Clock to data skew	TLVCCS	-0.02*Tc	-	0.02*Tc	ps	(b)
	Spread spectrum modulation range	F <sub>clkin_mod</sub>	ı	ı	1.02*Fc	MHz	(0)
	Spread spectrum modulation frequency	F <sub>SSM</sub>	ı	ı	200	KHz	(c)
	Frame Rate	Fr	55	60	70	Hz	$Tv=T_{vd}+T_{vb}$
Vertical Display	Total	Tv	780	806	840	Th	-
Term	Active Display	$T_{vd}$	768	768	768	Th	-
	Blank	T <sub>vb</sub>	$T_v$ - $T_{vd}$	38	T <sub>v</sub> -T <sub>vd</sub>	Th	-
	Total	Th	1240	1344	1360	Tc	$T_h = T_{hd} + T_{hb}$
Horizontal Display Term	Active Display	T <sub>hd</sub>	1024	1024	1024	Tc	-
101111	Blank	T <sub>hb</sub>	$T_{h}$ - $T_{hd}$	320	$T_{h}$ - $T_{hd}$	Tc	-

Note (1) Because this module is operated by DE only mode, Hsync and Vsync input signals should be set to low logic level or ground. Otherwise, this module would operate abnormally.

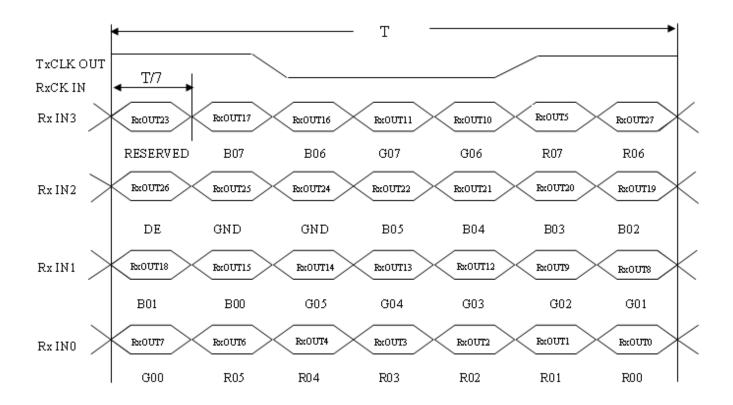
Note (2) The Tv(Tvd+Tvb) must be integer, otherwise, the module would operate abnormally.

#### **INPUT SIGNAL TIMING DIAGRAM**

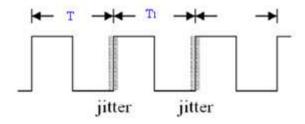




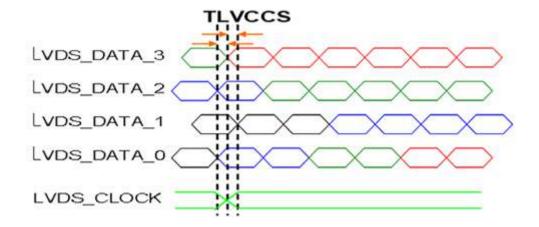
#### **TIMING DIAGRAM of LVDS**



Note (a) The input clock cycle-to-cycle jitter is defined as below figures.  $T_{rcl} = I T1 - TI$ 

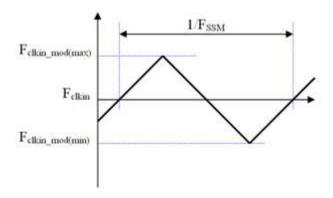


Note (b) Input Clock to data skew is defined as below figures.



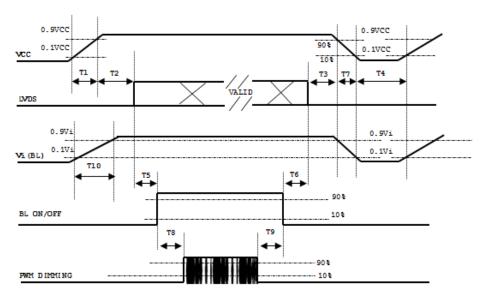


Note (c) The SSCG (Spread spectrum clock generator) is defined as below figures.



#### **6.2 POWER ON/OFF SEQUENCE**

To prevent a latch-up or DC operation of LCD assembly, the power on/off sequence should be as the diagram below.



Version 2.1 26 December 2023 19 / 41



Doromotor		Units				
Parameter	Min	Тур	Max	Units		
T1	0.5	-	10	ms		
T2	0	-	50	ms		
T3	0	-	50	ms		
T4	500	-	-	ms		
T5	450	-	-	ms		
Т6	200	-	-	ms		
T7	10	-	100	ms		
Т8	10	-	-	ms		
Т9	10	-	-	ms		
T10	20	-	50	ms		

#### Note:

- (1)The supply voltage of the external system for the module input should be the same as the definition of Vcc.
- (2)When the backlight turns on before the LCD operation of the LCD turns off, the display may momentarily become abnormal screen.
- (3)In case of VCC = off level, please keep the level of input signals on the low or keep a high impedance.
- (4)T4 should be measured after the module has been fully discharged between power off and on period.
- (5)Interface signal shall not be kept at high impedance when the power is on.
- (6)INX won't take any responsibility for the products which are damaged by the customers not following the Power Sequence.
- (7)There might be slight electronic noise when LCD is turned off (even backlight unit is also off). To avoid this symptom, we suggest "Vcc falling timing" to follow "T7 spec".



#### **6.3 SCANNING DIRECTION**

The following figures show the image see from the front view. The arrow indicates the direction of scan.

#### Fig.1 Normal Scan



Fig.2 Reverse Scan



PCBA on the top side

PCBA on the top side

- Fig. 1 Normal scan (pin 4, LR/UD = High or NC)
- Fig. 2 Reverse scan (pin 4, LR/UD = Low)



#### 7. OPTICAL CHARACTERISTICS

#### 7.1 TEST CONDITIONS

Item	Symbol	Value	Unit					
Ambient Temperature	Ta	25±2	ОС					
Ambient Humidity	Ha	50±10	%RH					
Supply Voltage	Accordin	According to typical value and tolerance in						
Input Signal	"ELECTRICAL CHARACTERISTICS"							
PWM Duty Ratio	D	100	%					

#### 7.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown here and all items are measured at the center point of screen unless otherwise noted. The following items should be measured under the test conditions described above and stable conditions shown in Note (5).

Iter	n	Symbol	Condition	Min.	Тур.	Max.	Unit	Note
	Red	Rx		0.597	0.647	0.697		
	Reu	Ry		0.288	0.338	0.388		
	Green	Gx		0.271	0.321	0.371		
Color	Gie	Gy		0.556	0.606	0.656	_	(1), (5)
Chromaticity	Blue	Bx	$\theta X=0^{\circ}, \ \theta Y=0^{\circ}$	0.107	0.157	0.207	_	(1), (3)
	Dide	Ву	Grayscale Maximum	0.000	0.039	0.089		
	White	Wx		0.263	0.313	0.363		
	vviiite	Wy		0.279	0.329	0.379		
Center Lumina	nce of White	LC		400	500			(4), (5)
Contrast	Ratio	CR		1800	2500			(2), (5)
Respons	o Timo	TR	θX=0°, θY =0°	-	16	21	-	(3)
Respons	e mine	TF	₩=0 , ₩1 =0	-	7	14	-	(3)
White Va	riation	δW	$\theta X=0^{\circ}, \ \theta Y=0^{\circ}$	75	80	-	%	(5), (6)
	Horizontal	θX+		80	88	-		
Viewing Angle	i ionzoniai	θX-	CR≧10	80	88	-	Deg.	(1) (5)
viewing Angle	Vertical	θΥ+	OI\≦ IU	80	88	-	Deg.	(1), (5)
	vertical	θΥ-		80	88	-		

#### Definition:

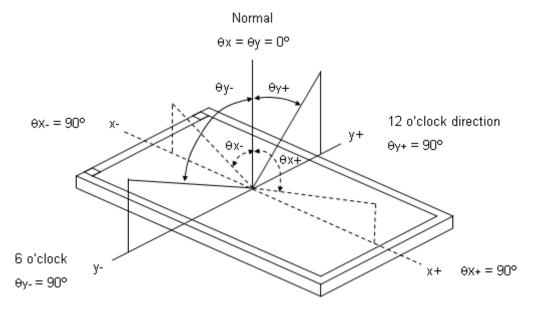
Grayscale Maximum : Grayscale 255 (10 bits: grayscale 1023 ; 8 bits : grayscale 255 ; 6 bits: grayscale 63)

White: Luminance of Grayscale Maximum (All R,G,B)

Black: Luminance of grayscale 0 (All R,G,B)



Note (1)Definition of Viewing Angle ( $\theta x$ ,  $\theta y$ ):

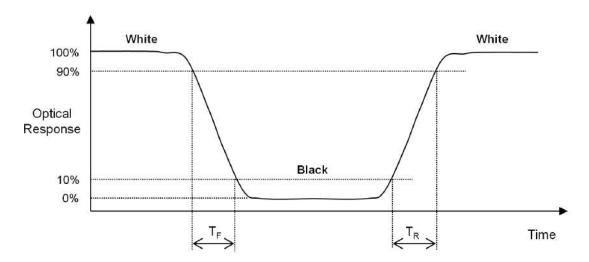


#### Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression at center point.

Contrast Ratio (CR) = White / Black

#### Note (3) Definition of Response Time (TR, TF):



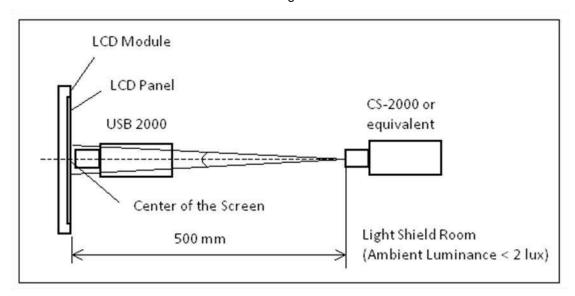


#### Note (4) Definition of Luminance of White (L<sub>C</sub>):

Measure the luminance of White at center point.

#### Note (5) Measurement Setup:

The LCD module should be stabilized at given temperature to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 40 minutes in a windless room. The measurement placement of module should be in accordance with module drawing.



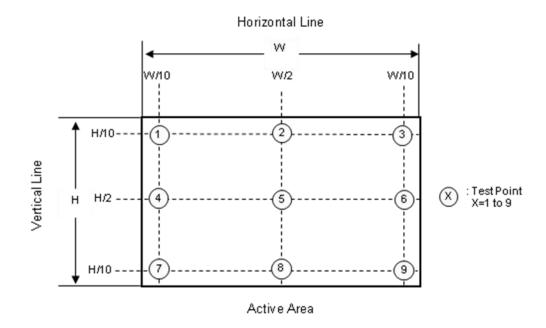
#### Note (6) Definition of White Variation ( $\delta W$ ):

Measure the luminance of White at 9 points.

Luminance of White : L(X) , where X is from 1 to 9.

$$\delta W = \frac{\text{Minimum [L(1) to L(9)]}}{\text{Maximum [L(1) to L(9)]}} \times 100\%$$







#### 8. RELIABILITY TEST CRITERIA

Test Item	Test Condition	Note
High Temperature Storage Test	80℃, 240 hours	
Low Temperature Storage Test	-40°C, 240 hours	
Thermal Shock Storage Test	$-30^{\circ}$ C, 0.5 hour $\longleftrightarrow$ 70 $^{\circ}$ C, 0.5 hour; 100cycles, 1 hour/cycle)	(1),(2)
High Temperature Operation Test	80℃, 240 hours	(4),(5)
Low Temperature Operation Test	-30°C, 240 hours	( ),( )
High Temperature & High Humidity Operation Test	60℃, RH 90%, 240 hours	
	150pF, 330 Ω , 1 sec/cycle	
ESD Test (Operation)	Condition 1 : panel contact, ±8 KV	(1), (4)
	Condition 2 : panel non-contact $\pm 15$ KV	
Shock (Non-Operating)	50G, 11ms, half sine wave, 1 time for ± X, ± Y, ± Z direction	
Vibration (Non-Operating)	1.5G, 10 ~ 300 Hz sine wave, 10 min/cycle, 3 cycles each X, Y, Z direction	(2), (3)

- Note (1) There should be no condensation on the surface of panel during test,
- Note (2) Temperature of panel display surface area should be 80°C Max.
- Note (3) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.
- Note (4) In the standard conditions, there is no function failure issue occurred. All the cosmetic specification is judged before reliability test.
- Note (5) Before cosmetic and function test, the product must have enough recovery time, at least 24 hours at room temperature.



#### 9. PACKAGING

#### 9.1 PACKING SPECIFICATIONS

- (1) 16pcs LCD modules / 1 Box
- (2) Box dimensions: 511 (L) X 420 (W) X 360 (H) mm
- (3) Weight: approximately 18Kg (16 modules per box)

#### 9.2 PACKING METHOD

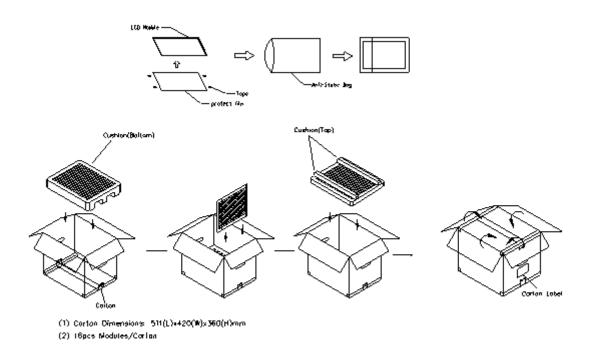


Figure. 9-1 Packing method

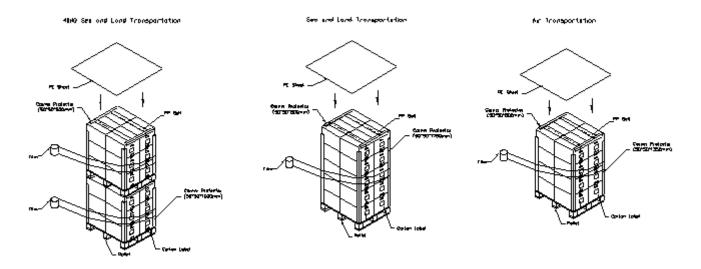


Figure. 9-2 Packing method



### 9.3 UN-PACKING METHOD

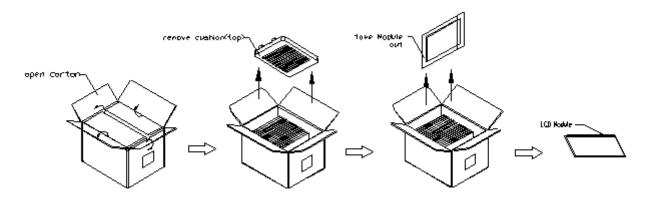


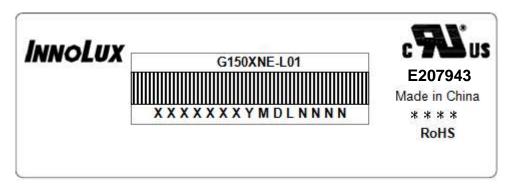
Figure. 9-3 UN-Packing method



#### 10. DEFINITION OF LABELS

#### **10.1 INX MODULE LABEL**

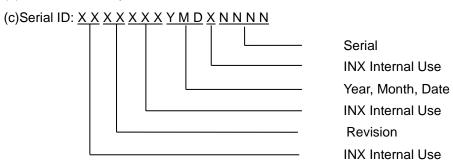
The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



Note (1) Safety Compliance(UL logo) will open after C1 version.

(a)Model Name: G150XNE-L01

(b)\* \* \* \* : Factory ID



Serial ID includes the information as below:

(a) Manufactured Date: Year: 1~9, for 2021~2029

Month: 1~9, A~C, for Jan. ~ Dec.

Day:  $1\sim9$ ,  $A\sim Y$ , for  $1^{st}$  to  $31^{st}$ , exclude I , O and U

(b) Revision Code: cover all the change

(c) Serial No.: Manufacturing sequence of product



#### 11. PRECAUTIONS

#### 11.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) The module should be assembled into the system firmly by using every mounting hole. Be careful not to twist or bend the module.
- (2) While assembling or installing modules, it can only be in the clean area. The dust and oil may cause electrical short or damage the polarizer.
- (3) Use fingerstalls or soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (4) Do not press or scratch the surface harder than a HB pencil lead on the panel because the polarizer is very soft and easily scratched.
- (5) If the surface of the polarizer is dirty, please clean it by some absorbent cotton or soft cloth. Do not use Ketone type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanently damage the polarizer due to chemical reaction.
- (6) Wipe off water droplets or oil immediately. Staining and discoloration may occur if they left on panel for a long time.
- (7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contacting with hands, legs or clothes, it must be washed away thoroughly with soap.
- (8) Protect the module from static electricity, it may cause damage to the C-MOS Gate Array IC.
- (9) Do not disassemble the module.
- (10) Do not pull or fold the lamp wire.
- (11) Pins of I/F connector should not be touched directly with bare hands.

#### 11.2 STORAGE PRECAUTIONS

- (1) When storing for a long time, the following precautions are necessary.
  - (a)Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 30°C at humidity 50+-10%RH.
  - (b) The polarizer surface should not come in contact with any other object.
  - (c) It is recommended that they be stored in the container in which they were shipped.
  - (d)Storage condition is guaranteed under packing conditions.
    - The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition
- (2) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
  - It is dangerous that moisture come into or contacted the LCD module, because the moisture may damage LCD module when it is operating.
  - It may reduce the display quality if the ambient temperature is lower than 10 °C. For example, the response time will become slowly, and the starting voltage of lamp will be higher than the room temperature.

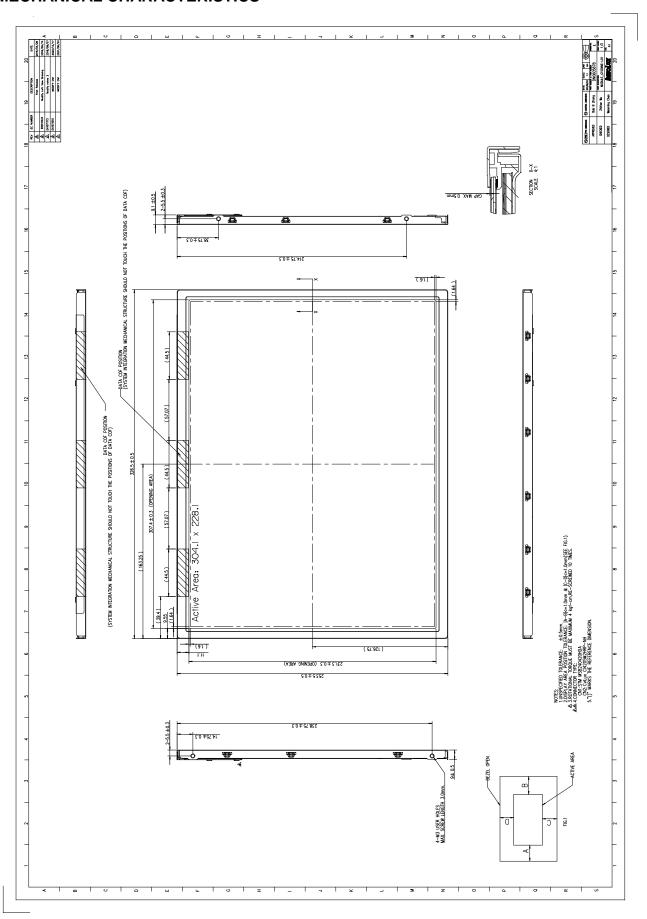


#### 11.3 OTHER PRECAUTIONS

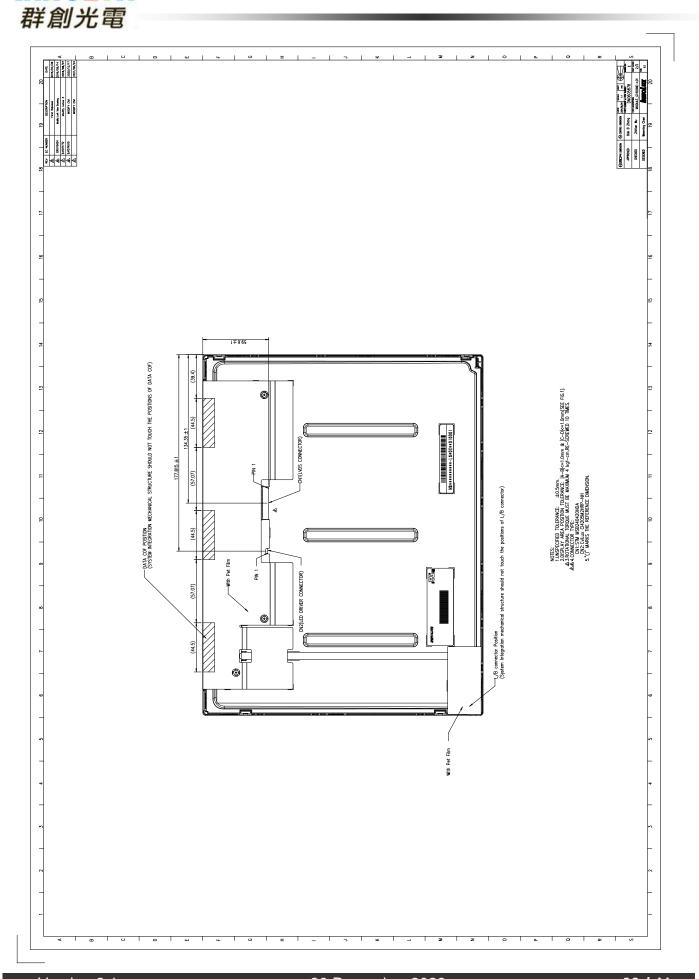
- (1) Normal operating condition
  - (a) Display pattern: dynamic pattern (Real display)(Note) Long-term static display can cause image sticking.
- (2) Operating usages to protect against image sticking due to long-term static display
  - (a) Suitable operating time: under 16 hours a day.
  - (b) Static information display recommended to use with moving image.
  - (c)Cycling display between 5 minutes' information(static) display and 10 seconds' moving image.
- (3) Abnormal condition just means conditions except normal condition.



#### 12. MECHANICAL CHARACTERISTICS

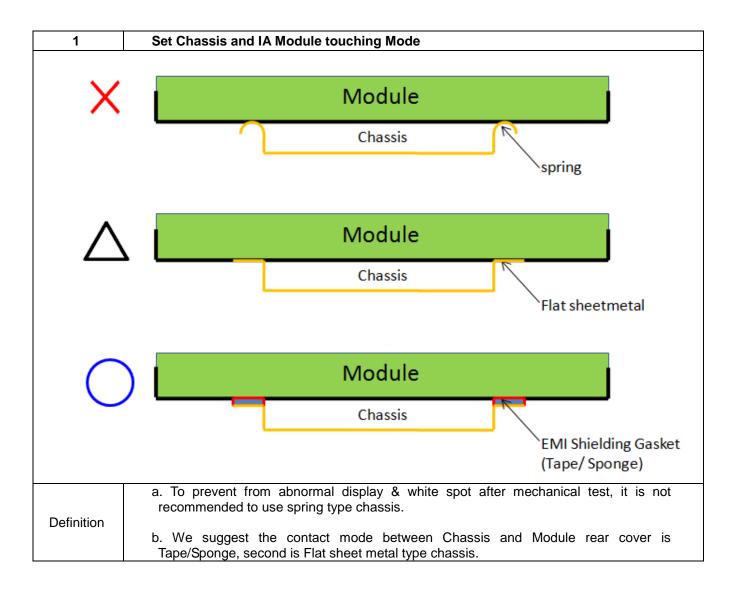




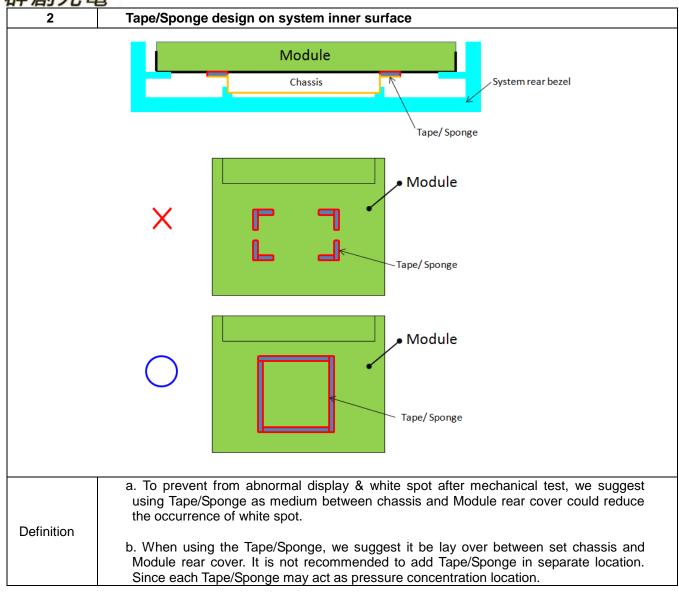




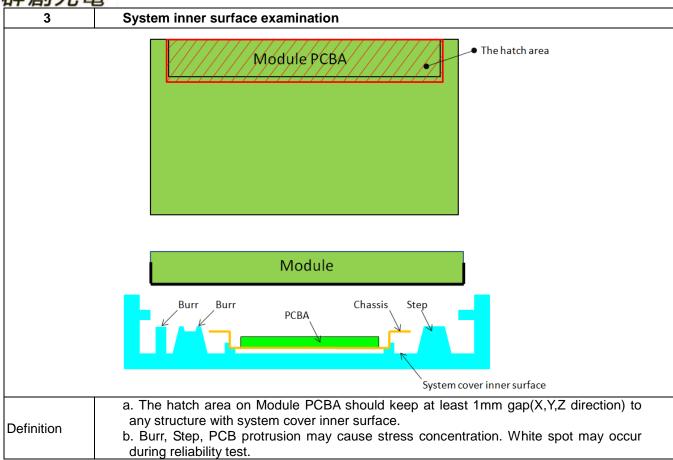
### **Appendix. SYSTEM COVER DESIGN NOTICE**

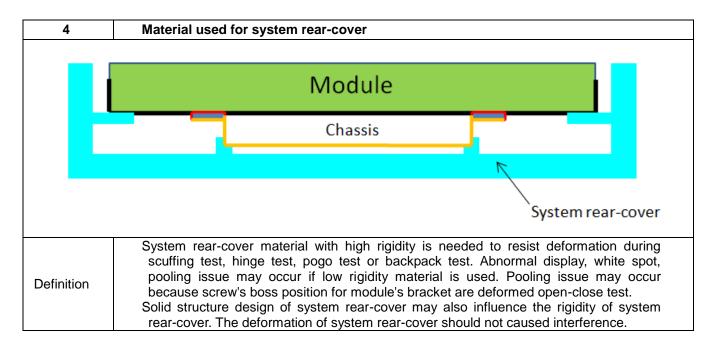




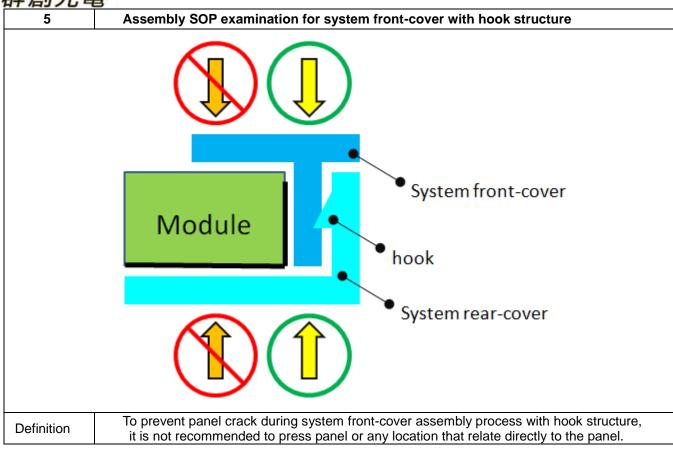






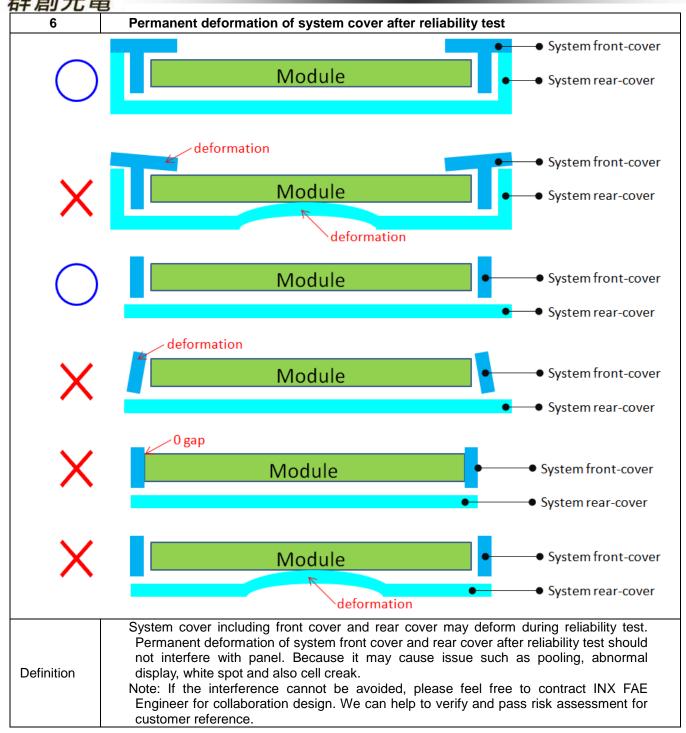






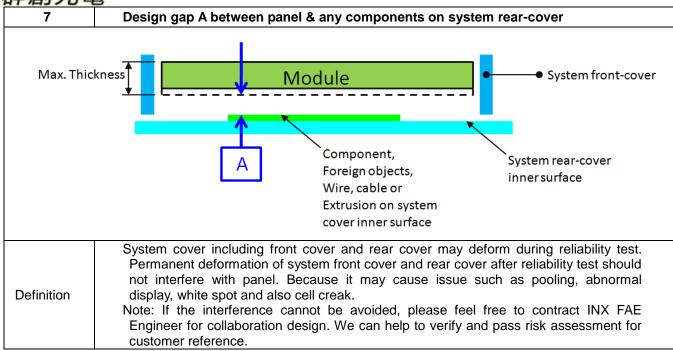
Version 2.1 26 December 2023 37 / 41

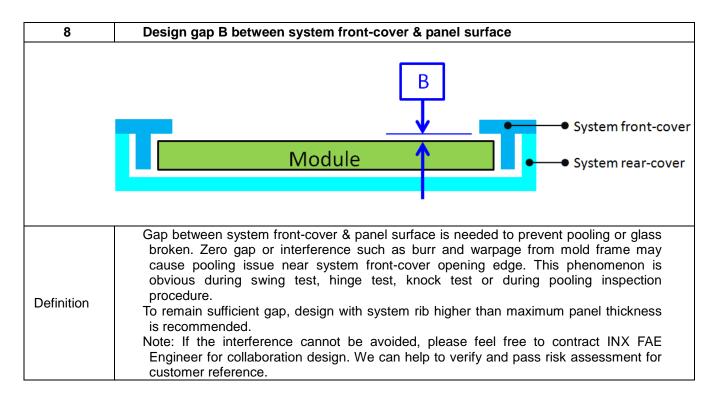




Version 2.1 26 December 2023 38 / 41







Version 2.1 26 December 2023 39 / 41



