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# TITLE: DV320FHM-NN0

# **Product Specification**

Rev. B

# BEIJING BOE DISPLAY TECHNOLOGY

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S8-64-8A-130	TFT-LCD	В	2019.7.11	<sup>1</sup> OF 36
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# **REVISION HISTORY**

REV.	ECN NO.	DESCRIPTION OF CHANGES DATE	PR	REPARED
Rev.P0		Preliminary Specification	Lu	Shunsha
Rev.O		Final Product Specification May.22.20	17 Lu	Shunsha
Rev.A		Add Portrait Usage Suggestion July.11.20	19 G	eng W.B
Rev.B		Change optical data Nov.28.20	19 G	eng W.B
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DGC			TFT-LCD	
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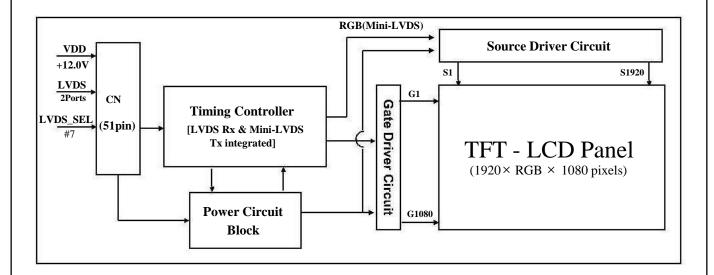
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# **1.0 GENERAL DESCRIPTION**

# 1.1 Introduction

DV320FHM-NN0 is a color active matrix TFT LCD MDL using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This open cell has a 31.51 inch diagonally measured active area with FHD resolutions (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this open cell can display 16.7M colors. The TFT-LCD panel used for this open cell is adapted for a low reflection and higher color type.



## 1.2 Features

- LVDS interface with 2 pixel / clock
- High-speed response
- Low color shift image quality
- 8-bit color depth, display 16.7M colors
- High luminance and contrast ratio, low reflection and wide viewing angle
- DE (Data Enable) only mode
- ADSDS technology is applied for high display quality
- RoHS compliant

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# 1.3 Application

- Commercial Digital Display
- Display Terminals for Control System
- Landscape and Portrait Display

# 1.4 General Specification

## < Table 1. General Specifications >

Parameter	Specification	Unit Remark	
Active area	$698.4(H) \times 392.85(V)$	mm	
Number of pixels	1920(H)×1080(V)	pixels	
Pixel pitch	121.25(H)×RGB×363.75(V)	$\mu { m m}$	
Pixel arrangement	Pixels RGB Vertical stripe		
Display colors	16.7M(8bits-true)	colors	
Display mode	Transmission mode, Normally Black		
Open Cell Transmittance	5.0 (Typ.)	%	At center point with BOE BLU
Weight	4.0(Typ)	Kg	
Power Consumption	39W	Watt	
Surface Treatment	Haze 1%		

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# 2.0 ABSOLUTE MAXIMUM RATINGS

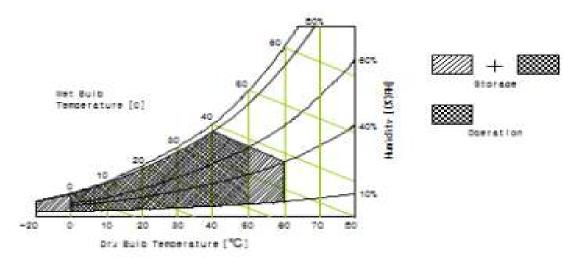
## 2.1 ABSOLUTE RATINGS OF ENVIRONMENT

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

					TA99-040-04	
Parameter	Symbol	Min.	Max.	Unit	Remark	
Power Supply Voltage	VDD	VSS-0.3	14	V	Ta = 25 °C	
Operating Temperature	Top	0	+50	°C	Note 1	
	Tsue	0	+60	°C		
Storage Temperature	T <sub>ST</sub>	-20	+60	°C		
Operating Ambient Humidity	Нор	10	80	%RH		
Storage Humidity	Hst	10	80	%RH		

#### < Table 2. Open Cell Electrical Specifications >

Note 1 : Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max, and no condensation of water.



## 2.2 ABSOLUTE RATINGS OF ENVIRONMENT

When storing modules as spares for a long time, the following precaution is necessary. (a) Do not leave the module in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 20 to 25°Cat and 50±10%RH. (b) The module shall be stored in dark place. Do not store the TFT-LCD module in direct sunlight or fluorescent light.

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2.3 ELECTRICAL ABSOLUTE RATINGS				

### < Table 3. Electrical Absolute Ratings(Open Cell) >

Item	Symbol	N	/alue	Unit	Note
nem	Symbol	Min	Max	Onit	NOLE
Power Supply Voltage	VDD	VSS-0.3	14	V	(1)
Logic Input Voltage	Vin	-0.3	3.6	V	(')

## < Table 4 Backlight Unit >

ltem	Symbol		Value		Unit	Note
nem	Symbol	Min.	Min. Typ Max.	Onit	NOLE	
LED DC forward current	lf	-	540	-	mA	
LED peak pulse	lp	-		-	mA	
LED Reverse voltage	Vr	3.0	3.2	3.4	V	

Note (1)Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under normal operating conditions.

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. <b>0 ELEC1</b> 3.1 TFT LC		DLE < Table 5. Op		trical Sp	ecificat	ions >	p	a =25±2
	Dawa	meter	Combal		Values	ė.	Unit	Barney
	Para	meter	Symbol	Min	Тур	Max	Unit	Remar
Power Su	pply Inpu	ut Voltage	VDD	10.8	12	14	Vdc	12
Power Sup	pply Ripp	ole Voltage	VRP			300	mV	-
Power Sup	pply Cur	rent	IDD	1.22	333	630	mA	Note 1
Power Col	nsumptio	חכ	PDD		4.0	7.6	Watt	
Rush curre	ent	• F. K.	IRUSH	10	-	3.3	Α	Note 2
LVDS	LVDS	Swing Voltage	VID	±100		±300	mV	Note 3
Interface	Comr	non Input Voltage	VLVC	1.0	1.2	1.4	V	
CMOS	Input I Voltag	High Threshold e	VIH	2.7	( <del>2</del> )	3.3	v	
Interface	Input I Voltag	ow Threshold	VIL	0		0.6	v	1
Note 1 - The	-		macified at the	interface	connects			
The Fran Test a) Typ : Mo	supply vo current dr me rate f,4 t Pattern c saic 7X5	Itage is measured and s raw and power consump 60Hz and Clock freque of power supply current (L0/L255) b) Max	action       specified         incy       75.4MH         :       Horizontal       1         G       B       R       G         G       B       R       G       E         G       B       R       G       E         G       B       R       G       E         G       B       R       G       E         G       B       R       G       E         G       B       R       G       E         G       B       R       G       E         G       B       R       G       E         G       B       R       G       E         G       B       R       G       E         G       B       R       G       E         G       B       R       G       E	Line (LO/L R G E R G E	D=12.0V (255))	c) Flic	ker Pallı	em 
The Fran Test a) Typ : Mo	supply vo current dr me rate f,4 t Pattern c saic 7X5	Itage is measured and s raw and power consump 60Hz and Clock freque of power supply current (L0/L255) b) Max (L0/L255) b) Max (L0/L255) b) Max (L0/L255) b) Max (L0/L255) b) Max	ancy       75.4MH         Horizontal 1         G       B       R       G         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G       B         G       B       R       G	Line (LO/L R G E R G E R G E R G E R G E R G E R G E	D=12.0V (255))	or of LCM.	ker Path    	em 



# TFT-LCD

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## **3.2 INTERFACE CONNECTIONS**

## 3.2.1 LCD MODULE

- Connector : IS050-C51B-C39-S (UJU) / FI-RE51S-HF-R1500 (JAE) or Equivalent.

< Table 6. Open Input Connector Pin Configuration >

Pin No	Symbol	Description	Pin No	Symbol	Descr	iption
1	NC	No Connection	21	GND	Gro	und
2	SDA	I <sup>2</sup> C Data	22	CH1[3]-	First pixel ne differential da	gative LVDS ta input. Pair3
3	SCL	I <sup>2</sup> C Clock	23	CH1[3]+	First pixel po differential da	ositive LVDS ta input. Pair3
4	NC	Not Connected	24	NC	Not Co	nnected
5	NC	Not Connected	25	NC	Not Cor	nnected
6	NC	Not Connected	26	NC or GND	Not Cor	nnected
7	SELLVDS	High: JEIDA Low or Open: VESA	27	NC	Not Co	nnected
8	NC	Not Connected	28	CH2[0]-		negative LVDS ta input. Pair0
9	NC	Not Connected	29	CH2[0]+	Second pixel differential da	positive LVDS ta input. Pair0
10	NC	Not Connected	30	CH2[1]-	Second pixel r differential da	egative LVDS ta input. Pair1
11	GND	Ground	31	CH2[1]+	Second pixel differential da	
12	CH1[0]-	First pixel negative LVDS differential data input. Pair0	32	CH2[2]-	Second pixel negative LVDS differential data input. Pair2	
13	CH1[0]+	First pixel positive LVDS differential data input. Pair0	33	CH2[2]+	Second pixel positive LVDS differential data input. Pair2	
14	CH1[1]-	First pixel negative LVDS differential data input. Pair1	34	GND	Gro	und
15	CH1[1]+	First pixel positive LVDS differential data input. Pair1	35	CH2CLK-		negative LVDS lock
16	CH1[2]-	First pixel negative LVDS differential data input. Pair2	36	CH2CLK+	Second pixel positive LVDS clock	
17	CH1[2]+	First pixel positive LVDS differential data input. Pair2	37	GND	Ground	
18	GND	Ground	38	CH2[3]-		negative LVDS ta input. Pair3
19	CH1CLK-	First pixel negative LVDS dock	39	CH2[3]+		positive LVDS ta input. Pair3
20	CH1CLK+	First pixel positive LVDS clock				
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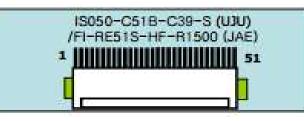
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Pin No	Symbol	Description	Pin No	Symbol	Descr	ription
					Ground	

		T THE SHOULD BE READING THE			
41	NC	Not Connected	47	NC	Not Connected
42	NC or GND	Not Connected	48	VCC	Input Voltage +12V
43	NC or GND	Not Connected	49	VCC	Input Voltage +12V
44	NC or GND	Ground	50	VCC	Input Voltage +12V
45	GND	Ground	51	VCC	Input Voltage +12V

Notes : 1. NC(Not Connected) : This pins are only used for BOE internal operations.

- 2. Input Level of LVDS signal is based on the IEA 664 Standard.
- LVDS\_SEL : This pin is used for selecting LVDS signal data format. If this Pin : High (3.3V) → JEIDA LVDS format Otherwise : Low (GND) or Open (NC) → Normal NS LVDS format

Rear view of LCM



PT1: Block [1.995]	PT1:White (3.9%)	PEL:Red G.sec)	PT1: Grown (5 set)	PTL: blice (1. sec)

**BIST Pattern** 

## 3.3 BACKLIGHT UNIT

3.3.1 LED LIGHTBAR UNIT CHARACTERISTICS (Ta = 25  $\pm$  2 °C)

## < Table 7. LED Lightbar Unit Characteristics>

Min.Typ.Max.Lightbar input VoltageV606468 $V_{RMS}$ One chLightbar input currentI_L520540560mAOne chPower consumeW31.234.5638.08WOne ch	Symbol Valu	е	Unit	Note
Lightbar input currentIL520540560mAOne chPower consumeW31.234.5638.08WOne chLightbar Life TimeLightbar Life TimeLightbar Life TimeLightbar Life TimeLightbar Life Time		. Max.	Onit	NOLE
Power consume     W     31.2     34.56     38.08     W     One ch	it Voltage V 60 64	68	V <sub>RMS</sub>	One channel
	it current I <sub>L</sub> 520 540	560	mA	One channel
Lighthar Life Time I 30,000 - Hrs	ime W 31.2 34.5	6 38.08	W	One channel
$Lightbar Life finite L_{BL} = 50,000 $	Time L <sub>BL</sub> 30,000 -	-	Hrs	
	Time L <sub>BL</sub> 30,000 -	-	Hrs	

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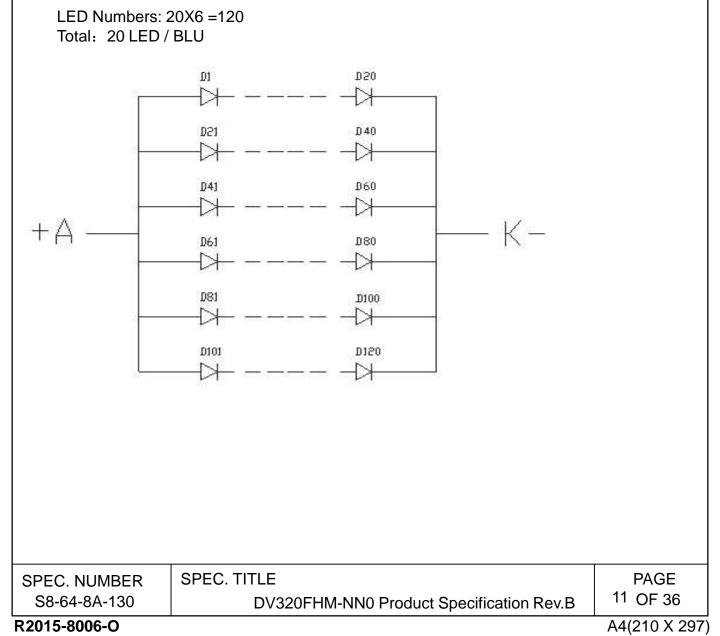
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Note (1) The lifetime is defined as the time which luminance of the LED decays to 50% compared to the initial value, Operating condition: Continuous operating at Ta =  $25\pm2^{\circ}$ C , IL=540mA

The pin configuration for the housing and leader wire is shown in the table below. Light bar connector type: CI0102S0000-A, PITCH=2mm

Pin No.	Symbol	Description	Wire Color
1	+	CH+	Red
2	-	CH-	WHITE



## 3.4 LED Constant current source LED

3.4.1 Input Electrical Characteristics

## < Table 8 Input Electrical Characteristics >

NO	ltem	Symbol	Min	Туре	Max	Unit					
1	Input Voltage	Vin	20.8	24	26	V					
2	Input Current	lin		0.7	2	А					
3	Input Power	Pin		36	52	W					
4	Brightness Voltage	Vadj	0(bright)		5 (dark)	V					
5	Control Voltage	trol Voltage Enable Von=1.55.0V Disable Voff=00.5V									

## 3.4.2 Out Electrical Characteristics

## < Table 9 Output Electrical Characteristics >

Item	Symbol	Test Conditions	Min	Туре	Max	Unit			
Output Current (per group)	lout	Vin=24.0V; Vout=57V; Ta=28°C							
Output Voltage	Vout	Vin=24.0V; Ta=28°C	V						
Efficiency	η	Vin=24.0V; Vout=57V;			%				
Output otal group	Ggp			1					
The Total Output Current	R	According to the backlight parameters to adjust output current	ght ers to 540 utput		750	MA			
output power	W out	Vin=24.0V; Vout=30-85V;		36 63.75					
The parameter o	f upon wil	I change when the LCE	) module c	hanges					
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## 3.4.3 Pin Assignments

< Table 10 Pin Assignments>

The pin configuration for the housing and leader wire is shown in the table below. CN1 connector type: CI0114S0000, PITCH=2mm

Input connector: CN1											
Pin No.	Symbol	Description	Parameter								
1、2、3、4、5	+24V	Supply voltage	22~26V								
6、7、8、9、10	GND	Ground	0V								
13	ADJ	Dimming control	0V=Brightness Max 5V=Brightness Min								
12	N/F	Standby/Operation	On =1-5.0V Off=0- 0.5V								
14、11	NC										

Output Socket: CN3											
Pin No.	Pin No. Symbol Description Parameter										
1	OUT+	Output voltage	30-85V								
2	OUT-										

The above output parameters are determined according to the optical requirement. The products are not intended for use in systems in which failures of product could result in personal injury.

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## **3.5 Signal Timing Specifications**

Timing Parameters (DE only mode)

	ltem	Symt	ols	Min	Тур	Max	Unit	
	Frequency	l/Te		58	74.25 (92.8)	97	MHz	
Clock High Time		Tel	h	ар С	4/7Te			
	Low Time	Tc	I.	÷.	4/7Tc	÷.		
Frame Period				1100	1125	1149	lines	
E	ane renou	T		47	60 (75)	78	Hz	
Hori	zontal Active	Valid	Ļ <sub>HV</sub>		960		I <sub>CLK</sub>	
Di	isplay Term	Total	ι, <sub>HP</sub>	1060	1100	1200	t <sub>cl.K</sub>	
Vertical Active Display Term		Valid	$\mathbf{t}_{VV}$	3	1080	<b>7</b> 2	t <sub>HP</sub>	
		Total	I <sub>VP</sub>	1100	1125	1149	t <sub>HP</sub>	

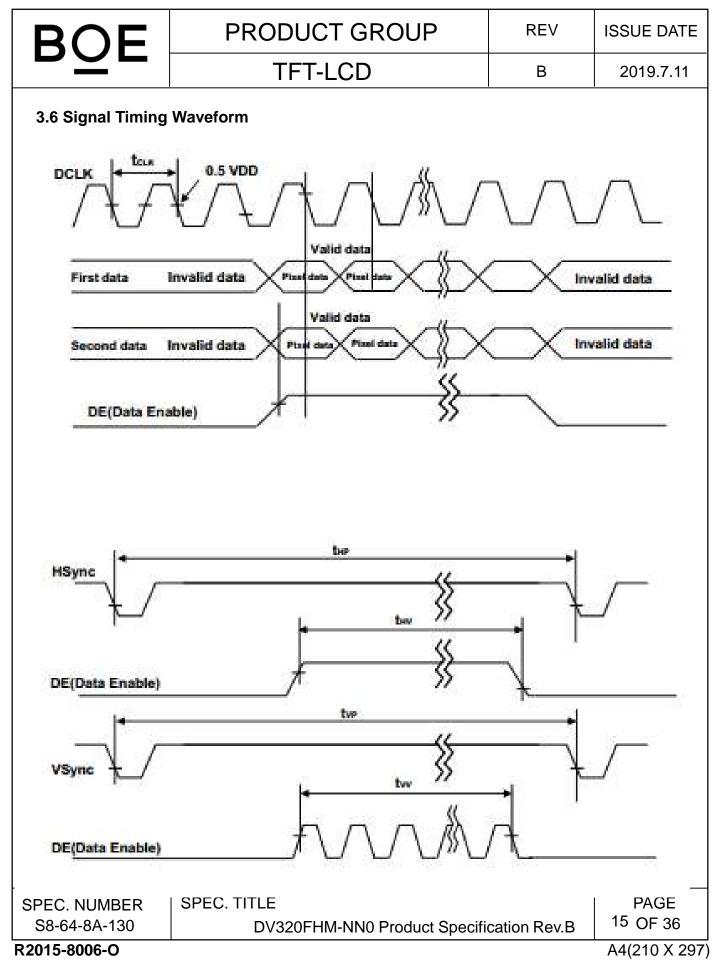
Notes: This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

< Table 13 LVDS Input SSCG>

Symbol	1	Parameter	Condition	Min	Тур	Max	Unit	
F	LVDS In	put frequency	3 <del>4</del>	58	74.25 (92.8)	97	MHz	
T <sub>lvsk</sub>	LVDS ch skew	S channel to channel VIC=1.2V VID=±200mV				(1/F)*3 0%	ps	
FLVMOD		ing frequency of inp during SSC	ng SSC F=85MHz			200	KHz	
FLYDEV		n deviation of input quency during SSC	V <sub>IC</sub> =1.2V V <sub>ID</sub> =±200mV	-3	4	+3	%	
T <sub>CY-CY</sub>	Cycle to	Cycle jitter		- 22	24	50	ps	

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# 3.7 COLOR DATA INPUT ASSIGNMENT

Input Signals, Basic Display Colors and Gray Scale of Colors

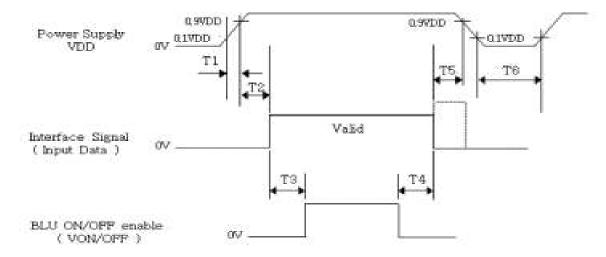
< Table 14 Input Signal and Display Color Table >

			Input Data Signal																							
Color & G	ray Scale	e			R	ed	Da	ta			Green Data						Blue Data									
			<b>R7</b>	R6	R5	R4	R3	<b>R</b> 2	R1	RO	G7	G6	G5	G4	G3	G2	G1	GO	<b>B</b> 7	<b>B6</b>	-	_	<b>B</b> 3	_	<b>B1</b>	<b>B</b> 0
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	-	1	1	1
	Green	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Colors	Red		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magent		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-	1	1	1	1	1	1	1	1
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Δ		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darke	r	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale																										_
of Red	V					_				_			_	_		_	_					_		_	_	
	Brighte	r	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	▼		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	Δ		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Darke	r	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
of Green	Δ		1								1						1									
	₽		_		_			_	_	_	+						_	_	_	_		_	_	_		
	Brighte	r	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	▼		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	1
Gray Scale	Darke	r		U	U	<u> </u>	<u> </u>	U	U	U	v	U		<u> </u>	U	v	U	v	•	U	U	0	<u> </u>	U.		
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of Blue			0	0	0	0	_		0	0	0	0				~	0	0	4	4	-	4		4	0	-4
	Brighte	ar	0	0 0	0	0	0	0		0	0	0	0	0	0	0	0	0	1			H	1	-	1	1
	Blue		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1				-	-	1	1
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Ó	0	0	0	0	0	0
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Gray Scale			~		<u> </u>	<u> </u>		-		4	~	~		<u> </u>		<u> </u>	-	- <del>-</del>	~		<u> </u>				-	
of White														_												_
	Brighte	r	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	Dingric		1	T.	1	1	1	1	1	Ō	1	1	-	1	1	1	1	Ō	1	i	1	Ť	1	1	1	0
	White		1	1	1	1	1	1	1	1	1	1	i	1	1	1	1	1	1	1	1	i	-	1	1	1
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#### **3.8 POWER SEQUENCE**

To prevent a latch-up or DC operation of the Open Cell, the power on/off sequence shall be as shown in below.



< Table 15 Sequence Table >

Bassantas	Values			
Parameter	Min	Тур	Max	Units
T1	0.5		20	ms
T2	10	6 <del></del>	-	ms
T3	200	-	<u> </u>	ms
T4	100	i s <del>e</del> s i	-	ms
T5	0		<u> </u>	ms
T6	18	1	-	S

Notes: 1. Back Light must be turn on after power for logic and interface signal are valid.

Even though T1 is out of SPEC, it is still ok if the inrush current of VDD is below the limit.

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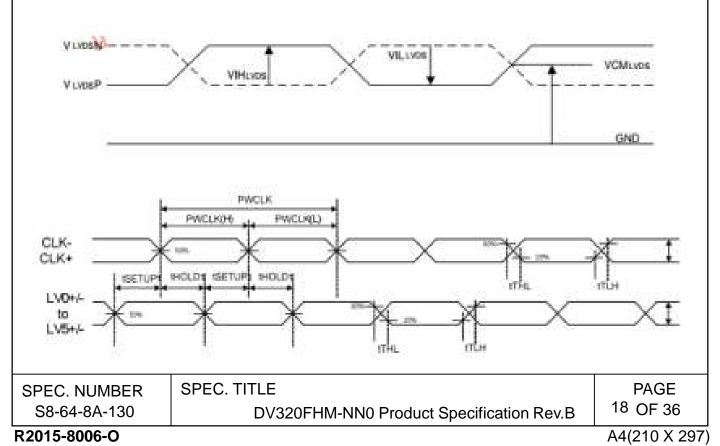
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## 3. 9 MINI-LVDS SIGNAL SPECIFICATIONS

< Table 16 Timing Table >

Symbol	Parameter	Min	Тур	Max	Unit
FM	Mini-LVDS Clock frequency	(3 <del>3</del> )?		400	MHz
FMLVMOD	Modulating frequency of input clo ck during SSC	3 <b>3</b> 2	÷	600	KHz
FMLVDEV	Maximum deviation of input clock frequency during SSC	-3	,	+3	%
VIHLVDS	Mini-LVDS high input voltage	200	-		mV
VILLUDS	Mini-LVDS high input voltage	373	E	-200	mV
ISETUP	Data setup time	0.5	<u> </u>	<u> </u>	ns
HOLD	Data hold time	0.5	<u> </u>	-	ns



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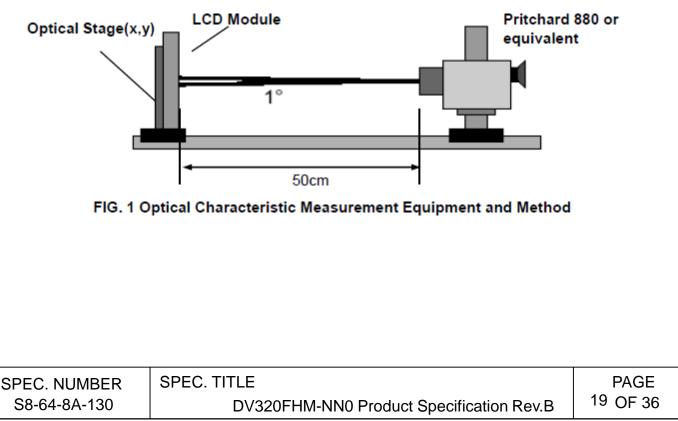
# 4.0 OPTICAL CHARACTERISTICS

## 4.1 Test Conditions

ltem	Symbol	Value	Unit		
Ambient Temperature	Та	25±2	°C		
Ambient Humidity	На	50±10	%RH		
Supply Voltage	V <sub>cc</sub>	-	V		
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"				
Lightbar Current	١	240	mA		
Light bar operation voltage	V	49.5	V		

# 4.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown as below. The following items should be measured under the test conditions described in 4.1 and stable environment shown in Note (5).



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< '	Table	17	Optical	Specifications>
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ltem		Syn	nbol	Conditi on	Min.	Тур.	Max.	Unit	Note
Contrast Ratio	)	С	R		900	1200	_	_	(2)
Response Tim	ie	Gray t	o gray			8	10	ms	(3)
Center Lumina White	ance of	L	С		350	400			(4)
White Variatio	n	δΙ	N			-	1.38	-	(6)
		R	X	θ <sub>x</sub> =0°,		0.630		—	
	Red	R	y	θ <sub>Y</sub> =0° Viewin		0.344			
		G	х	g		0.302	Typ. +0.04	_	
	Green	G	iy	Angle at	Typ.	0.642			(5)
Color	Blue	В	х	Normal Directi		0.153			
Chromaticity	Diue	Ву		on		0.046			
		V	Wx			0.280		_	
	White	W	/y			0.290			
	Color Gamut	С	G			72		%	NTSC Ratio
	2D	Horizon	$\Theta_3$			89			
Viewing	Viewing (CR>1	ta ta	Θ <sub>9</sub>	CR≥10		89	_	Deg.	(1)
Angle		Vertical	Θ <sub>12</sub>			89	_	Dey.	(י)
			$\Theta_6$			89	_		

Note (1) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

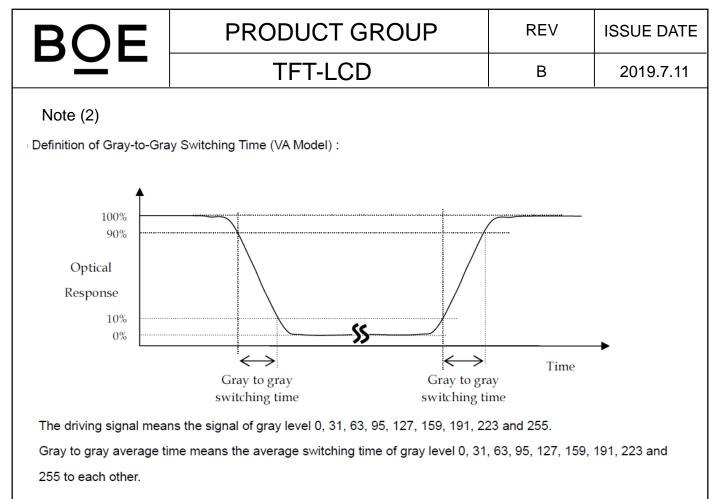
Contrast Ratio (CR) = L255 / L0

L255: Luminance of gray level 255

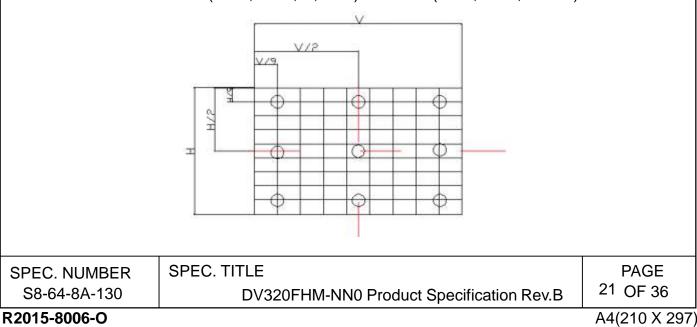
L 0: Luminance of gray level 0

CR = CR (5), where CR (X) is corresponding to the Contrast Ratio of the point X at the figure in note(6)

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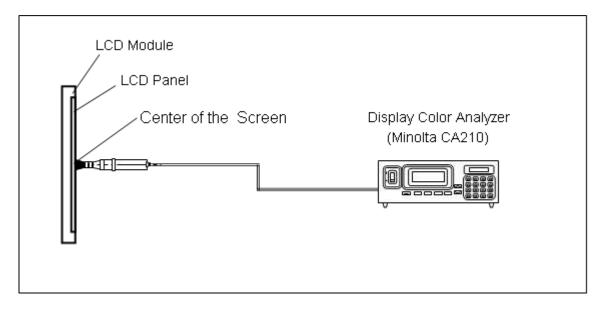
- Note (3) Definition of Luminance of White  $(L_c)$ : Measure the luminance of gray level 255 at center point and 9 points  $L_c = L$  (5), where L (X) is corresponding to the luminance of the point X at the figure in Note (6)
- Note (4) Definition of White Variation ( $\delta W$ ) : Measure the luminance of gray level 255 at 9 points  $\delta W$  = Maximum(Lon1, Lon2,...,Lon9)/ Minimum(Lon1, Lon2,...Lon9)



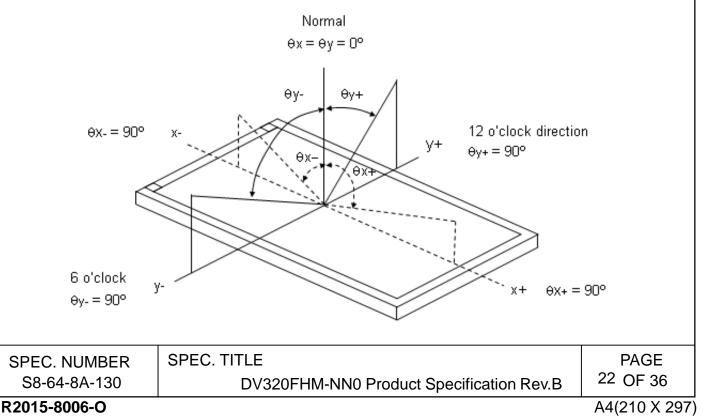
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Note (5) Measurement Setup:

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a windless room.



Note (6) Definition of Viewing Angle ( $\theta x, \theta y$ ): Viewing angle are measured by CS-2000.



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## **5.0 MECHANICAL SPECIFICATIONS**

**5.1 Dimensional Requirements** 

Figure 1(located in Appendix) shows mechanical outlines for the model DV320FHM-NN0. Other parameters are shown in Table 18.

< Table 18. Dimensional Parameters >

Parameter	Specification	Unit
Active area	698.4 (H) $ imes$ 392.85(V)	mm
Pixel pitch	121.25 (H) $ imes$ 363.75 (V)	μm
Number of pixels	1920(H) $\times$ 1080(V) (1 pixel = R + G + B dots)	pixels
Weight	850	gram

## 5.2 Semi-Glare and Polarizer Hardness

The surface of the LCD has an Anti-glare coating to minimize reflection and a coating to Reduce scratching.

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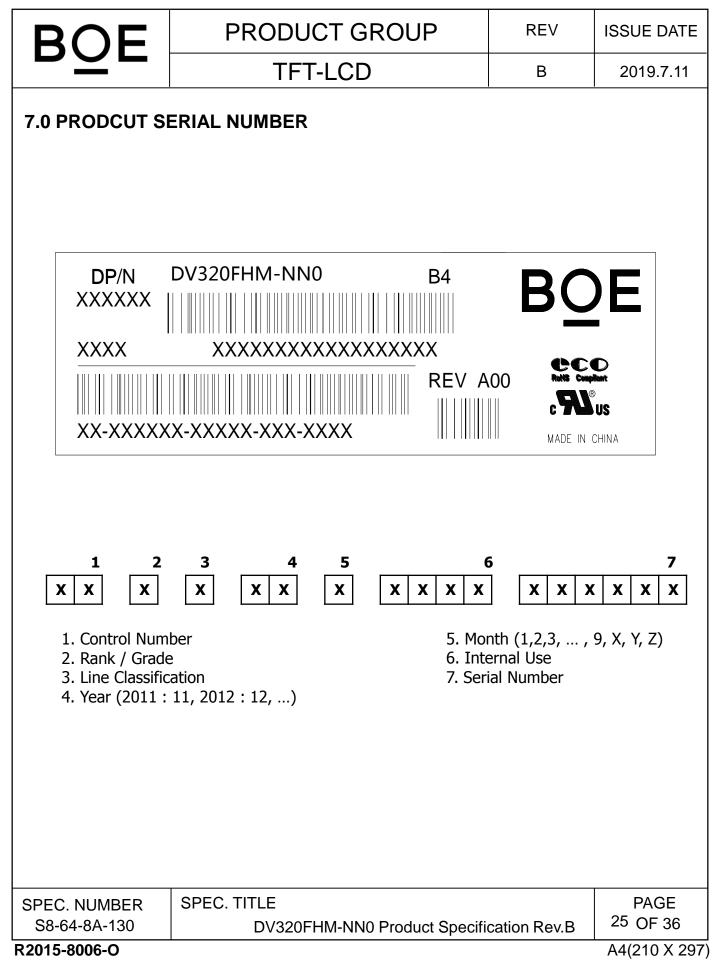
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# **6.0 RELIABILITY**

The Reliability test items and its conditions are shown in below.

< Table 19. Reliability Test Parameters >

No	Т	est Items	Conditions	
1	High tempe humidity ste	rature & high orage test	Ta = 60 °C, 90%RH, 240 hrs	
2	Low temper	ature storage test	Ta = -20 °C, 240 hrs	
3	High tempe humidity op	rature & high eration test	Ta = 50 °C, 80%RH, 240hrs	
4	Low temper	ature operation test	Ta = 0 °C, 240hrs	
5	Thermal sho	ock test	Ta = -20 °C $\leftrightarrow$ 60 °C (0.5 hr), 100 cyc	cle
6	On/off test		On/Off:10sec(on) / 5sec(off), 30000	times
7	Altitude Tes (non-operat		40000ft -10 °C /24hrs ,25°C /24hrs, -10 °C /24hrsFrequency : 10 ~ 300 Hz, RandomGravity / AMP : 1.0 GrmsPeriod : X, Y, Z 30 min/axisGravity : 50GPulse width : 11msec, Sine wave $\pm X, \pm Y, \pm Z$ Once for each directionAir : $\pm 15kV$ ,150pF/330 $\Omega$ ,100Point ,1time/PointContact : $\pm 8kV$ ,150pF/330 $\Omega$ ,100Point ,1time/PointNon operationContact: $\pm 4KV \sim \pm 6KV$ ,150pF/330 $\Omega$ ,100Point, Input connector Pin, 3 times/pin with no function	
8	Vibration tes (non-operat			
9	Shock test (non-operat	ing)		
10	Electro-stati	c discharge test		
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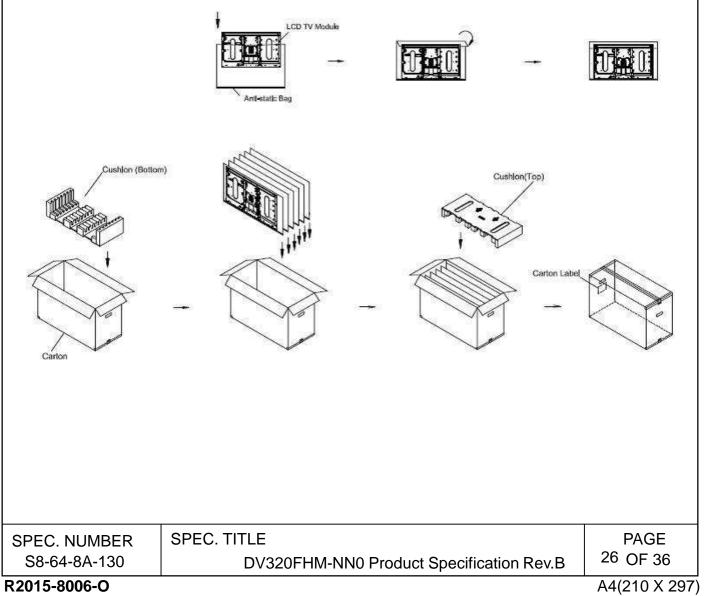
# **8.0 PACKING INFORMATION**

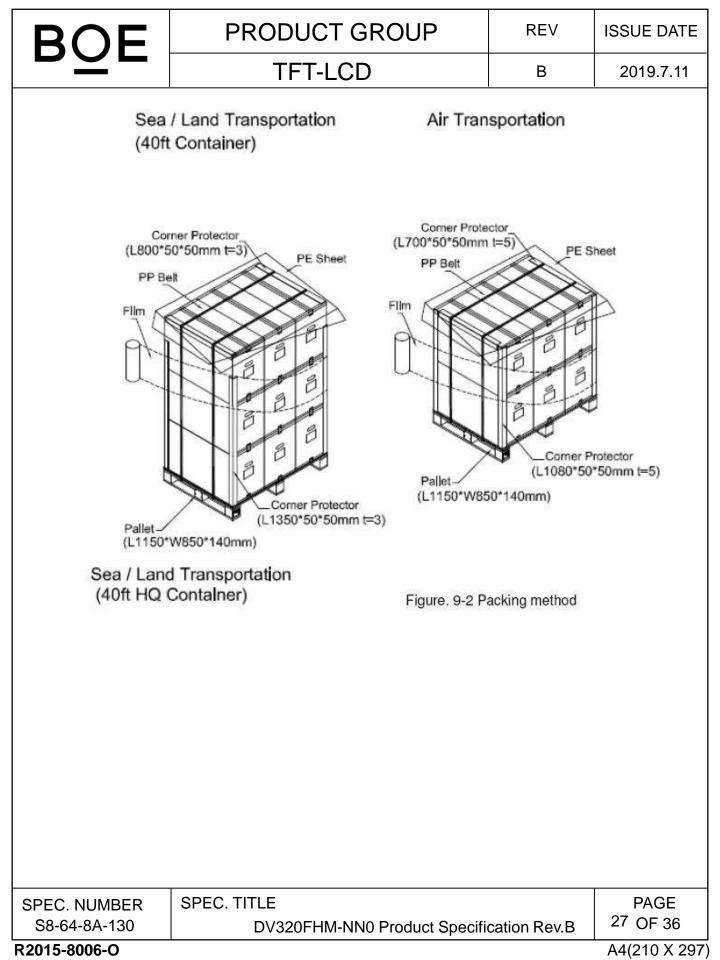
BOE provides the standard shipping container for customers, unless customer specifies their packing information. The standard packing method and Barcode information are shown in below.

## 8.1 Packing Specifications

- (1) 6LCD TV modules / 1 Box
- (2) Box dimensions: 820(L) x375(W) x520(H)mm
- (3) Weight: approximately 36Kg (6 modules per box)

## 8.2 Packing Method

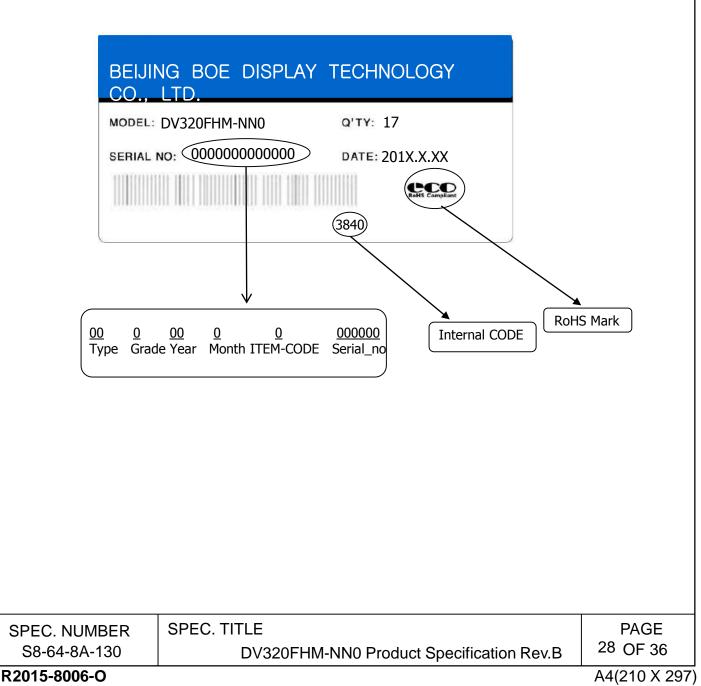




## 8.3 Box Label

- Label Size : 110 mm (L)  $\times$  55 mm (W)
- Contents

Model : DV320FHM-NN0 Q`ty : 6Pcs Serial No. : Box Serial No. See next page for detail description. Date : Packing Date FG Code : FG Code of Product



# 9.0 HANDING & CAUTIONS

Please pay attention to the followings when you use this TFT LCD Module.

- 9.1 Mounting Precautions
- Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- You must mount a module using specified mounting holes (Details refer to the drawings)
- You should consider the mounting structure so that uneven force (ex. Twisted stress, Concentrated stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- Do not apply mechanical stress or static pressure on module; Abnormal display cause by pressing some parts of module during assembly process, do not belong to product failure, the press should be agreed by two sides.
- Determine the optimum mounting angle, refer to the viewing angle range in the ٠ specification for each model.
- Do not apply mechanical stress or static pressure on module, and avoid impact, vibration and falling.
- Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- Protection film for polarizer on the module should be slowly peeled off before display.
- Be careful to prevent water & chemicals contact the module surface.
- You should adopt radiation structure to satisfy the temperature specification.
- Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane & alcohol is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene, because they cause chemical damage to the polarizer.
- Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading...

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- This module has its circuitry PCB's on the rear side and Driver IC, should be handled carefully in order not to be stressed.
- Avoid impose stress on PCB and Driver IC during assembly process ,Do not drawing, bending, COF package & wire
- Do not disassemble the module.

# 9.2 Operating Precautions

- Do not connector or disconnect the cable to/from the Module at the "Power On" Condition.
- When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the module would be damaged.
- Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- Do not allow to adjust the adjustable resistance or switch
- The electrochemical reaction caused by DC voltage will lead to LCD module degradation, so DC drive should be avoided.
- The LCD modules use C-MOS LSI drivers, so customers are recommended that any unused input terminal would be connected to Vdd or Vss, do not input any signals before power is turn on, and ground you body, work/assembly area, assembly equipment to protect against static electricity.
- Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module may be damaged.
- Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- Design the length of cable to connect between the connector for back-light and the converter as shorter as possible and the shorter cable shall be connected directly, The long cable between back-light and Converter may cause the Luminance of LED to lower and need a higher startup voltage
- The cables should be as short as possible between System Board and PCB interface.
- Connectors are precision devices to transmit electrical signals, and operators should plug in parallel
- Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.

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# 9.3 Electrostatic Discharge Precautions

- Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.
- Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc.
- Do not close to static electricity to avoid product damage.
- Do not touch interface pin directly.

# 9.4 Precautions for Strong Light Exposure

• Do not leave the module operation or storage in Strong light . Strong light exposure causes degradation of polarizer and color filter.

# 9.5 Precautions for Storage

## A. Atmosphere Requirement

ITEM	UNIT	MIN	MAX
Storage Temperature	(°C)	5	40
Storage Humidity	(%rH)	40	75
Storage Life	6 months		
Storage Condition	<ul> <li>The storage room should be equipped with a dark and good ventilation facility.</li> <li>Prevent products from being exposed to the direct sunlight, moisture and water.</li> <li>The product need to keep away from organic solvent and corrosive gas.</li> <li>Be careful for condensation at sudden temperature change.</li> <li>Storage condition is guaranteed under packing conditions.</li> </ul>		

## B. Package Requirement

- The product should be placed in a sealed polythene bag.
- Product Should be placed on the pallet, Which is away from the floor, Be cautions not to pile the product up.
- The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.
- As the original protective film, do not use the adhesive protective film to avoid change of Pol color and characteristic.

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# 9.6 Precautions for protection film

- Remove the protective film slowly, keeping the removing direction approximate 30-degree not vertical from panel surface, If possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.
- People who peeled off the protection film should wear anti-static strap and grounded well.

# 9.7 Appropriate Condition for Commercial Display

-Generally large-sized LCD modules are designed for consumer applications . Accordingly, long-term display like in Commercial Display application, can cause uneven display including image sticking. To optimize module's lifetime and function, several operating usages are required.

1. Normal operating condition

- Temperature: 20±15℃
- Operating Ambient Humidity :  $55\pm20\%$
- Display pattern: dynamic pattern (Real display)
- Well-ventilated place is recommended to set up Commercial Display system
- 2. Special operating condition
  - a. Ambient condition
  - Well-ventilated place is recommended to set up Commercial Display system.
  - b. Power and screen save
  - Periodical power-off or screen save is needed after long-term display.

c. As the low temperature, the response time is greatly delayed. As the high temperatures (higher than the operating temperature) the LCD module may turn black screen. The above phenomenon cannot explain the failure of the display. When the temperature returns to the normal operating temperature, the LCD module will return to normal display.

d. When expose to drastic fluctuation of temperature (hot to cold or cold to hot) ,the LCD module may be affected; Specifically, drastic temperature fluctuation from cold to hot ,produces dew on the LCD module 's surface which may affect the operation of the polarizer and LCD module e. Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module may be damaged.

f. Products exposed to low temperature environment for a long time, need to carry out necessary protection, low temperature environment is usually refrigerators, vending machine Etc...

g. Long time and large angle forword use or unconventional use, It is strongly recommended to contact BOE for filed application engineering advice

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f. Product reliability and functions are only guaranteed when the product is used under right operation usages. If product will be used in extreme conditions such as high temperature, high humidity, high altitude, special display images, running time, long time operation, outdoor operation, etc. It is strongly recommended to contact BOE for filed application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at airports, transit stations, banks, stock market and controlling systems.

3. Operating usages to protect against image sticking due to long-term static display.

- a. Suitable operating time: under 20 hours a day.
- b. Static information display recommended to use with moving image.
- Cycling display between 5 minutes' information(static) display and 10 seconds' moving image.
- c. Background and character (image) color change
- Use different colors for background and character, respectively.
- Change colors themselves periodically.
- d. Avoid combination of background and character with large different luminance.
- 1) Abnormal condition just means conditions except normal condition.
- 2) Black image or moving image is strongly recommended as a screen save
- 4. Lifetime in this spec. is guaranteed only when Commercial Display is used according to operating usages.
- 5. Module should be turned clockwise based on front view when used in portrait mode.
- Landscape Mode

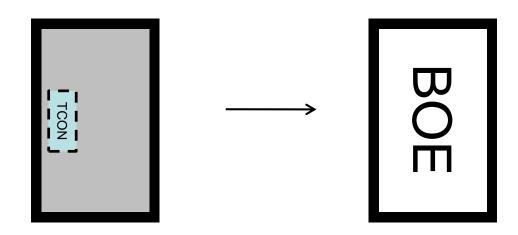
The default placement is TCON side on the lower side and the image is shown upright via viewing from the front.

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## • Portrait Mode

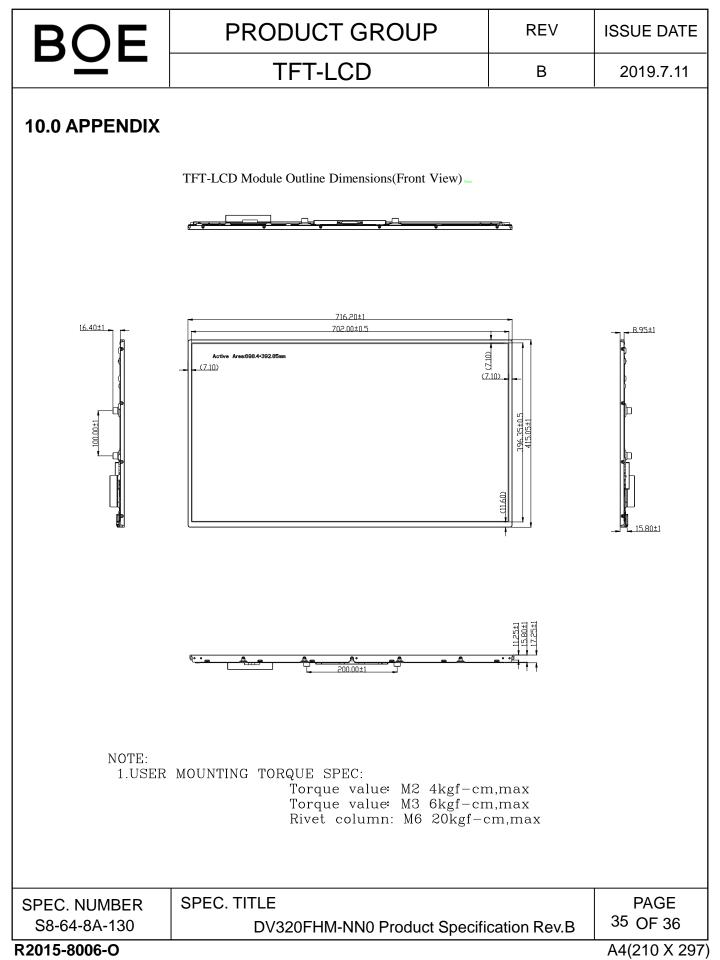
The default placement is that TCON side has to be placed on the left side via viewing from the front



# 9.8 Other Precautions

- A. LC Leak
- If the liquid crystal material leaks from the panel, it is recommended to wash the LC with acetone or ethanol and then burn it.
- If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- If LC in mouth, mouth need to be washed, drink plenty of water to induce vomiting and follow medical advice.
- If LC touch eyes, eyes need to be washed with running water at least 15 minutes.
- B. Rework
- When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

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O 10-M2.0 Locking depth max2.0 effective thread depth 1	TFT-LCD Module Outline Dimensions(RearView)	C 0 5-M2.0 Locking depth max2.0 effect thread depth 1.8 C 0 8-M3.0 Locking depth max5. effective thread depth 1.8 C 0	
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