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DV430FHM-NN1 Product Specification Rev. B

Fuzhou BOE Optoelectronics CO., LTD

| SPEC. NUMBER | 1 | ISSUE DATE | PAGE | |
|--------------|---------|------------|------------|---------|
| S8-65-8D-082 | TFT-LCD | Rev. B | 2020/09/14 | 1 OF 39 |

DAS-RD-2019028-O A4(210 X 297)



REV

ISSUE DATE

Customer SPEC

Rev. B

2020/09/14

REVISION HISTORY

)preliminary specification

 $(\sqrt{)}$ Final specification

| Revision No. | Page | Description of changes | Date | Prepared |
|--------------|-------|-------------------------------------|------------|----------|
| 0 | 1 | Initial Release | 2019/05/28 | Weng Z.W |
| А | 36&37 | Add portrait usage suggestion | 2019/07/11 | Geng W.B |
| В | 5&38 | Thickness adjusted 2D draft updated | 2020/09/14 | Han. J |

| SPEC. NUMBER |
|--------------|
| S8-65-8D-082 |



REV

ISSUE DATE

Customer SPEC

Rev. B

2020/09/14

Contents

| No | ITEM | Page |
|----------|--|------|
| | REVISIONS HISTORY | 2 |
| | CONTENTS | 3 |
| 1 | GENERAL DESCRIPTION | 4 |
| | 1.1 Introduction | |
| | 1.2 Features | |
| | 1.3 Application | |
| | 1.4 General Specification | |
| 2 | ABSOLUTE MAXIMUM RATINGS | 6 |
| 3 | ELECTRICAL SPECIFICATIONS | 7 |
| | 3.1 TFT LCD Open Cell | |
| | 3.2 LED Constant current source LED | |
| 4 | INTERFACE CONNECTION | 12 |
| | 4.1 Open Cell Input Signal & Power | |
| | 4.2 LVDS Interface | |
| | 4.3 LVDS Rx Interface Eye Diagram | |
| | 4.4 LVDS Receiver Differential Input | |
| 5 | SIGNAL TIMING SPECIFICATIONS | 17 |
| | 5.1 Timing Parameters (DE only mode) | |
| | 5.2 Signal Timing Waveform | |
| | 5.3 Input Signals, Basic Display Colors and Gray Scale of Colors | |
| | 5.4 Power Sequence | |
| 6 | OPTICAL SPECIFICATIONS | 21 |
| 7 | MECHANICAL CHARACTERISTICS | 25 |
| 8 | RELIABILITY TEST | 26 |
| 9 | DEFINITION OF LABELS | 27 |
| 10 | PACKING INFORMATION | 28 |
| 11 | PRECAUTIONS | 32 |
| 12 | APPENDIX | 38 |
| PEC. NUN | MBER SPEC. TITLE | PAGE |

S8-65-8D-082 DAS-RD-2019028-O SPEC. TITLE

DV430FHM-NN1 Product Specification Rev.B

3 OF 39 A4(210 X 297)

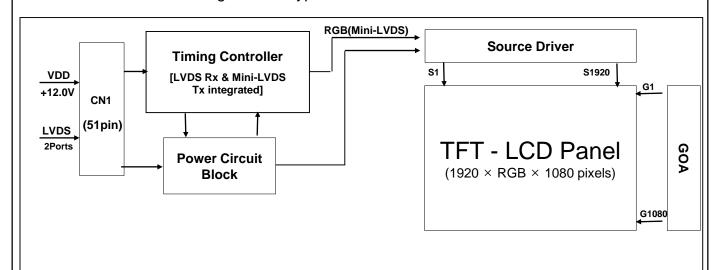


| PRODUCT GROUP | REV | ISSUE DATE |
|---------------|--------|------------|
| Customer SPEC | Rev. B | 2020/09/14 |

1.0 GENERAL DESCRIPTION

1.1 Introduction

DV430FHM-NN1 is a color active matrix TFT LCD MDL using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This MDL has a 42.5 inch diagonally measured active area with FHD resolutions (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.7M colors. The TFT-LCD panel is adapted for a low reflection and higher color type.



1.2 Features

- LVDS interface with 2 pixel / clock
- High-speed response
- Low color shift image quality
- 8-bit color depth, display 16.7M colors
- Wide viewing angle
- DE (Data Enable) only mode
- ADS technology is applied for high display quality
- RoHS compliant

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|---------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 4 OF 39 |

| D | 0 | |
|---|---|--|
| D | V | |
| | - | |

| PRODUCT GROUP | REV | ISSUE DATE |
|---------------|--------|------------|
| Customer SPEC | Rev. B | 2020/09/14 |

1.3 Application

- Commercial Digital Display
- Display Terminals for Control System
- Landscape and Portrait Display

1.4 General Specification

< Table 1. General Specifications >

| Parameter | Specification | Unit | Remarks |
|-----------------------|---|--------|-------------------------|
| Active area | 940.896(H) × 529.254(V) | mm | |
| Number of pixels | 1920(H) × 1080(V) | pixels | |
| Pixel pitch | $163.35(H) \times 490.05(V)$ | um | |
| Pixel arrangement | Pixels RGB Vertical stripe | | |
| Display colors | 16.7M | colors | 8bits True |
| Display mode | Normally Black | | |
| Dimensional outline | $962.1(H) \times 550.5(V) \times 12.9(B)$ | mm | Detail refer to drawing |
| Weight | 8.36(Typ) | Kg | |
| Power Consumption | LED Driver:56.3 | Watt | Тур. |
| Surface Treatment | Haze 1% | | |
| Back-light | E-LED Backlight | _ | |
| Possible display type | Landscape and Portrait Enabled | | |

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|---------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 5 OF 39 |



| PRODUCT GROUP | REV | ISSUE DATE |
|---------------|-------|------------|
| Customer SPEC | Roy R | 2020/09/14 |

2.0 ABSOLUTE MAXIMUM RATINGS

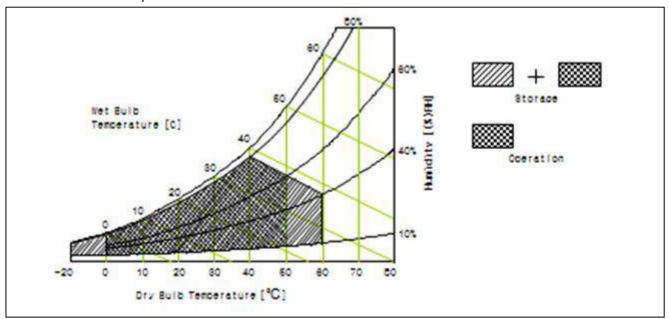
The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Open Cell Electrical Specifications >

[VSS=GND=0V]

| Parameter | Symbol | Min. | Max. | Unit | Remark |
|-------------------------------|------------------|---------|------|------|-----------|
| Power Supply Voltage | VDD | VSS-0.3 | 13.5 | V | Ta = 25 ℃ |
| Operating Temperature | T _{OP} | 0 | +50 | °C | |
| Storage Temperature | T _{SUR} | -20 | +60 | °C | |
| | T _{ST} | -20 | +60 | °C | Note 1 |
| Operating Ambient Humidity | Нор | 10 | 80 | %RH | |
| Storage Humidity | Hst | 10 | 80 | %RH | |

Note 1 : Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water.



SPEC. NUMBER S8-65-8D-082

SPEC. TITLE
DV430FHM-NN1 Product Specification Rev.B

PAGE 6 OF 39



REV

ISSUE DATE

Customer SPEC

Rev. B

2020/09/14

3.0 ELECTRICAL SPECIFICATIONS

3.1 TFT LCD Open Cell

< Table 3. Open Cell Electrical Specifications >

[Ta =25 ± 2 °C]

| Parameter | | Symbol | Values | | | l lm:4 | Damark |
|-------------------|--|--------|--------|-----|------|--------|--------|
| | | | Min | Тур | Max | Unit | Remark |
| Power Sup | ply Input Voltage | VDD | 10.8 | 12 | 13.2 | Vdc | |
| Power Sup | ply Ripple Voltage | VRP | - | - | 300 | mV | |
| Power Sup | ply Current | IDD | - | 500 | 950 | mA | Note 1 |
| Power Con | sumption | PDD | - | 6 | 11.4 | Watt | Note i |
| Rush curre | nt | IRUSH | - | | 3.0 | Α | Note 2 |
| | Differential Input High Threshold Voltage | VLVTH | +100 | - | +300 | mV | |
| LVDS Interface | Differential Input Low Threshold Voltage | VLVTL | -300 | - | -100 | mV | |
| interrace | Common Input Voltage | VLVC | 1.0 | 1.2 | 1.4 | V | |
| CMOS | Input High Threshold Voltage | VIH | 2.7 | - | 3.3 | | |
| Interface | Input Low Threshold Voltage | VIL | 0 | - | 0.6 | V | |

Note 1: The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for VDD=12.0V,

Frame rate f_V =60Hz and Clock frequency = 74.25MHz.

Test Pattern of power supply current

a) Typ: Mosaic 7X5 (L0/L255)

b) Max : Horizontal 1 Line (L0/L255)

R G B R G B R G B

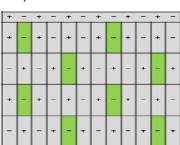
R G B R G B R G B

R G B R G B R G B R G B R G B R G B R G B R G B R G B R G B

R G B R G B R G B R G B

R G B R G B R G B

c) Flicker Pattern



Note 2: The duration of rush current is about 2ms and rising time of Power Input is 1ms(min)

| | . , | |
|--------------|--|---------|
| SPEC. NUMBER | SPEC. TITLE | PAGE |
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 7 OF 39 |



PRODUCT GROUP REV

Customer SPEC

Rev. B

2020/09/14

ISSUE DATE

3.2 LED Constant current source LED

3.2.1 Input Electrical Characteristics

| 输入电压 Input voltage | 22Vdc to 26Vdc | |
|--------------------|---------------------------------------|--|
| 输入电流 Input current | Max.3.5A at 24Vdc input and full load | |

3.2.2 Output Electrical Characteristics

(LED DRIVER(DC/DC)ELECTRICAL REQUIREMENTS)

输出功率(Output Power)

| 输出功率 | Max. 64W |
|--------------|-------------|
| Outpur Power | IVIAX. 04VV |

恒流输出特性(Constant Current Output Characteristics)

| 输出通道 | 最低电压 | 典型电压 | 最高电压 | 输出电流 |
|----------------|-------------|--------------|--------------|----------------|
| Output Channel | Min Voltage | Type Voltage | Max. Voltage | Output current |
| LED | 58V | 62V | 66V | 480mA*2 |

背光控制(The Backlight On/Off Control)

| BL Signal | Remark | Outputs |
|-----------|-------------|---------|
| BL-High | ≥2.5V & 2mA | Output |
| BL-Low | ≦0.5V | X |
| BL-Open | | X |

备注:恒流源输出受控于一个 TTL 电平兼容的信号(BL), 此信号电平需在 0-5V 之间 Remark: The Constant Current Source DC outputs current shall be enable with an active-TT L-compatible signal (BL). The signal level must be between 0-5V.

● BL 高电平, 打开输出。

When BL is pulled to TTL high, the DC current outputs are to be enabled.

● BL 低电平或悬空 , 关闭输出。

When BL is pulled to TTL low or open circuit, the DC outputs are to be disabled.

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|---------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 8 OF 39 |



PRODUCT GROUP REV ISSUE DATE Customer SPEC Rev. B 2020/09/14

调光(Adjust Backlight Brightness)

| PWM Signal | Remark | |
|---------------|-------------|--|
| PWM-High | ≥2.5V & 2mA | |
| PWM-Low | ≦0.5V | |
| PWM-Duty | 10%-99% | |
| PWM-Frequency | 150-300Hz | |

| DC Signal | Remark | |
|------------|--------|--|
| DC-Voltage | 0V-5V | |

备注(Remark):

I ADJ 脚接 PWM 信号,此 PWM 信号能调节背光亮度,PWM 信号占空比越宽,背光越亮,此信号电平需在2.5V-5V 之间。

The ADJ pin must be connected to a PWM signal. The PWM signal can adjust the bac klight brightness, the wider the duty cycle, the brighter the backlighting. The signal leve I must be between 2.5V-5V.

I ADJ脚接DC信号。此DC信号能调节背光亮度,电压越高,背光越暗,此信号电平需在 0V-5V之间。

The ADJ pin must be connected to a DC signal. The DC signal can adjust the backlight brightness, the higher the voltage, the darker the backlighting. The signal level must be between 0-5V.

I 调光方式:□ PWM调光 ■ DC调光 □ Other

| SPEC. NUMBER | |
|--------------|--|
| S8-65-8D-082 | |



REV

ISSUE DATE

Customer SPEC

Rev. B

2020/09/14

3.2.3 Interface Connections

CON1(Type: Pitch 2.0) Connect(XH2.5-14aW)

| () | | <u></u> | |
|------------------|------------|----------------------------|--|
| PIN号(Pin Number) | 符号(Symbol) | 功能(Function) | |
| 1. 2. 3. 4. 5 | +24V | INPUT VOLTAGE | |
| 6. 7. 8. 9. 10 | GND | Ground | |
| 11. 14 | NC | NC | |
| 12 | BL-ON | LED ON/OFF CONTROL(ON≥2.5) | |
| 13 | ADJ | Dimming control | 0V=Brightness Max 5V=Brightness Min |

CON2(Type: Pitch 2.0mm)

| PIN号(Pin Number) | 符号(Symbol) | 功能(Function) |
|------------------|------------|--------------|
| 1 | LED1+ | LED+ OUTPUT |
| 2 | LED1- | LED- OUTPUT |

Notice: 1. PIN 13:Extermal ADJ Control

| SPEC. NUMBER |
|--------------|
| S8-65-8D-082 |



REV

ISSUE DATE

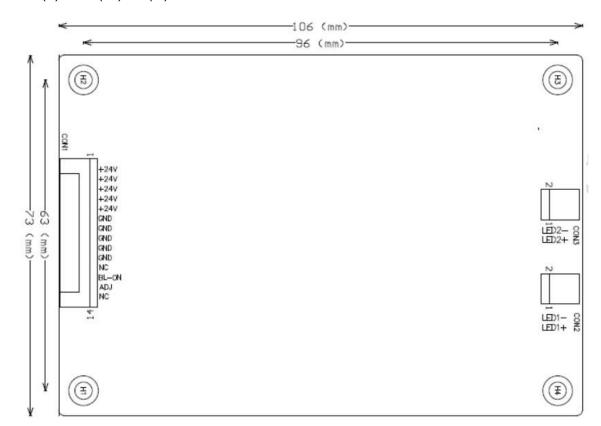
Customer SPEC

Rev. B

2020/09/14

3.2.4 Mechanical Characteristics

Outline Dimension 106.0(L)*73.0(W)*13(H) mm



| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 11 OF 39 |

| D | 0 | |
|---|---|--|
| D | | |
| | | |

REV

ISSUE DATE

Customer SPEC

Rev. B

2020/09/14

4.0 INTERFACE CONNECTION

- 4.1 Open Cell Input Signal & Power
 - LVDS Connector : PM.LVS.S040505101(UJC) $\ \ \$ IS050-C51B-C39-S(UJU) or 0.5Ph H3.75 LVDS(DEREN).

< Table 4. Open Cell Input Connector Pin Configuration >

| Pin No | Symbol | Description | Pin No | Symbol | Description |
|--------|---------|--|--------|---------|---|
| 1 | NC | No Connection | 21 | GND | Ground |
| 2 | SDA | I ² C Data | 22 | CH1[3]- | First pixel negative LVDS differential data input. Pair3 |
| 3 | SCL | I ² C Clock | | | First pixel positive LVDS differential data input. Pair3 |
| 4 | NC | Not Connected | 24 | NC | Not Connected |
| 5 | NC | Not Connected | 25 | NC | Not Connected |
| 6 | NC | Not Connected | 26 | NC | Not Connected |
| 7 | SELLVDS | High: JEIDA Low or Open: VESA | 27 | NC | Not Connected |
| 8 | NC | Not Connected | 28 | CH2[0]- | Second pixel negative LVDS differential data input. Pair0 |
| 9 | NC | Not Connected | 29 | CH2[0]+ | Second pixel positive LVDS differential data input. Pair0 |
| 10 | NC | Not Connected | 30 | CH2[1]- | Second pixel negative LVDS differential data input. Pair1 |
| 11 | GND | Ground | 31 | CH2[1]+ | Second pixel positive LVDS differential data input. Pair1 |
| 12 | CH1[0]- | First pixel negative LVDS differential data input. Pair0 | 32 | CH2[2]- | Second pixel negative LVDS differential data input. Pair2 |
| 13 | CH1[0]+ | First pixel positive LVDS differential data input. Pair0 | 33 | CH2[2]+ | Second pixel positive LVDS differential data input. Pair2 |
| 14 | CH1[1]- | First pixel negative LVDS differential data input. Pair1 | 34 | GND | Ground |
| 15 | CH1[1]+ | First pixel positive LVDS differential data input. Pair1 | 35 | CH2CLK- | Second pixel negative LVDS clock |
| 16 | CH1[2]- | First pixel negative LVDS differential data input. Pair2 | 36 | CH2CLK+ | Second pixel positive LVDS clock |
| 17 | CH1[2]+ | First pixel positive LVDS differential data input. Pair2 | 37 | GND | Ground |
| 18 | GND | Ground | 38 | CH2[3]- | Second pixel negative LVDS differential data input. Pair3 |
| 19 | CH1CLK- | First pixel negative LVDS clock | 39 | CH2[3]+ | Second pixel positive LVDS differential data input. Pair3 |
| 20 | CH1CLK+ | First pixel positive LVDS clock | | | |

| SPEC. NUMBER |
|--------------|
| S8-65-8D-082 |

| SPEC. TITLE | |
|------------------------------------|-------|
| DV430FHM-NN1 Product Specification | Rev.B |



REV

ISSUE DATE

Customer SPEC

Rev. B

2020/09/14

| Pin No | Symbol | Description | Pin No | Symbol | Description | |
|--------|--------|---------------|--------|--------|--------------------|--|
| 40 | NC | Not Connected | | | Ground | |
| 41 | NC | Not Connected | | | | |
| 42 | NC | Not Connected | 48 | VCC | Input Voltage +12V | |
| 43 | NC | Not Connected | 49 | VCC | Input Voltage +12V | |
| 44 | GND | Ground | | | Input Voltage +12V | |
| 45 | GND | Ground | | | Input Voltage +12V | |

Notes: 1. NC(Not Connected): This pins are only used for BOE internal operations.

- 2. Input Level of LVDS signal is based on the EIA-644 Standard.
- 3. LVDS_SEL: This pin is used for selecting LVDS signal data format.

If this Pin: High (3.3V) → JEIDA LVDS format

Otherwise : Low (GND) or Open (NC) → Normal NS LVDS format

Rear view of LCM

BIST Pattern





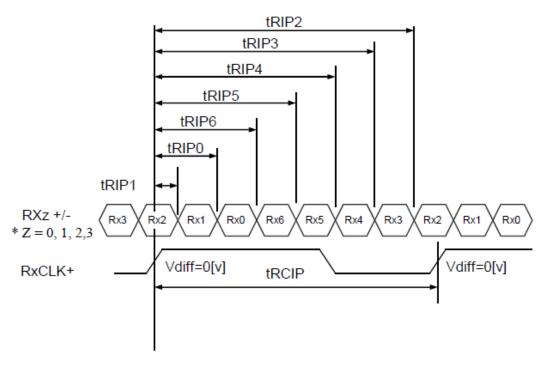
| PRODUCT GROUP | REV | ISSUE DATE |
|---------------|--------|------------|
| Customer SPEC | Rev. B | 2020/09/14 |

4.2 LVDS Interface

-LVDS Receiver: Timing Controller (LVDS Rx merged) / LVDS Data: Pixel Data

< Table 5. Open Cell Input Connector Pin Configuration >

| ltem | Symbol | Min | Тур | Max | Unit | Remark |
|--------------|--------|-----------------|--------------|-----------------|------|--------|
| CLKIN Period | tRCIP | 10.31 | 13.47(10.78) | 15.87 | nsec | |
| Input Data 0 | tRIP1 | -0.42 | 0.0 | +0.42 | nsec | |
| Input Data 1 | tRIP0 | tRCIP/7-0.42 | tRCIP/7 | tRCIP/7+0.42 | nsec | |
| Input Data 2 | tRIP6 | 2 ×tRCIP/7-0.42 | 2 ×tRCIP/7 | 2 ×tRCIP/7+0.42 | nsec | |
| Input Data 3 | tRIP5 | 3 ×tRCIP/7-0.42 | 3 ×tRCIP/7 | 3 ×tRCIP/7+0.42 | nsec | |
| Input Data 4 | tRIP4 | 4 ×tRCIP/7-0.42 | 4 ×tRCIP/7 | 4 ×tRCIP/7+0.42 | nsec | |
| Input Data 5 | tRIP3 | 5 ×tRCIP/7-0.42 | 5 ×tRCIP/7 | 5 ×tRCIP/7+0.42 | nsec | |
| Input Data 6 | tRIP2 | 6 ×tRCIP/7-0.42 | 6 ×tRCIP/7 | 6 ×tRCIP/7+0.42 | nsec | |



* Vdiff = (RXz+)-(RXz-),....,(RXCLK+)-(RXCLK-)

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|------------------|--|-----------------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 14 OF 39 |
| SPEC. NUMBER | SPEC. TITLE | PAGE |



PRODUCT GROUP REV

Rev. B

2020/09/14

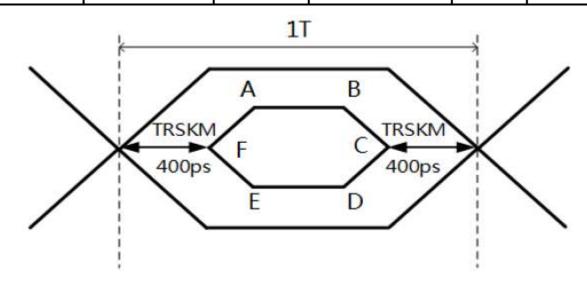
ISSUE DATE

4.3 LVDS Rx Interface Eye Diagram

< Table 6. LVDS Rx Interface Eye Diagram>

Customer SPEC

| Symbol | Min | Тур | Max | Unit | Note |
|--------|-----|------|-----|------|------|
| Α | _ | 100 | _ | m∨ | |
| В | 1 | 100 | _ | m∨ | |
| С | 1 | 0 | | m∨ | |
| D | 1 | -100 | 1 | m∨ | |
| E | | -100 | _ | m∨ | |
| F | _ | 0 | _ | m∨ | |



Notes:

- 1. Time F to A,B to C,C to D,E to F is 150p second.
- 2. LVDS clock=85Mhz.
- 3. The time A to B=1T-2*TRSKM-2*150ps.

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 15 OF 39 |
| | | |

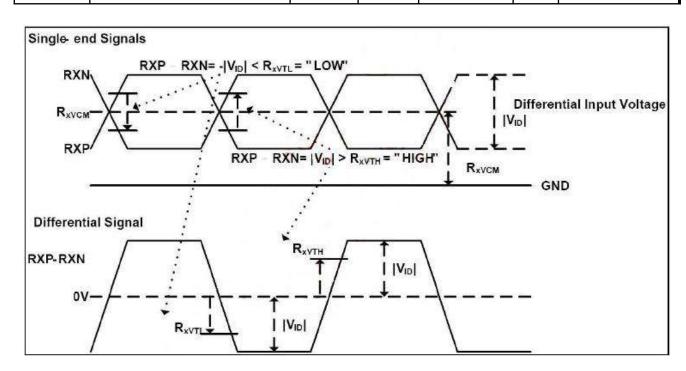


| PRODUCT GROUP | REV | ISSUE DATE |
|---------------|--------|------------|
| Customer SPEC | Rev. B | 2020/09/14 |

4.4 LVDS Receiver Differential Input

< Table 7. LVDS Receiver Differential Input>

| Symbol | Parameter | Parameter Min Typ Max | | Uni t | Condition | |
|-------------------|---|-----------------------|--|---------------------------|-----------|-------------|
| R _{xVTH} | Differential input high threshold voltage | | | +0.1v | V | RxVCM =1.2V |
| R _{xVTL} | Differential input low threshold voltage | -0.1V | | | V | |
| R _{XVIN} | Input voltage range (singled-end) | 0 | | 2.4 | V | |
| R _{xVCM} | Differential input common mode voltage | V _{ID} /2 | | 2.4- V _{ID} /2 | V | |
| V _{ID} | Differential input voltage | 0.1 | | 0.6 | V | |



| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 16 OF 39 |



| PRODUCT GROUP | REV | ISSUE DATE |
|---------------|--------|------------|
| Customer SPEC | Rev. B | 2020/09/14 |

5.0 SIGNAL TIMING SPECIFICATION

5.1 Timing Parameters (DE only mode)

< Table 8. Timing Table >

| | Symb | ols | Min | Тур | Max | Unit | |
|--------------|-------------------|-------|-----------------|------|-------|------|------------------|
| | Frequency | 1/To | С | 60 | 74.25 | 78 | MHz |
| Clock | High Time | Tch | 1 | - | 4/7Tc | - | |
| | Low Time | Tel | | - | 4/7Tc | - | |
| | | Т | | 1100 | 1125 | 1149 | lines |
| ľ | Frame Period | Tv | | 48.5 | 60 | 63 | Hz |
| Но | Horizontal Active | | t _{HV} | - | 960 | - | t _{CLK} |
| Display Term | | Total | t _{HP} | 1060 | 1100 | 1200 | t _{CLK} |
| V | ertical Active | Valid | t _{VV} | - | 1080 | - | t _{HP} |
| Display Term | | Total | t _{VP} | 1100 | 1125 | 1149 | t _{HP} |

Notes: This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

< Table 9. LVDS Input SSCG>

| Symbol | Parameter | Condition | Min | Тур | Max | Unit |
|--------------------|---|--|------|-------|------|------|
| F | LVDS Input frequency | - | 45 | 74.25 | 85 | MHz |
| T _{LVSK} | LVDS channel to channel skew | $F=100MHz \\ V_{IC}=1.2V \\ V_{ID}=\pm400mV$ | -380 | - | +380 | ps |
| F _{LVMOD} | Modulating frequency of input cl ock during SSC | | - | - | 85 | KHz |
| F _{LVDEV} | Maximum deviation of input clock frequency during SSC | | -3 | - | +3 | % |
| T _{CY-CY} | Cycle to Cycle jitter | | - | - | 100 | ps |

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 17 OF 39 |



REV

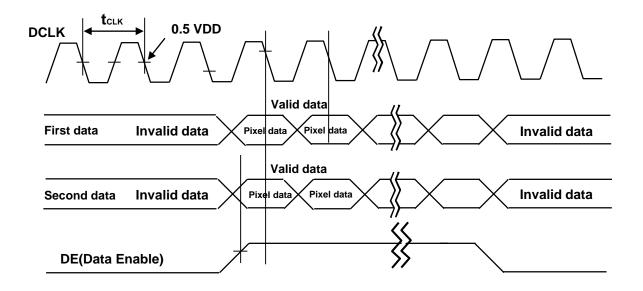
ISSUE DATE

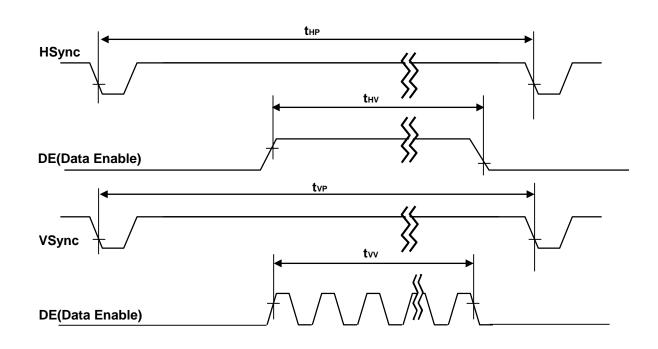
Customer SPEC

Rev. B

2020/09/14

5.2 Signal Timing Waveform





| SPEC. NUMBER | |
|--------------|--|
| S8-65-8D-082 | |

DV430FHM-NN1 Product Specification Rev.B

PAGE 18 OF 39

DAS-RD-2019028-O A4(210 X 297)



REV

ISSUE DATE

Customer SPEC

Rev. B

2020/09/14

5.3 Input Signals, Basic Display Colors and Gray Scale of Colors

< Table 10. Input Signal and Display Color Table >

| | | | Input Data Signal | | | | | | | | | | | | | | | | | | | | | | |
|------------|---------------|----------|-------------------|----|----------|----------|---|---------------|----|----------|----|---|----------|--------------|----|---|----|----------|----|-----|----------|----------|----|----------|----------|
| Color & G | | | R | ed | Da | ta | | | | | | | ı D | | | | | | В | lue | Da | ta | | | |
| | | R7 | R6 | | | | | R1 | R0 | G7 | G6 | | | | | | G0 | B7 | B6 | B5 | | | | B1 | B0 |
| | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Blue | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Green | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Basic | Cyan | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| Colors | Red | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Magenta | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Yellow | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | White | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | \triangle | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Darker | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Gray Scale | \triangle | | | | | <u> </u> | | | | | | | | <u> </u> | | | | | | | | <u> </u> | | | |
| of Red | ∇ | | | | | | | | | | | | | <u> </u> | | | | | | | | <u> </u> | | | |
| | Brighter | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | ∇ | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | \triangle | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Gray Scale | Darker | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| of Green | \triangle | | | | | <u> </u> | | | | | | | | <u> </u> | | | | | | | | <u> </u> | | | |
| 0.00 | ∇ | L_ | _ | _ | | <u> </u> | _ | _ | _ | <u> </u> | | | · · | | | _ | | _ | _ | | | <u> </u> | _ | _ | |
| l . | Brighter | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| l . | ∇ | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| Gray Scala | Darker | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 |
| Gray Scale | | | | | | | | | | | | | | <u> </u> | | | | _ | | | | <u> </u> | | | _ |
| of Blue | • | <u> </u> | _ | | <u> </u> | _ | _ | | _ | _ | | _ | <u> </u> | _ | _ | _ | _ | _ | | T 4 | <u> </u> | 1 | | _ | |
| l | Brighter | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 |
| l | Plue | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 |
| | Blue | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| l | Black | 0 | 0 | 0 | - | 0 | | 0 | _ | _ | 0 | 0 | 0 | 0 | 0 | - | 0 | 0 | _ | 0 | 0 | 0 | 0 | 0 | 0 |
| | Darker | 0 | 0 | 0 | 0 | 0 | 0 | <u>0</u> 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Gray Scale | <u>Darker</u> | ۲ | ΙU | Įυ | ب | L ∪ | U | | U | ۲ | U | U | ب | <u> </u> | LU | | Įυ | ۲ | ΙU | ΙU | LU | <u>,</u> | Lυ | <u> </u> | Ч |
| of White | | \vdash | | | | <u> </u> | | | | \vdash | | | | | | | | \vdash | | | | | | _ | \dashv |
| | Brighter | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 |
| | <u> </u> | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 |
| | • | _ | - | | | - | | - | | 1 | - | | - | - | - | 1 | _ | - | _ | _ | | _ | - | - | _ |
| | White | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | L | 1 | 1 | 1 | 1 | 1 | L | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |

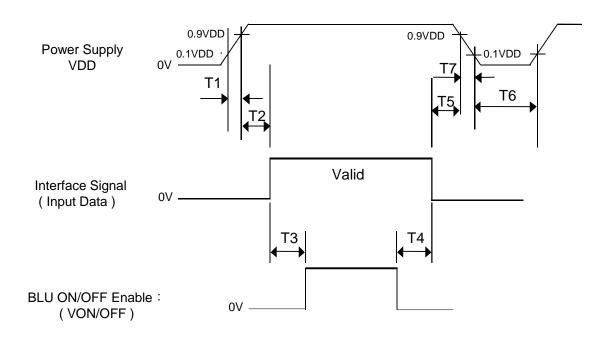
| SPEC. NUMBER |
|--------------|
| S8-65-8D-082 |



PRODUCT GROUP REV ISSUE DATE Customer SPEC Rev. B 2020/09/14

5.4 Power Sequence

To prevent a latch-up or DC operation of the Open Cell, the power on/off sequence shall be as shown in below



< Table 11. Sequence Table >

| 201000000000000000000000000000000000000 | | | | | | | | | | | |
|---|-----|-------|-----|-------|--|--|--|--|--|--|--|
| Doromotor | | Units | | | | | | | | | |
| Parameter | Min | Тур | Max | Units | | | | | | | |
| T1 | 0.5 | - | 20 | ms | | | | | | | |
| T2 | 10 | - | 100 | ms | | | | | | | |
| T3 | 200 | - | - | ms | | | | | | | |
| T4 | 200 | - | - | ms | | | | | | | |
| T5 | 0 | - | - | ms | | | | | | | |
| T6 | 1 | - | - | S | | | | | | | |

Notes: 1. Back Light must be turn on after power for logic and interface signal are valid.

- 2. Even though T1 is out of SPEC, it is still ok if the inrush current of VDD is below the limit.
- 3. When VDD<0.9VDD(Typ.), Power off.
- 4. T7 decreases smoothly, if there were rebounding voltage, it must smaller than 5 volts.

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 20 OF 39 |



| PRODUCT GROUP | REV | ISSUE DATE |
|---------------|--------|------------|
| Customer SPEC | Rev. B | 2020/09/14 |

6.0 OPTICAL SPECIFICATIONS

Optical characteristics are determined after the unit has been 'ON' and stable in a dark envir onment at $25\pm2^{\circ}C$. The values are specified at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °. It is presented additional information conc erning the measurement equipment and method in FIG. 1.

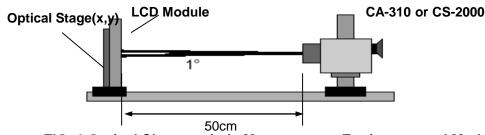


FIG. 1 Optical Characteristic Measurement Equipment and Method

< Table 12. Optical Table >

[VDD = 12.0V, Frame rate = 60Hz, Ta =25 \pm 2 °C] **Parameter** Symbol Condition Max Unit Remark Min Typ Θ_3 89 Deg. Horizontal Viewing Θ_9 89 Deg. Angle CR > 10 Note 5 Θ₁₂ 89 Deg. Vertical $\Theta_{\underline{6}}$ 89 Deg. Color Temperature K 9000 10,000 11,500 % Color Gamut 70 72 Contrast ratio CR 800:1 1200:1 Note 1 Y_{w} Luminance of White 350 400 cd/m² Note 2 White luminance uniformity Note 3 ΔΥ 70 75 % W_{x} 0.280 White W_{v} $\Theta = 0^{\circ}$ 0.290 (Center) R_x 0.640 Red Normal R_v TYP. 0.334 TYP. Reproduction Viewing of color G_{x} - 0.03 +0.03Angle 0.296 Green G_v 0.607 B_x 0.149 Blue B_{y} 0.069 Response Time G to G T_{a} Note 4 8 10 ms 2.2 Note 6 Gamma Scale 2.0 2.4

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 21 OF 39 |



REV

ISSUE DATE

Customer SPEC

Rev. B

2020/09/14

Note: 1. Contrast Ratio(CR) is defined mathematically as:

Contract Potio - Surface Luminance with all white pixels

Surface Luminance with all black pixels

It is measured at center 1-point.

- 2. Surface luminance are determined after the unit has been 'ON' and 1 Hour after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance , δ WHITE is defined as :

 δ WHITE(9P) = Minimum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on0}) /Maximum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on9}) Where L_{on1} to L_{on9} are the luminance with all pixels displaying white at 9 locations . For more information, see the FIG. 2.

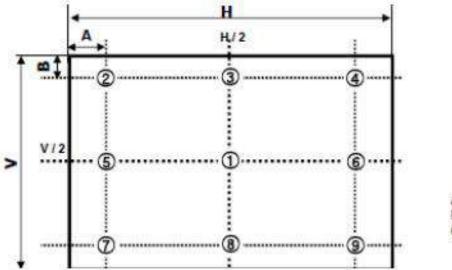
- 4. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 5. Gray scale specification
 Gamma Value is approximately 2.2.

| SPEC. NUMBER |
|--------------|
| S8-65-8D-082 |



| PRODUCT GROUP | REV | ISSUE DATE |
|---------------|--------|------------|
| Customer SPEC | Rev. B | 2020/09/14 |

Measuring point for surface luminance & luminance variationCA-310, Contact method)



A:H/9 mm B:V/9 mm @H,V:Active Area

FIG. 2 9 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

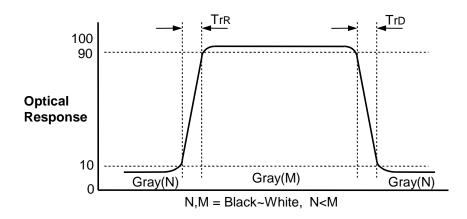


FIG. 3 Response Time

| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 23 OF 39 |
|--------------|--|----------|
| SPEC. NUMBER | SPEC. TITLE | PAGE |

DAS-RD-2019028-O A4(210 X 297)



| PRODUCT GROUP | REV |
|---------------|--------|
| Customer SPEC | Rev. B |

Rev. B 2020/09/14

ISSUE DATE

Definitions of viewing angle range

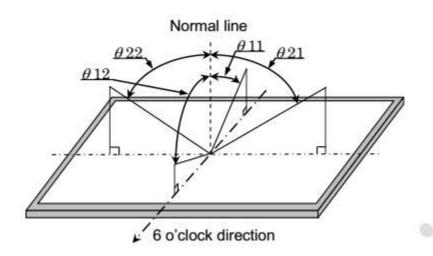


FIG. 4 Viewing Angle

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 24 OF 39 |



| PRODUCT GROUP | REV | ISSUE DATE |
|---------------|-------|------------|
| Customer SPEC | Rev B | 2020/09/14 |

7.0 MECHANICAL CHARACTERISTICS

7.1 Dimensional Requirements

Figure 5 (located in Appendix) shows mechanical outlines for the model DV430FHM-NN1. Other parameters are shown in Table 13.

< Table 13. Dimensional Parameters >

| Parameter | Specification | Unit |
|---------------------|--|--------|
| Dimensional outline | $961.7(H) \times 550.0(V) \times 11.7(B)$ | mm |
| Weight | 8.36 | Kg |
| Active area | 940.896(H) × 529.254(V) | mm |
| Pixel pitch | $163.35(H) \times 490.05(V)$ | mm |
| Number of pixels | $1920(H) \times 1080(V) $ (1 pixel = R + G + B dots) | pixels |
| Back-light | E-LED Backlight | |

7.2 Mounting

See Figure 6. (Shown in Appendix)

7.3 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an low haze coating to reduce scratching. Front Polarizer hardness is at less 3H.

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 25 OF 39 |



| PRODUCT GROUP | REV | ISSUE DATE |
|---------------|-------|------------|
| Customer SPEC | Rev B | 2020/09/14 |

8.0 RELIABILITY TEST

The Reliability test items and its conditions are shown in below.

< Table 14. Reliability Test Parameters >

| No | Test Items | Conditions | |
|----|---|---|--|
| 1 | High temperature storage test | $Ta = 60 ^{\circ}\text{C}, 240 \text{hrs}$ | 3 |
| 2 | Low temperature storage test | $Ta = -20 ^{\circ}\text{C}, 240 \text{hr}$ | 'S |
| 3 | High temperature & high humidity operation test | Ta = 50 °C, 80%RH, 240hrs | |
| 4 | High temperature operation test | $Ta = 50 ^{\circ}\text{C}, 240 \text{hrs}$ | |
| 5 | Low temperature operation test | Ta = -5 °C, 240hrs | |
| 6 | Thermal shock | Ta = -20 °C \leftrightarrow 60 °C (0.5 hr), 100 cycle | |
| 7 | Vibration test (non-operating) | Frequency Gravity / AMP Period | 10 ~ 300 Hz, Random 1.0 G X, Y, Z 30 min |
| | | Gravity | 50G |
| 8 | Shock test (non-operating) | Pulse width | 11msec, sine wave |
| | | Direction | \pm X, \pm Y, \pm Z Once for each |
| 9 | Electro-static discharge test | Air : 150 pF, 330Ω, 15 KV Contact : 150 pF, 330Ω, 8 KV | |

This test condition is based on BOE module.

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 26 OF 39 |



REV

ISSUE DATE

Customer SPEC

Rev. B

2020/09/14

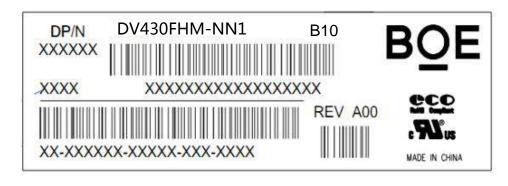
9.0 DEFINITION OF LABELS

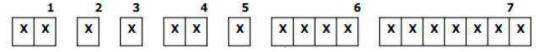
MDL Label

He bar code nameplate is pasted on each module as illustration, and its definitions are as following explanation.

Label Size: 80mm (L) x 25mm (W)

Label Picture:





- 1. Control Number
- 2. Rank / Grade
- 3. Line Classification
- 4. Year (2001: 01, 2002: 02, ...)

- 5. Month (1,2,3, ..., 9, X, Y, Z)
- 6. Internal Use
- 7. Serial Number



REV

ISSUE DATE

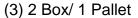
Customer SPEC

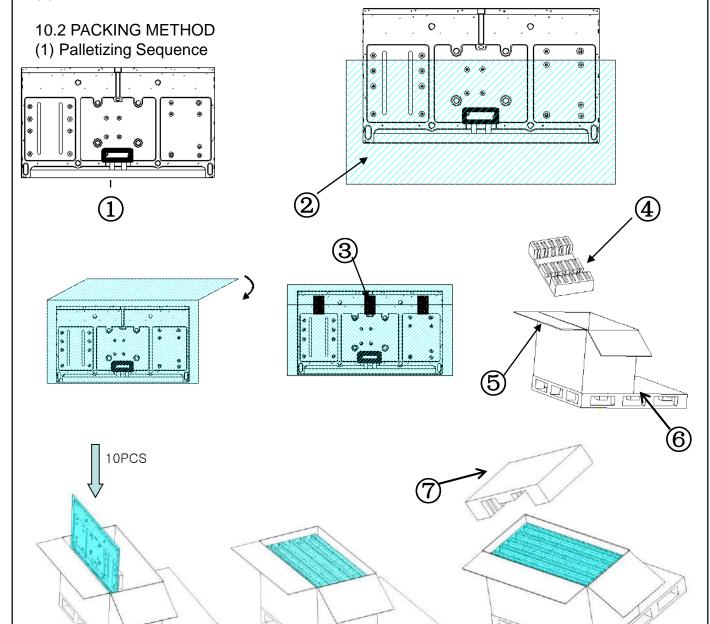
Rev. B

2020/09/14

10.0 PACKING INFORMATION

- 10.1 PACKING SPECIFICATIONS
- (1) 10 PCS LCD TV modules / 1 Box
- (2) Box dimensions: 1065(W) x 565(D) x 635(H)mm





SPEC. NUMBER S8-65-8D-082

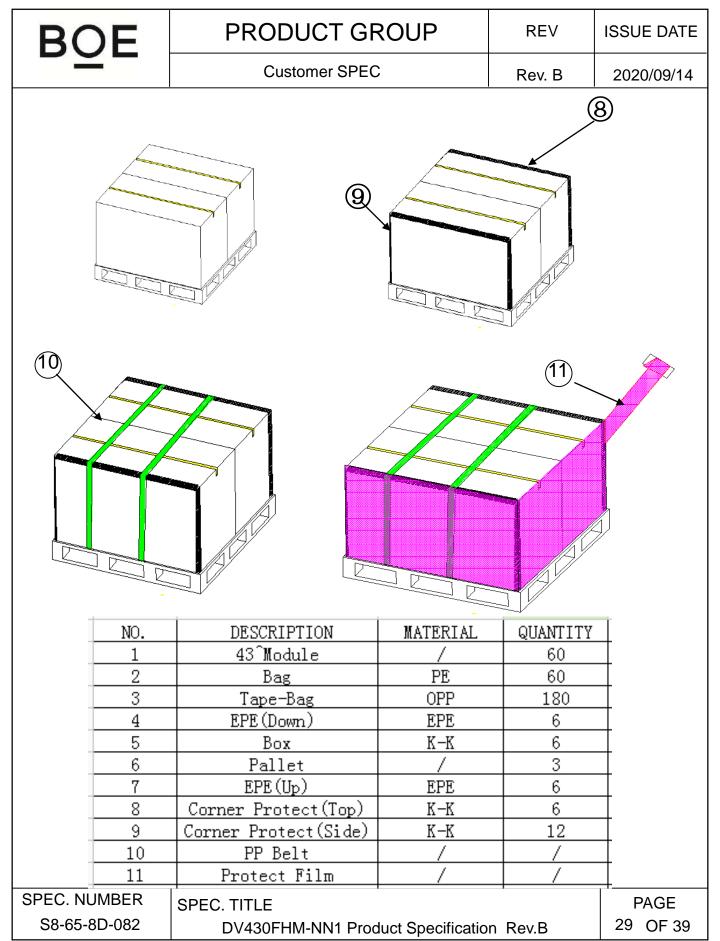
SPEC. TITLE

DV430FHM-NN1 Product Specification Rev.B

PAGE 28 OF 39

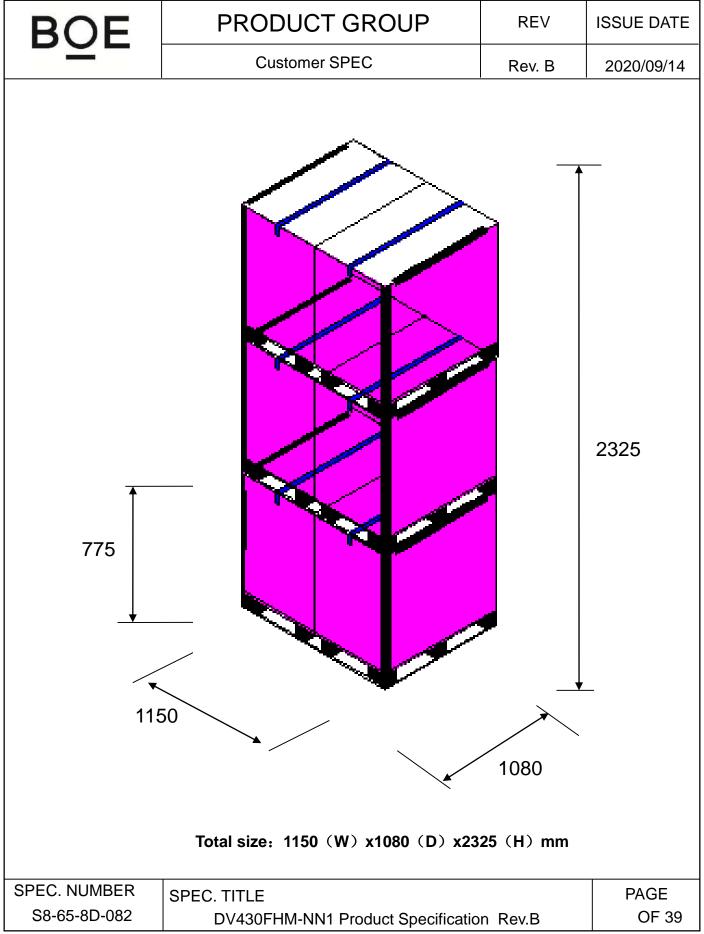
DAS-RD-2019028-O

A4(210 X 297)



DAS-RD-2019028-O

A4(210 X 297)



DAS-RD-2019028-O A4(210 X 297)



Customer SPEC

REV

Rev. B

2020/09/14

ISSUE DATE

10.3 Box Label

• Label Size : 100 mm (L) × 50 mm (W)

Contents

Model: DV430FHM-NN1 Q'ty: 10 Module in one box.

Serial No.: Box Serial No. See next page for detail description.

Date: Packing Date

FG Code: FG Code of Product



- ▶ 打印标识,说明如下:
- 1. FG-CODE
- 2. 产品数量
- 3. Box ID, 编码规则如下
- 4. Box Packing 日期
- 5. FG-CODE 后四位

BOX ID编码原则

| Digit | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 |
|-------------|---|---------------|-------|------|----|-----|-------|-----------------|---|----|-----------|----|----|
| Code | s | L | S | Α | 1 | 6 | 3 | D | 0 | 0 | 1 | Α | 1 |
| Description | | oducts GBN | Grade | Line | Υє | ear | Month | Revison Code | | S | erial No. | | |

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 31 OF 39 |



REV

ISSUE DATE

Customer SPEC

Rev. B

2020/09/14

11.0 PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD Module.

11.1 Mounting Precautions

- Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- You must mount a module using specified mounting holes (Details refer to the drawings)
- You should consider the mounting structure so that uneven force (ex. Twisted stress, Concentrated stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- Do not apply mechanical stress or static pressure on module; Abnormal display cause by
 pressing some parts of module during assembly process, do not belong to product failure,
 the press should be agreed by two sides.
- Determine the optimum mounting angle, refer to the viewing angle range in the specification for each model.
- Do not apply mechanical stress or static pressure on module, and avoid impact, vibration and falling.
- Acetic acid type and chlorine type materials for the cover case are not desirable because
 the former generates corrosive gas of attacking the polarizer at high temperature and the
 latter causes circuit break by electro-chemical reaction.
- Protection film for polarizer on the module should be slowly peeled off before display.
- Be careful to prevent water & chemicals contact the module surface.
- You should adopt radiation structure to satisfy the temperature specification.
- Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- When the surface becomes dusty, please wipe gently with absorbent cotton or other soft
 materials like chamois soaks with petroleum benzine. Normal-hexane & alcohol is
 recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use
 acetone, toluene, because they cause chemical damage to the polarizer.
- Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading..

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 32 OF 39 |



This module has its circuitry PCB's on the rear side and Driver IC, should be handled carefully in order not to be stressed.

2020/09/14

- Avoid impose stress on PCB and Driver IC during assembly process, Do not drawing, bending, COF package & wire
- Do not disassemble the module.

11.2 Operating Precautions

- Do not connector or disconnect the cable to/from the Module at the "Power On" Condition.
- When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the module would be damaged.
- · Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- Do not allow to adjust the adjustable resistance or switch
- · The electrochemical reaction caused by DC voltage will lead to LCD module degradation, so DC drive should be avoided.
- The LCD modules use C-MOS LSI drivers, so customers are recommended that any unused input terminal would be connected to Vdd or Vss, do not input any signals before power is turn on, and ground you body, work/assembly area, assembly equipment to protect against static electricity.
- · Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module may be damaged.
- Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- Design the length of cable to connect between the connector for back-light and the converter as shorter as possible and the shorter cable shall be connected directly, The long cable between back-light and Converter may cause the Luminance of LED to lower and need a higher startup voltage
- The cables should be as short as possible between System Board and PCB interface.
- · Connectors are precision devices to transmit electrical signals, and operators should plug in parallel
- Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 33 OF 39 |



REV

ISSUE DATE

Customer SPEC

Rev. B

2020/09/14

11.3 Electrostatic Discharge Precautions

- Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.
- Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc.
- Do not close to static electricity to avoid product damage.
- · Do not touch interface pin directly.

11.4 Precautions for Strong Light Exposure

 Do not leave the module operation or storage in Strong light . Strong light exposure causes degradation of polarizer and color filter.

11.5 Precautions for Storage

A. Atmosphere Requirement

| ITEM | UNIT | MIN | MAX |
|------------------------|--|--|--|
| Storage Temperature | (°C) | 5 | 40 |
| Storage Humidity | (%rH) | 40 | 75 |
| Storage Life | | 6 months | |
| Storage Condition | facility. • Prevent products from and water. • The product need to k • Be careful for conden | ould be equipped with a dark in being exposed to the direct seep away from organic solves assistant at sudden temperatur guaranteed under packing co | t sunlight, moisture vent and corrosive gas. e change. |

B. Package Requirement

- The product should be placed in a sealed polythene bag.
- Product Should be placed on the pallet, Which is away from the floor, Be cautions not to pile the product up.
- The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.
- As the original protective film, do not use the adhesive protective film to avoid change of Pol color and characteristic.

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 34 OF 39 |



REV

ISSUE DATE

Customer SPEC

Rev. B

2020/09/14

11.6 Precautions for protection film

- Remove the protective film slowly, keeping the removing direction approximate 30-degree not vertic al from panel surface, If possible, under ESD control device like ion blower, and the humidity of wor king room should be kept over 50%RH to reduce the risk of static charge.
- People who peeled off the protection film should wear anti-static strap and grounded well.

11.7 Appropriate Condition for Commercial Display

- -Generally large-sized LCD modules are designed for consumer applications. Accordingly, long-term display like in Commercial Display application, can cause uneven display including image sticking. To optimize module's lifetime and function, several operating usages are required.
- 1. Normal operating condition
- Temperature: 20±15°C
- Operating Ambient Humidity: 55±20%
- Display pattern: dynamic pattern (Real display)
- Well-ventilated place is recommended to set up Commercial Display system
- 2. Special operating condition
 - a. Ambient condition
 - Well-ventilated place is recommended to set up Commercial Display system.
 - b. Power and screen save

Module may be damaged.

- Periodical power-off or screen save is needed after long-term display.
- c. As the low temperature, the response time is greatly delayed. As the high temperatures (higher than the operating temperature) the LCD module may turn black screen. The above phenomenon cannot explain the failure of the display. When the temperature returns to the normal operating temperature, the LCD module will return to normal display.
- d. When expose to drastic fluctuation of temperature (hot to cold or cold to hot) ,the LCD module may be affected; Specifically, drastic temperature fluctuation from cold to hot ,produces dew on the LCD module 's surface which may affect the operation of the polarizer and LCD module
- e. Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the
- f. Products exposed to low temperature environment for a long time, need to carry out necessary protection, low temperature environment is usually refrigerators, vending machine Etc...
- g. Long time and large angle forword use or unconventional use, It is strongly recommended to contact BOE for filed application engineering advice

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 35 OF 39 |



REV

ISSUE DATE

Customer SPEC

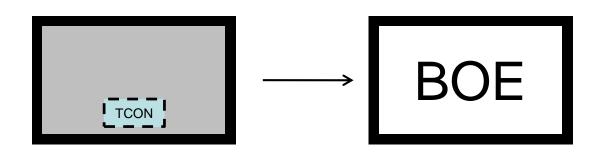
Rev. B

2020/09/14

f. Product reliability and functions are only guaranteed when the product is used under right operation usages. If product will be used in extreme conditions such as high temperature, high humidity, high altitude, special display images, running time, long time operation, outdoor operation, etc. It is strongly recommended to contact BOE for filed application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at airports, transit stations, banks, stock market and controlling systems.

- 3. Operating usages to protect against image sticking due to long-term static display.
 - a. Suitable operating time: under 20 hours a day.
 - b. Static information display recommended to use with moving image.
 - Cycling display between 5 minutes' information(static) display and 10 seconds' moving image.
 - c. Background and character (image) color change
 - Use different colors for background and character, respectively.
 - Change colors themselves periodically.
 - d. Avoid combination of background and character with large different luminance.
 - 1) Abnormal condition just means conditions except normal condition.
 - 2) Black image or moving image is strongly recommended as a screen save
- 4. Lifetime in this spec. is guaranteed only when Commercial Display is used according to operating usages.
- 5. Module should be turned clockwise based on front view when used in portrait mode.
- Landscape Mode

The default placement is TCON side on the lower side and the image is shown upright via viewing from the front.

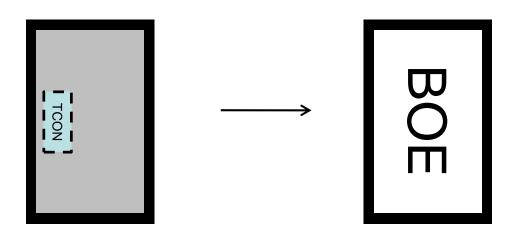


| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 36 OF 39 |



· Portrait Mode

The default placement is that TCON side has to be placed on the left side via viewing from the front



11.8 Other Precautions

A. LC Leak

- If the liquid crystal material leaks from the panel, it is recommended to wash the LC with acetone or ethanol and then burn it.
- If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- If LC in mouth, mouth need to be washed, drink plenty of water to induce vomiting and follow medical advice.
- If LC touch eyes, eyes need to be washed with running water at least 15 minutes.

B. Rework

• When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 37 OF 39 |



| PRODUCT GROUP |
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ISSUE DATE

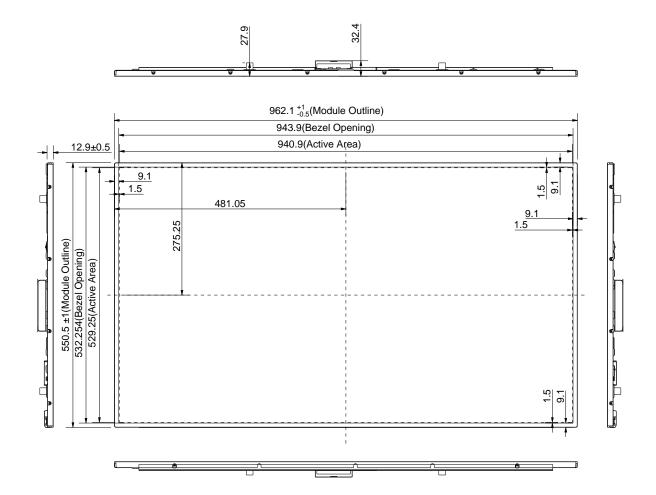
Customer SPEC

Rev. B

2020/09/14

12.0 APPENDIX

< Figure 5. TFT-LCD Module Outline Dimensions (Front View) >



| | PAGE |
|---|----------|
| S8-65-8D-082 DV430FHM-NN1 Product Specification Rev.B 3 | 38 OF 39 |



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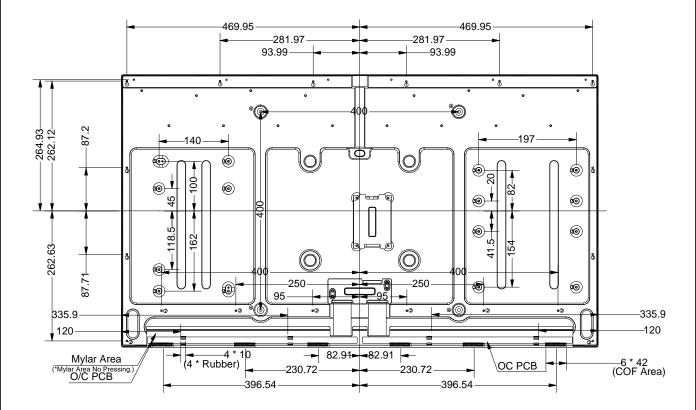
ISSUE DATE

Customer SPEC

Rev. B

2020/09/14

< Figure 6. TFT-LCD Module Outline Dimensions (Rear View) >



Note:

- 1. ① -M3 Locking depth max <u>4.8mm</u>,effective thread depth <u>2.3mm</u>,Total:6pcs
- 2. 2 -M3 Locking depth max $\frac{3.8 \text{mm}}{\text{effective thread depth}}$ $\frac{2.3 \text{mm}}{\text{constant}}$, Total:12pcs
- 3. \bigcirc -M3 Locking depth max $\overline{6.8mm}$, effective thread depth $\overline{2mm}$, \overline{T} otal:15pcs
- 5.USER MOUNTING TORQUE SPEC:

Torque value: M3 6kgf-cm,MAX Rivet column: M6 20kgf-cm,MAX

| SPEC. NUMBER | SPEC. TITLE | PAGE |
|--------------|--|----------|
| S8-65-8D-082 | DV430FHM-NN1 Product Specification Rev.B | 39 OF 39 |