

Model Name: P430HVN07.0
Issue Date : 2025/05/29

() Preliminary Specifications
(*) Final Specifications

Customer Signature	Date	AUO	Date
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[illegible]

1. General Description

This specification applies to the 43 inch Color TFT-LCD Module P430HVN07.0. This LCD module has a TFT active matrix type liquid crystal panel 1920 x 1080 pixels, and diagonal size of 43 inch. This module supports 1920 x 1080 mode. Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot. P430HVN07.0 has been designed to apply the 2 channel LVDS interface method. It is intended to support displays where high brightness, wide viewing angle, high color saturation, and high color depth are very important.

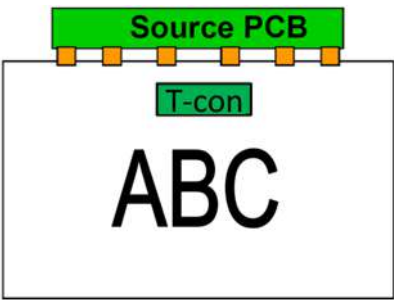
*** General Information**

Items	Specification	Unit	Note
Active Screen Size	43	inch	
Display Area	940.896(H) x 529.254(V)	mm	
Outline Dimension	961×549.7	mm	
Driver Element	a-Si TFT active matrix		
Bezel Opening	945.896 (H) x 533.25 (V)	mm	Recommend
Display Operation Mode	8 bit (16.7M)	Colors	
Display mode	Normally Black		
Number of Pixels	1,920 x 1,080	Pixel	
Pixel Pitch	0.49 (H) x 0.49(W)	mm	
Pixel Arrangement	RGB vertical stripe		
Surface Treatment	Anti-Glare, 3H		Haze=2%
Transmittance (with Polarizer)	6.0%		Typical value
Weight	7750	g	Typical value
Possible display type	Landscape Mode		Note 1
Remarks			

Note 1:Landscape Mode:

The default placement is T-Con Side on the higher side and the image is shown upright via viewing from the front.

Landscape (Front view)



2. Absolute Maximum Ratings

The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit or the unrecoverable damage on the device.

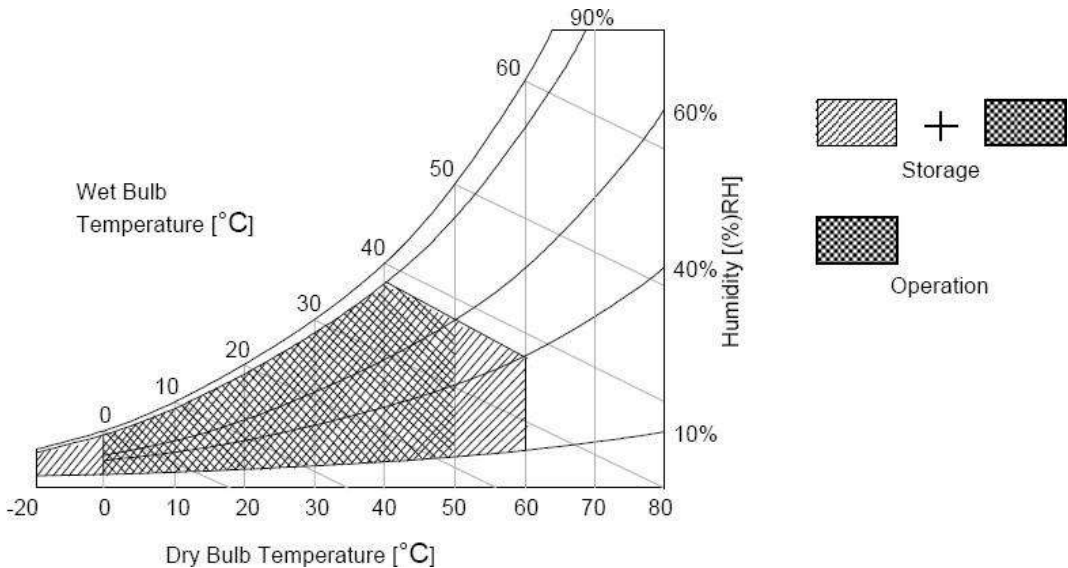
Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	V _{DD}	-0.3	14	[Volt] _{DC}	Note 1
Input Voltage of Signal	V _{in}	-0.3	4	[Volt] _{DC}	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	10	90	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	10	90	[%RH]	Note 2
Panel Surface Temperature	PST		65	[°C]	Note 3

Note 1: Duration:50 msec.

Note 2 : Maximum Wet-Bulb should be 39°C and No condensation.

The relative humidity must not exceed 90% non-condensing at temperatures of 40°C or less. At temperatures greater than 40°C, the wet bulb temperature must not exceed 39°C.

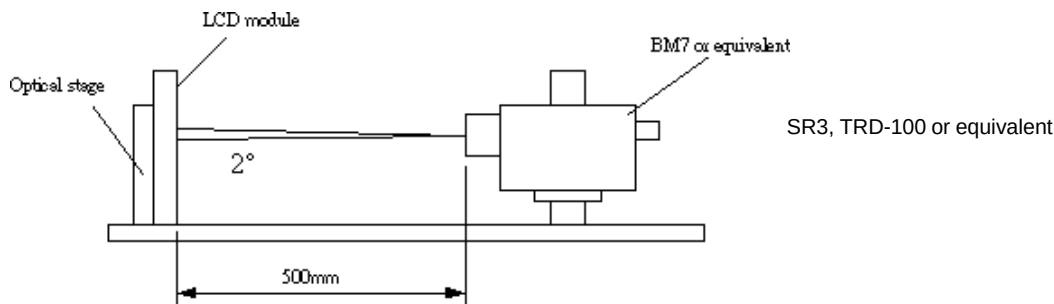
Note 3: Surface temperature is measured at 50°C Dry condition



3. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 45 minutes in a dark environment at 25°C with test condition $V_{DD}=12.0V$ and $F_v=60Hz$. The values specified are measured on the center of active area and at an approximate distance 500 mm from the LCD surface at a viewing angle of ϕ and θ equal to 0°.

Fig.1 presents additional information concerning the measurement equipment and method.



Parameter		Symbol	Condition	Values			Unit	Notes
				Min.	Typ.	Max		
Contrast Ratio		CR	SR3, TRD-100	2400	3000	--		1,2
Response Time (G to G)		T_Y		--	8	16	ms	3
Luminance Variation		$\delta_{WHITE(9P)}$		75	--	--		6
Surface Luminance (White)		L_{WH}		320	400	--	cd/m ²	2
Color Gamut		NTSC		--	72		%	
Color Coordinates								4
	Red	R_X	With SR3 Standard light source "C"	Typ.-0.03	0.641	Typ.+0.03		
		R_Y			0.338			
	Green	G_X			0.309			
		G_Y			0.625			
	Blue	B_X			0.148			
		B_Y			0.057			
	White	W_X			0.28			
		W_Y			0.29			
Viewing Angle								5
	x axis, right($\phi=0^\circ$)	θ_r	SR3	85	89	--	degree	
	x axis, left($\phi=180^\circ$)	θ_l		85	89	--	degree	
	y axis, up($\phi=90^\circ$)	θ_u		85	89	--	degree	
	y axis, down ($\phi=270^\circ$)	θ_d		85	89	--	degree	

1. Light source here is the BLU of AUO module.
2. Contrast Ratio (CR) is defined mathematically as:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance at center location of all white pixels}}{\text{Surface Luminance at center location of all black pixels}}$$

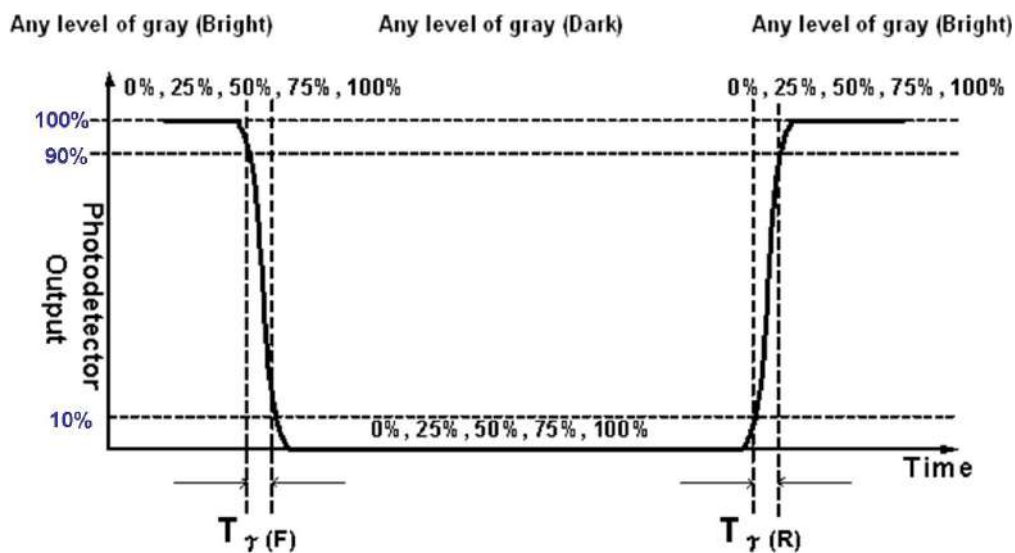
3. Response time T_{γ} is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100% brightness matrix) and is based on Frame rate = 60Hz to optimize.

Measured Response Time		Target				
		0%	25%	50%	75%	100%
Start	0%		0% to 25%	0% to 50%	0% to 75%	0% to 100%
	25%	25% to 0%		25% to 50%	25% to 75%	25% to 100%
	50%	50% to 0%	50% to 25%		50% to 75%	50% to 100%
	75%	75% to 0%	75% to 25%	75% to 50%		75% to 100%
	100%	100% to 0%	100% to 25%	100% to 50%	100% to 75%	

T_{γ} is determined by 10% to 90% brightness difference of rising or falling period. (As illustrated)

The response time is defined as the following figure and shall be measured by switching the input signal for “any level of gray(bright)” and “any level of gray(dark)”.

FIG.3 Response Time



4. Light source here is the standard light source “C” which is defined by CIE and driving voltages are based on suitable gamma voltages. The calculating method is as following :
- Measure the “Module” and “BLU” optical spectrums (W, R, G, B).
 - Calculate cell spectrum from “Module” and “BLU” spectrums.
 - Calculate color chromaticity by using cell spectrum and the spectrum of standard light source “C”.
5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG4.
6. The variation in surface luminance, δ_{WHITE} is defined (center of Screen) as: $\delta_{\text{WHITE(9P)}} = \text{Minimum}(L_{\text{on1}}, L_{\text{on2}}, \dots, L_{\text{on9}}) / \text{Maximum}(L_{\text{on1}}, L_{\text{on2}}, \dots, L_{\text{on9}})$.

FIG.4 Viewing Angle

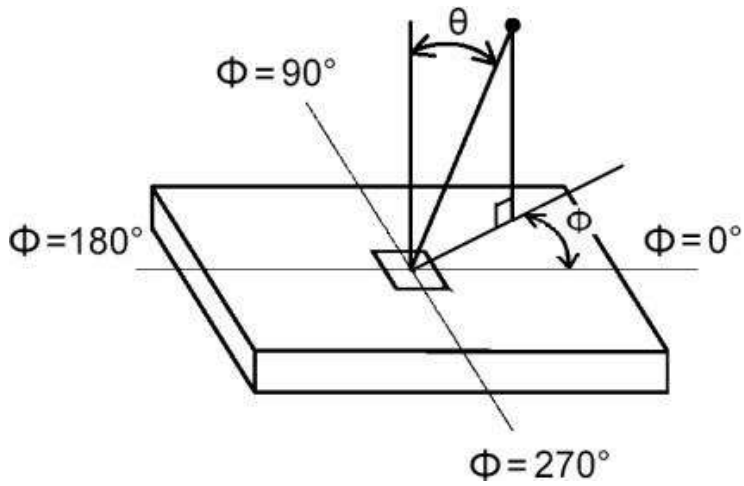
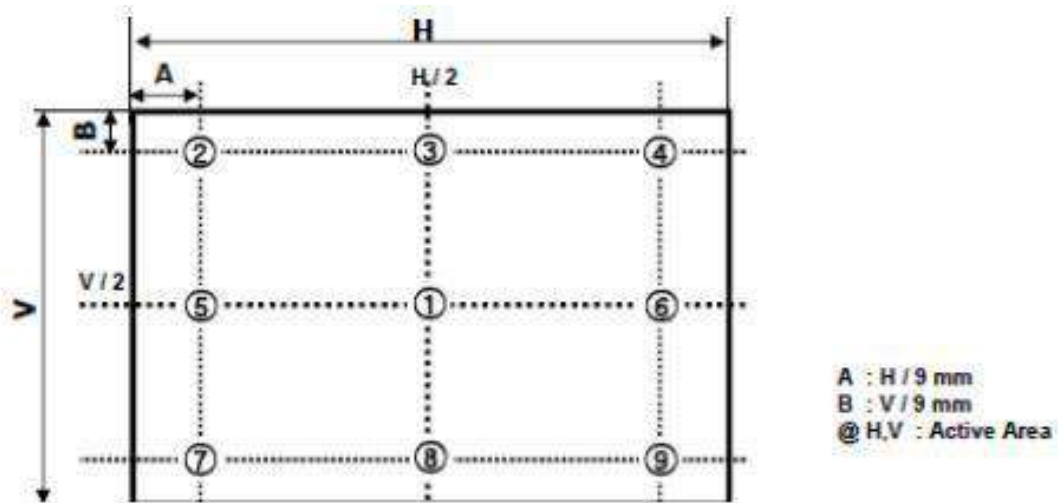


FIG. 5 Luminance

Measuring point for surface luminance & luminance variation CA-310 ,Contact method)



4. Interface Specification

4.1 Open Cell Input power

The P430HVN07.0 Open Cell Unit requires power input which is employed to power the LCD electronics and to drive the TFT array and liquid crystal.

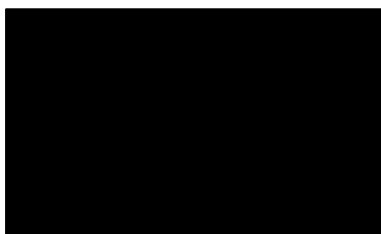
Item		Symbol	Min.	Typ.	Max	Unit	Note
Power Supply Input Voltage		V_{DD}	10.8	12	13.2	V	1
Power Supply Input Current	Black pattern	I_{DD}	-	0.42	0.5	A	2
	White pattern		-	0.42	0.5	A	
	H-strip pattern		-	0.65	0.78	A	
Power Consumption	Black pattern	P_C	-	5.04	6	Watt	
	White pattern		-	5.04	6	Watt	
	H-strip pattern		-	7.8	9.36	Watt	
Inrush Current		I_{RUSH}	--	--	5	A	3

Note1. The ripple voltage should be fewer than 10% of V_{DD} and min. V_{DD} (including ripple min.) must be over 10.8V at any time.

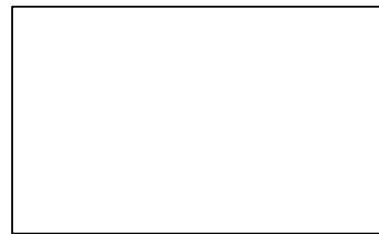
Note2. Test Condition:

- (1) $V_{DD} = 12.0V$, (2) $F_v = 60Hz$, (3) $F_{clk} = 74.25MHz$, (4) Temperature = 25 °C
(5) Power dissipation check pattern. (Only for power design)

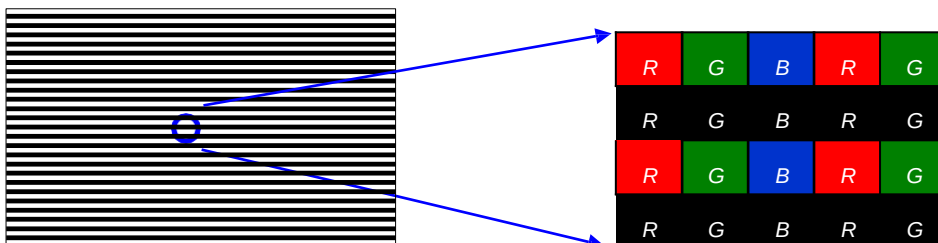
a. Black pattern



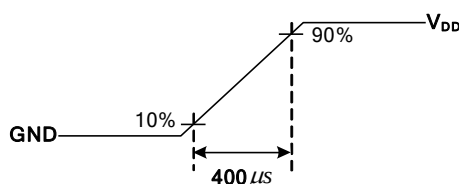
b. White pattern



c. H-Strip pattern



Note3. Measurement condition : Rising time = 400us

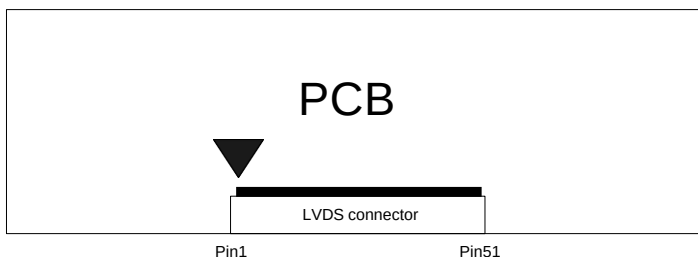


4.2 Open Cell Input Connection

■ LCD connector: 187059-5122 (P-two, LVDS connector) or compatible

PIN	Symbol	Description	Note	PIN	Symbol	Description	Note
1	N.C.	No connection	2	26	GND or N.C	Ground or No connection	7
2	SCL	I2C Clock	3,4	27	N.C.	No connection	2
3	WP	I2C Write Protection High(3.3V) for Writable, Low(GND) for Protection	3,5	28	CH2_0-	LVDS Channel 2, Signal 0-	
4	SDA	I2C Data	3,4	29	CH2_0+	LVDS Channel 2, Signal 0+	
5	N.C.	No connection	2	30	CH2_1-	LVDS Channel 2, Signal 1-	
6	N.C.	No connection	2	31	CH2_1+	LVDS Channel 2, Signal 1+	
7	LVDS_SEL	LVDS data format selection Open/High(3.3V) for NS, Low(GND) for JEIDA	3,6	32	CH2_2-	LVDS Channel 2, Signal 2-	
8	N.C.	No connection	2	33	CH2_2+	LVDS Channel 2, Signal 2+	
9	N.C.	No connection	2	34	GND	Ground	
10	N.C.	No connection	2	35	CH2_CLK-	LVDS Channel 2, Clock -	
11	GND	Ground		36	CH2_CLK+	LVDS Channel 2, Clock +	
12	CH1_0-	LVDS Channel 1, Signal 0-		37	GND	Ground	
13	CH1_0+	LVDS Channel 1, Signal 0+		38	CH2_3-	LVDS Channel 2, Signal 3-	
14	CH1_1-	LVDS Channel 1, Signal 1-		39	CH2_3+	LVDS Channel 2, Signal 3+	
15	CH1_1+	LVDS Channel 1, Signal 1+		40	N.C.	No connection	2
16	CH1_2-	LVDS Channel 1, Signal 2-		41	N.C.	No connection	2
17	CH1_2+	LVDS Channel 1, Signal 2+		42	N.C.	No connection	
18	GND	Ground		43	N.C.	No connection	
19	CH1_CLK-	LVDS Channel 1, Clock -		44	GND	Ground	
20	CH1_CLK+	LVDS Channel 1, Clock +		45	GND	Ground	
21	GND	Ground		46	GND	Ground	
22	CH1_3-	LVDS Channel 1, Signal 3-		47	N.C.	No connection	2
23	CH1_3+	LVDS Channel 1, Signal 3+		48	V _{DD}	Power Supply Input Voltage	
24	N.C.	No connection	2	49	V _{DD}	Power Supply Input Voltage	
25	N.C.	No connection	2	50	V _{DD}	Power Supply Input Voltage	
				51	V _{DD}	Power Supply Input Voltage	

Note1. Pin number start from the left side as the following figure.



Note2. Please leave this pin unoccupied. It can not be connected by any signal (Low/GND/High).

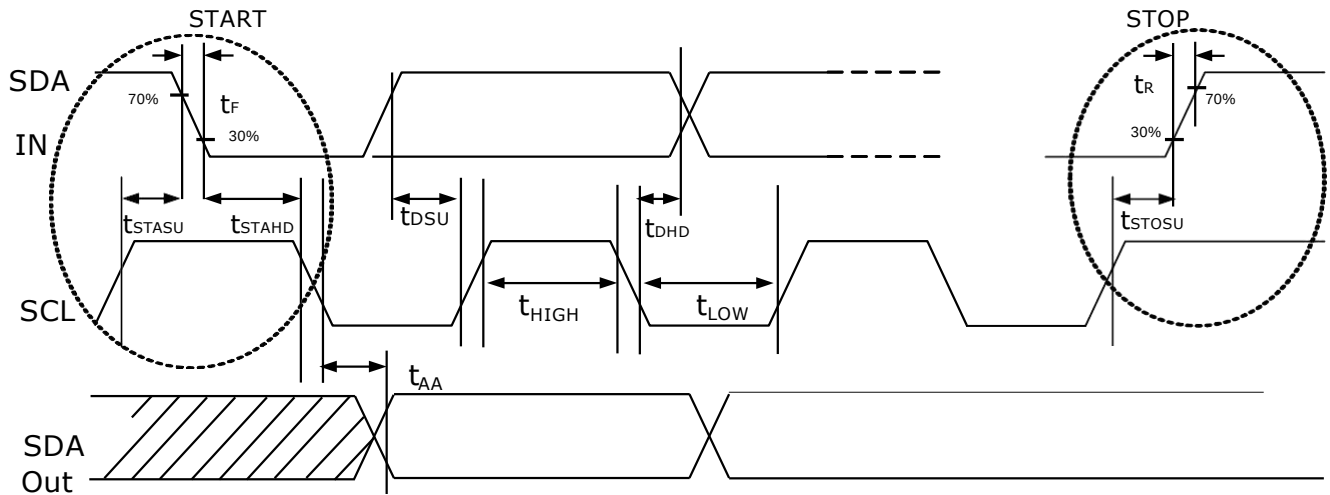
Note3. Input control signal threshold voltage definition

Item	Symbol	Min.	Typ.	Max.	Unit
Input High Threshold Voltage	VIH	2.7	-	3.6	V
Input Low Threshold Voltage	VIL	0	-	0.6	V

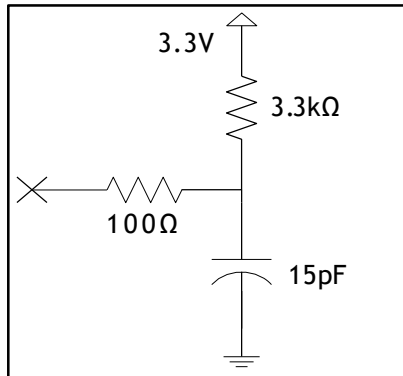
Note4. I2C Data and Clock

I2C Data and Clock timing

Parameter		Symbol	Min.	Typ.	Max	Unit
I2C	SCL clock frequency	fSCL	-	-	350	kHz
	Clock Pulse Width Low	tLOW	1.85	-	-	us
	Clock Pulse Width High	tHIGH	0.4	-	-	us
	Clock Low to Data Output Valid	tAA	1.76	-	-	us
	Start Setup Time	tSTASU	0.6	-	-	us
	Start Hold Time	tSTAHD	0.6	-	-	us
	Stop Setup Time	tSTOSU	0.6	-	-	us
	Data In Setup Time	tDSU	0.1	-	-	us
	Data In Hold Time	tDHD	0	-	-	us
	SCL/SDA Rise Time	tR	-	-	0.3	us
	SCL/SDA Fall Time	tF	-	-	0.3	us



Input equivalent impedance of SDA/SCL pin

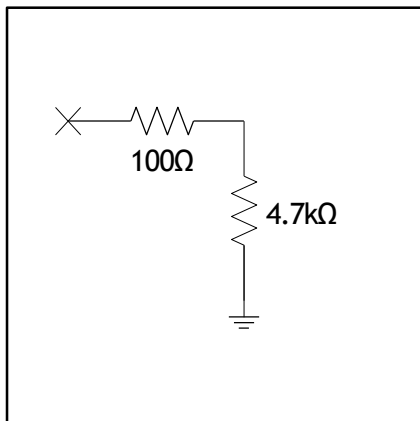


Note5. Write Protection

Mode selection

WP	Note
L or OPEN	Protection
H	Writable

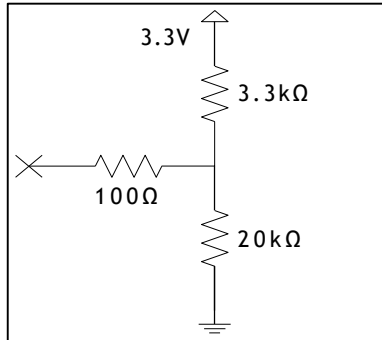
Input equivalent impedance of WP pin



Note6. LVDS data format selection

LVDS_SEL	Mode
H or OPEN	NS
L	Jeida

Input equivalent impedance of LVDE_SEL pin

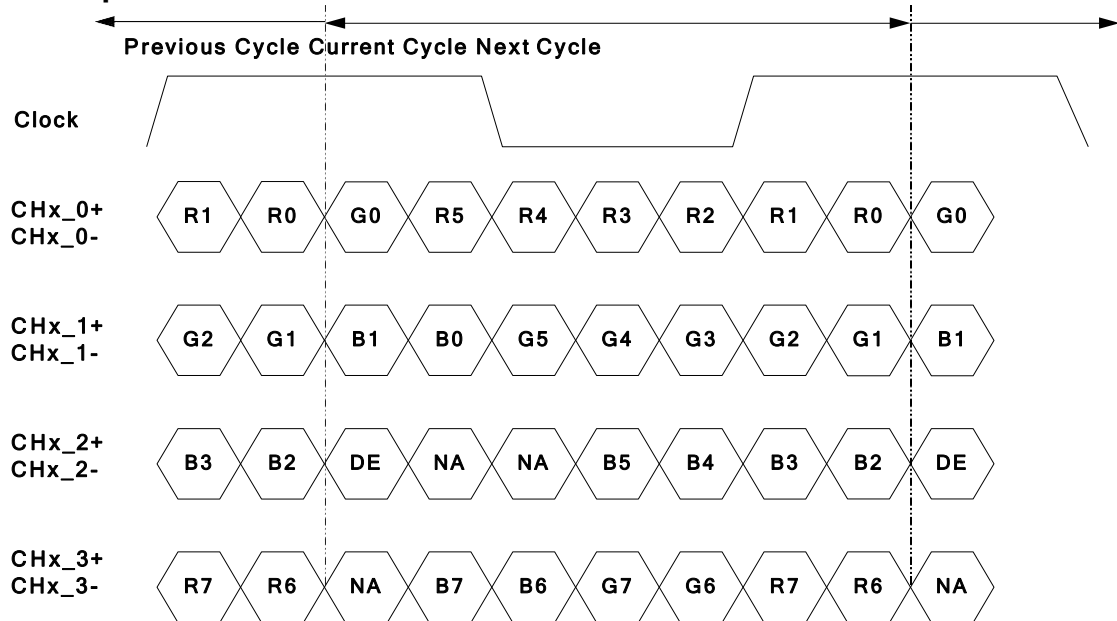


Note7. Please leave this pin unoccupied or connect to ground. It can not be connected by any signal (Low/High).

4.3 Input Data Format

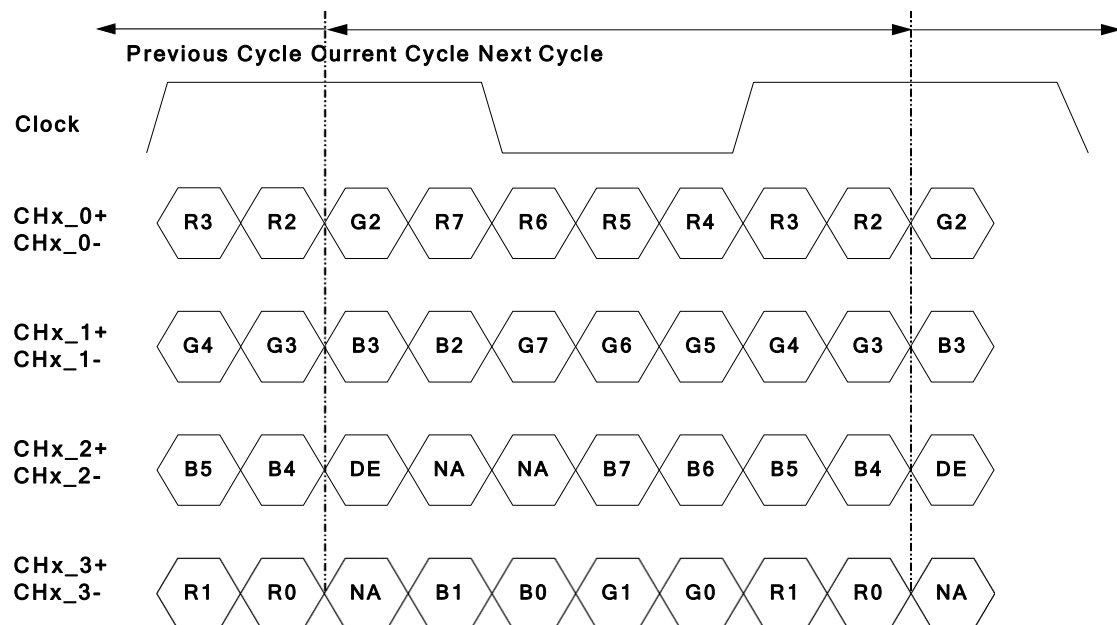
4.3.1 LVDS data mapping

■ LVDS Option NS



Note: x = 1, 2, 3, 4...

■ LVDS Option JEIDA



Note: x = 1, 2, 3, 4...

4.3.2 Color Input DataReference

The brightness of each primary color (red, green and blue) is based on the 8 bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

COLOR DATA REFERENCE

Color		Input Color Data																								
		RED								GREEN								BLUE								
		MSB				LSB				MSB				LSB				MSB				LSB				
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0	
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
R	RED(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	RED(001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

	RED(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
G	GREEN(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	GREEN(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	

	GREEN(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
B	BLUE(000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	BLUE(001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	

	BLUE(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	

5. Signal Timing Specification

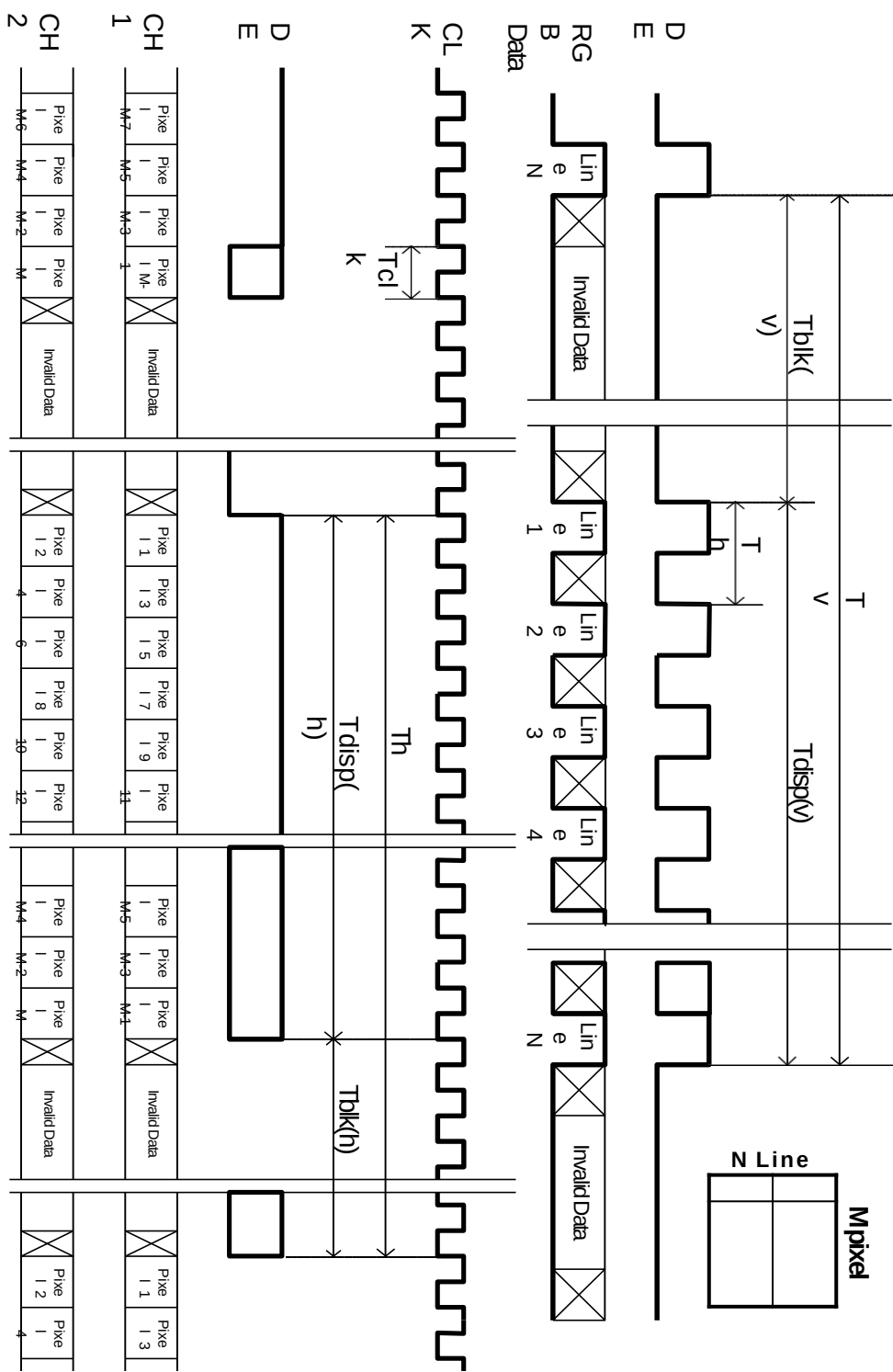
5.1 Input Timing

This is the signal timing required at the input of the user connector. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

Timing Table (DE only Mode)

Signal	Item	Symbol	Min.	Typ.	Max	Unit
Vertical Section	Period	Tv	1100	1125	1480	Th
	Active	Tdisp (v)	1080			
	Blanking	Tblk (v)	20	45	400	Th
Horizontal Section	Period	Th	1030	1100	1325	Tclk
	Active	Tdisp (h)	960			
	Blanking	Tblk (h)	70	140	365	Tclk
Clock	Frequency	Fclk=1/Tclk	53	74.25	82	MHz
Vertical Frequency	Frequency	Fv	47	60	63	Hz
Horizontal Frequency	Frequency	Fh	60	67.5	73	KHz

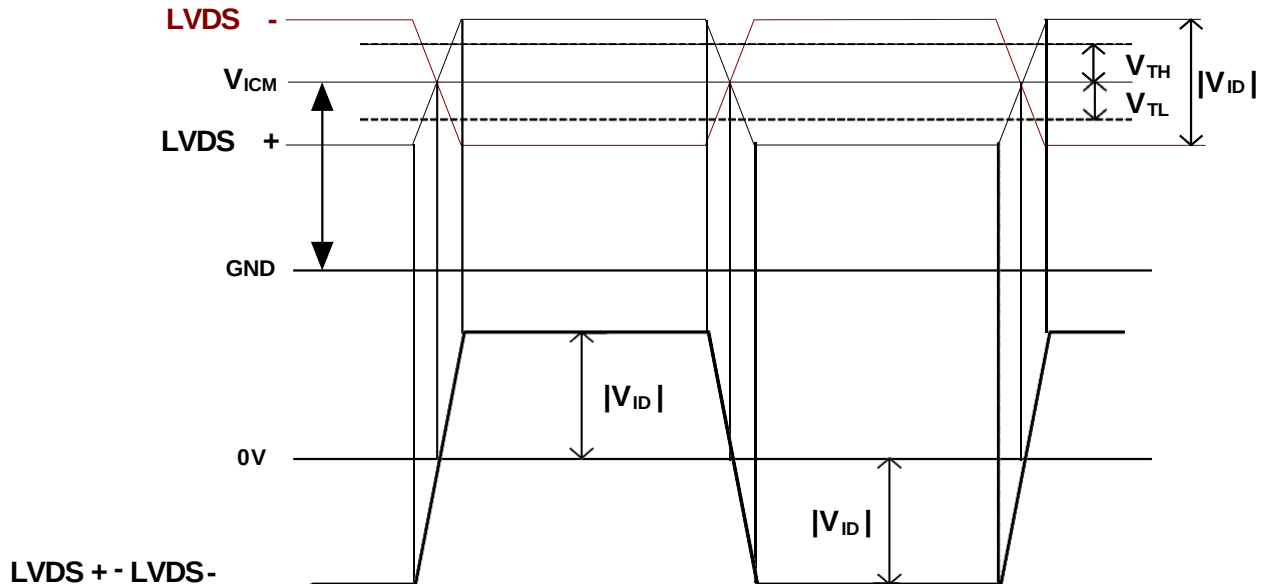
The timing diagrams of the input timing



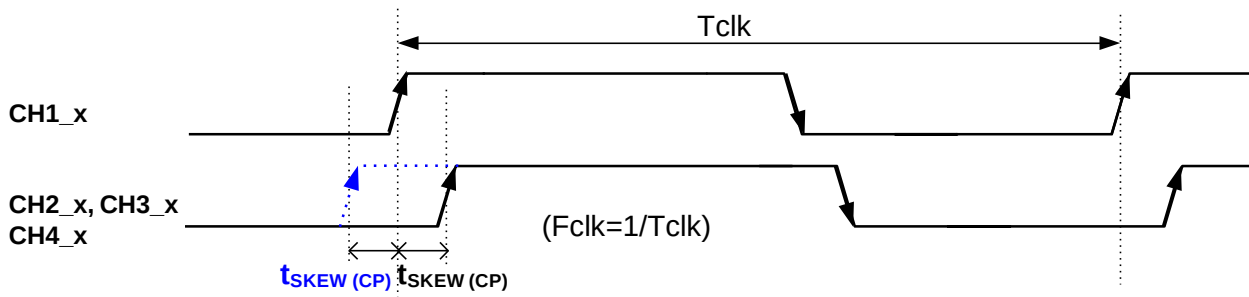
5.2 Input interface characteristics

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max		
LVDS Interface	Input Differential Voltage	$ V_{ID} $	200	400	600	mV _{DC}	1
	Differential Input High Threshold Voltage	V_{TH}	+100	--	+300	mV _{DC}	1
	Differential Input Low Threshold Voltage	V_{TL}	-300	--	-100	mV _{DC}	1
	Input Common Mode Voltage	V_{ICM}	1.1	1.25	1.4	V _{DC}	1
	Input Channel Pair Skew Margin	$t_{SKEW (CP)}$	-500	--	+500	ps	2
	Input Channel Pair Skew Margin (only for M'Star MST7428BB)	$t_{SKEW (CP)}$	-400	--	+400	ps	2
	Receiver Clock : Spread Spectrum Modulation range	Fclk_ss	Fclk -3%	--	Fclk +3%	MHz	3
	Receiver Clock : Spread Spectrum Modulation frequency	Fss	30	--	200	KHz	3
	Receiver Data Input Margin Fclk = 85 MHz Fclk = 65 MHz	tRMG	-0.4 -0.5	-- --	0.4 0.5	ns	8

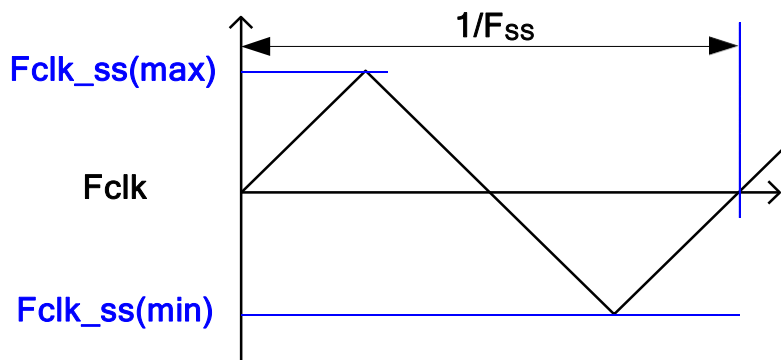
Note1. $V_{ICM} = 1.25V$



Note2. Input Channel Pair Skew Margin

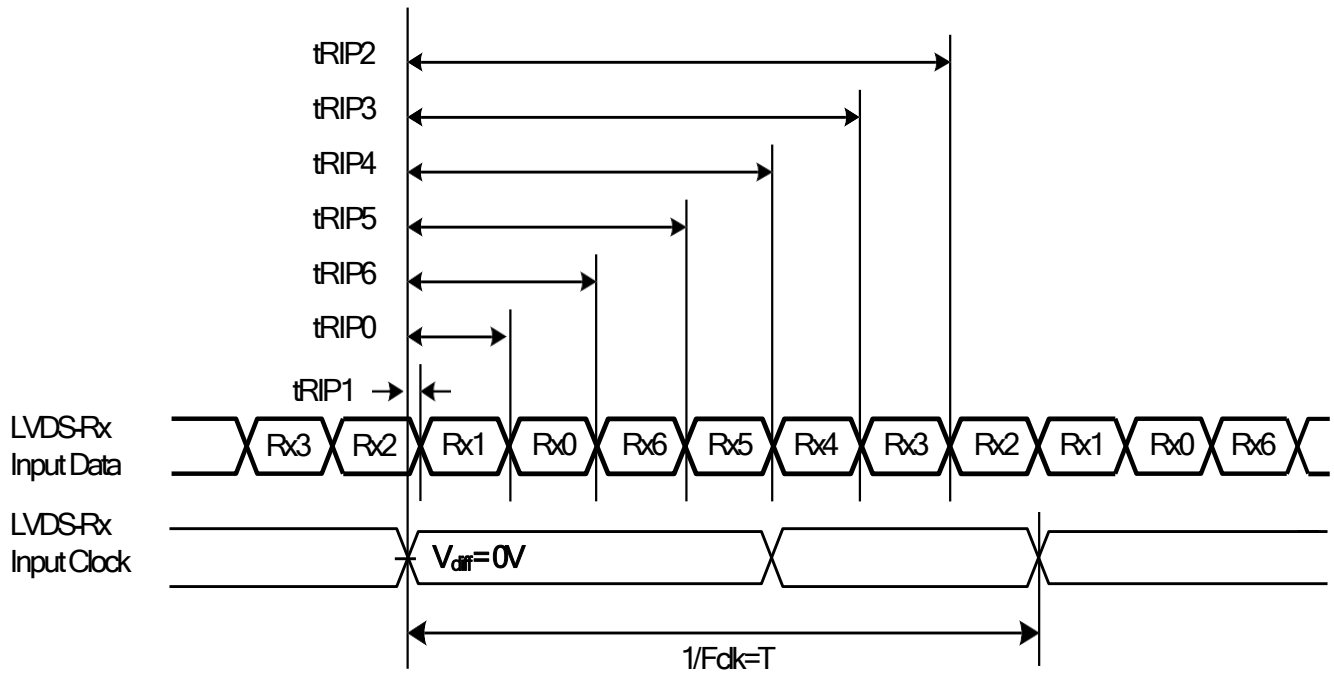


Note3. LVDS Receiver Clock SSCG (Spread spectrum clock generator) is defined as below figures.

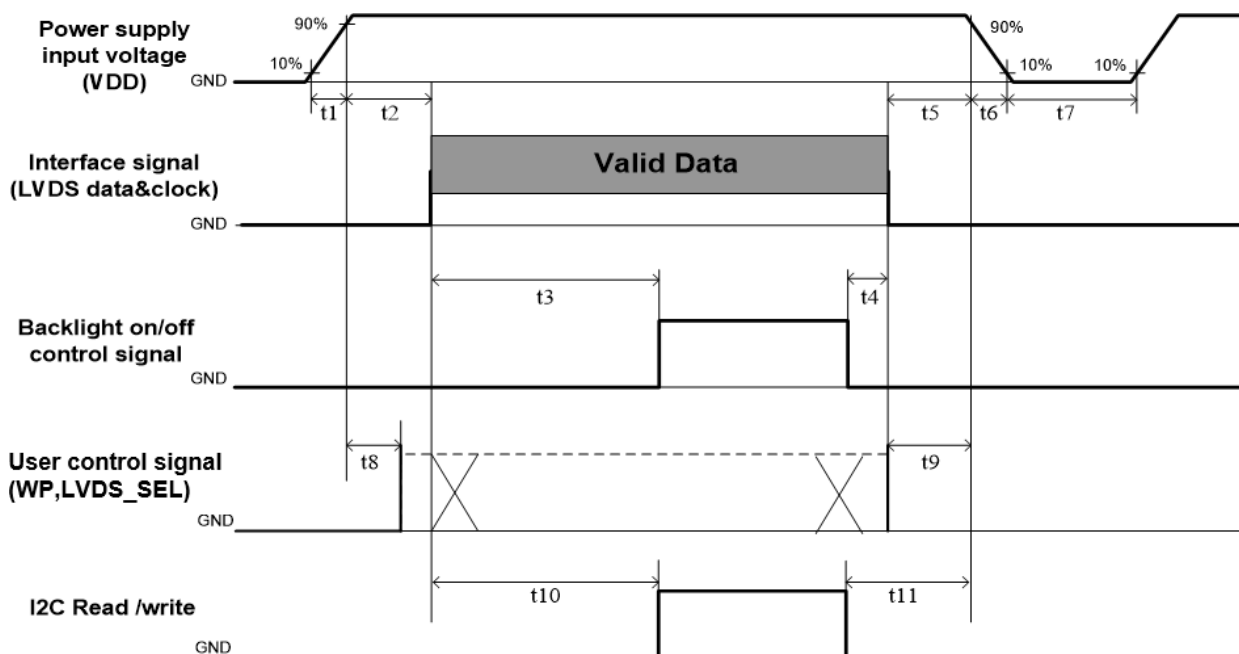


Note4. Receiver Data Input Margin

Parameter	Symbol	Rating			Unit	Note
		Min	Type	Max		
Input Clock Frequency	Fclk	Fclk (min)	--	Fclk (max)	MHz	$T=1/F_{clk}$
Input Data Position0	tRIP1	- tRMG	0	tRMG	ns	
Input Data Position1	tRIP0	$T/7- tRMG $	$T/7$	$T/7+ tRMG $	ns	
Input Data Position2	tRIP6	$2T/7- tRMG $	$2T/7$	$2T/7+ tRMG $	ns	
Input Data Position3	tRIP5	$3T/7- tRMG $	$3T/7$	$3T/7+ tRMG $	ns	
Input Data Position4	tRIP4	$4T/7- tRMG $	$4T/7$	$4T/7+ tRMG $	ns	
Input Data Position5	tRIP3	$5T/7- tRMG $	$5T/7$	$5T/7+ tRMG $	ns	
Input Data Position6	tRIP2	$6T/7- tRMG $	$6T/7$	$6T/7+ tRMG $	ns	



5.3 Power Sequence for LCD



Parameter	Values			Unit
	Min.	Type.	Max.	
t1	0.4	---	30	ms
t2	0.1	---	50	ms
t3	450	---	---	ms
t4	0 ^{*1}	---	---	ms
t5	0	---	---	ms
t6	---	---	--- ^{*2}	ms
t7	500	---	---	ms
t8	20 ^{*3}	---	50	ms
t9	0	---	---	ms
t10	450	---	---	ms
t11	150	---	---	ms

Note:

- (1) t4=0 : concern for residual pattern before BLU turn off.
- (2) t6 : voltage of VDD must decay smoothly after power-off. (customer system decide this value)
- (3) When user control signal is N.C. (no connection), opened in Transmitted end, t8 timing spec can be negligible.

6. Backlight Specification

6.1 Electrical specification

	Item	Symbol	Condition	Min	Typ	Max	Unit	Note
1	Power Supply Input Voltage	V _{DDB}	-	22	24	26	V	-
2	Power Supply Input Current	I _{DDB}	V _{DDB} =24V	-	2.8	2.88	A	1
3	Power Consumption	P _{DDB}	V _{DDB} =24V	-	67	75	Watt	
4	Control signal voltage	V _{Signal}	V _{DDB} =24V	2.5	3.3	5	V	
				0	-	0.8		
5	Control signal current	I _{Signal}	V _{DDB} =24V	-	-	1.5	mA	
6	Brightness Voltage	V _{adj}	V _{DDB} =24V	0(bright)	-	5(dark)	V	
7	DET status signal	DET	V _{DDB} =24V	Open Collector			V	
				0	-	0.8	V	
8	LED lifetime	LTLED	-	30,000	-	-	Hr	2

Note 1: Dimming ratio= 100%, (Ta=25±5°C, Turn on for 45minutes)

Note : The lifetime (MTTF) is defined as the time which luminance of LED is 50% compared to its original value.
[Operating condition: Continuous operating at Ta = 25±2°C, for single LED only]

6.2 Input Pin Assignment

Connector: PH2.0-14AWB or compatible

Pin	Symbol	Description	Note
1	V24	V24 CHANNEL DC OUTPUT	
2	V24	V24 CHANNEL DC OUTPUT	
3	V24	V24 CHANNEL DC OUTPUT	
4	V24	V24 CHANNEL DC OUTPUT	
5	V24	V24 CHANNEL DC OUTPUT	
6	GND	V24 CHANNELS DC RETURN	
7	GND	V24 CHANNELS DC RETURN	
8	GND	V24 CHANNELS DC RETURN	
9	GND	V24 CHANNELS DC RETURN	
10	GND	V24 CHANNELS DC RETURN	
11	NC	NC	
12	BL-ON	BACKLIGHT ON/OFF CONTROL SIGNAL INPUT	1,2
13	ADJ	DIMMING SIGNAL INPUT	3
14	NC	NC	

Note1. input control signal threshold voltage definition

Item	Symbol	Min.	Typ.	Max.	Unit
Input High Threshold Voltage	VIH	2.5	-	5.0	V
Input Low Threshold Voltage	VIL	0	-	0.8	V

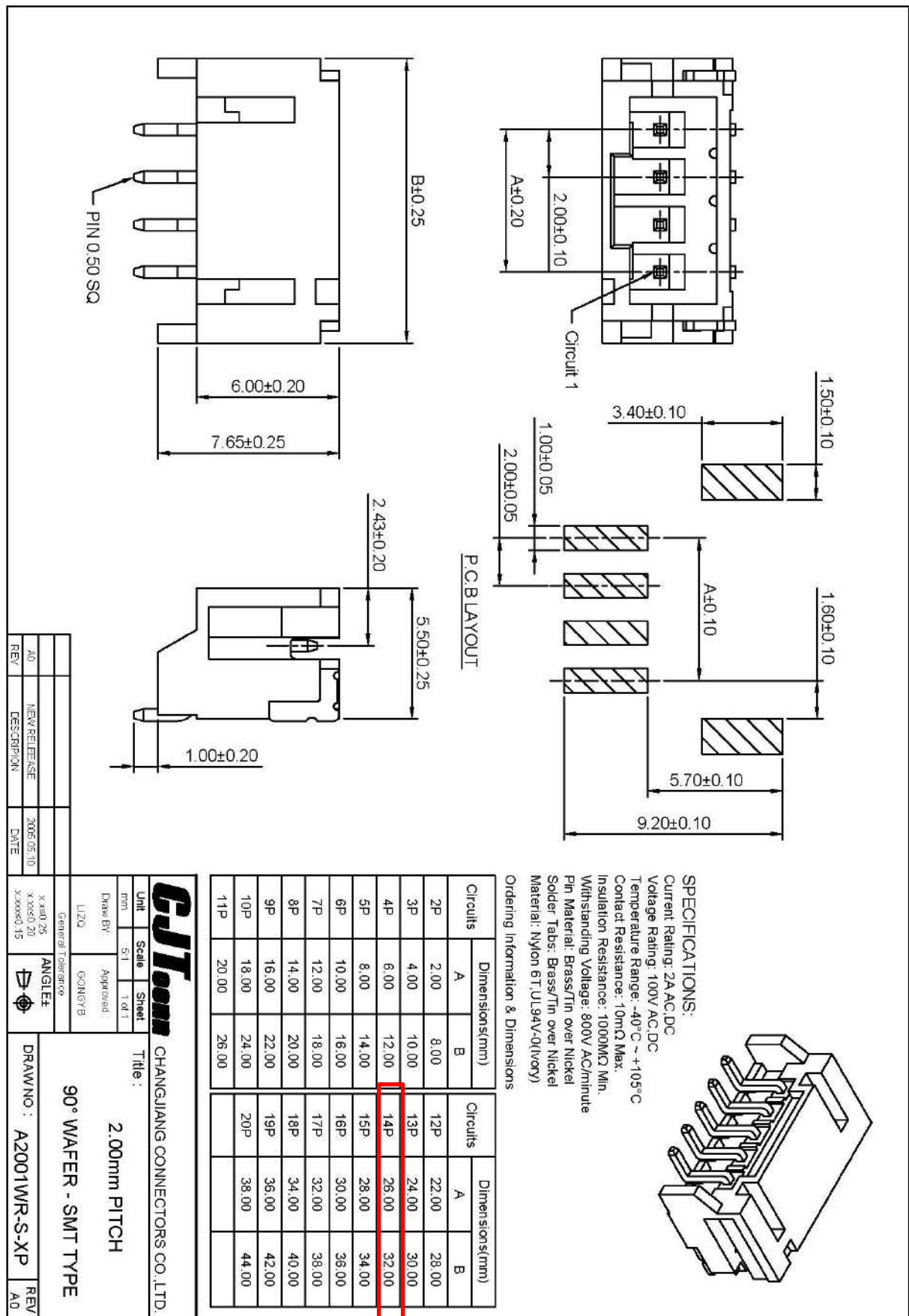
Note2 VBLON

Mode selection

VBLON	Note
VIH	BL On
VIL	BL Off

Note3. 0V=Brightness Max 5V=Brightness Min

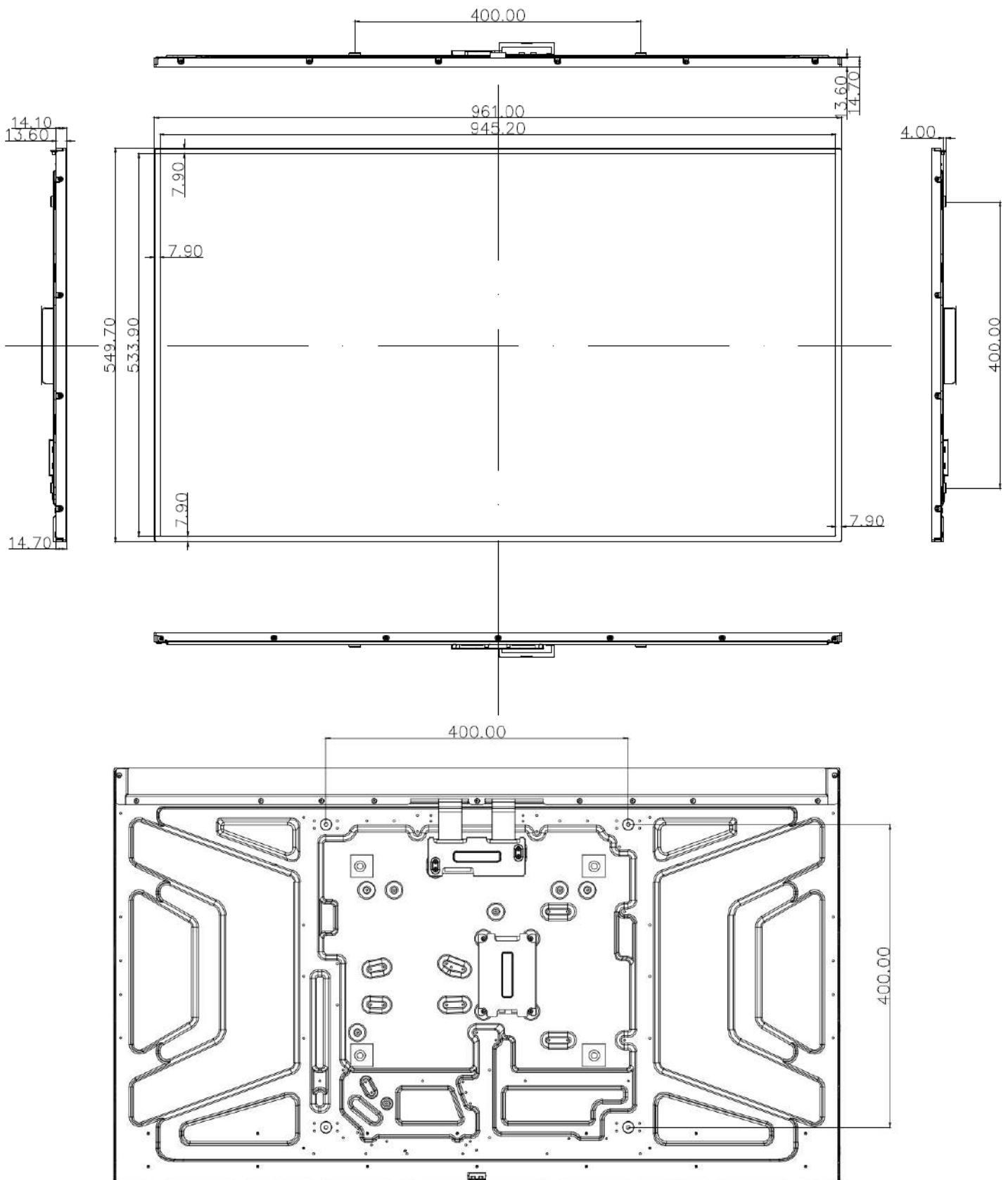
Note3 Input Pin Drawing



7. Mechanical Characteristics

The contents provide general mechanical characteristics for the model P430HVN07.0 In addition the figures in the next page are detailed mechanical drawing of the LCD.

Item		Dimension	Unit	Note
Outline Dimension	Horizontal	961	mm	TYP
	Vertical	549.7	mm	TYP
	Depth (Dmin)	17.1	mm	Front bezel to Back Bezel
Weight	7750		g	TYP



8. Reliability Test Items

	Test Item	Condition
1	THB	50℃ 80%RH 240hr operation
2	HTO	50℃ 240hr operation
3	LTO	0℃ 240hr operation
4	HTS	60℃ 240hr Storage
5	LTS	-20℃ 240hr Storage
6	TST	-20 ℃ ↔ 60 ℃ (0.5 hr), 100 cycle
7	MDL ESD	Air : 150 pF, 330Ω, 15 KV Contact : 150 pF, 330Ω, 8 KV
8	P-VIB	1.15G , 2-200hz ,

9. International Standard

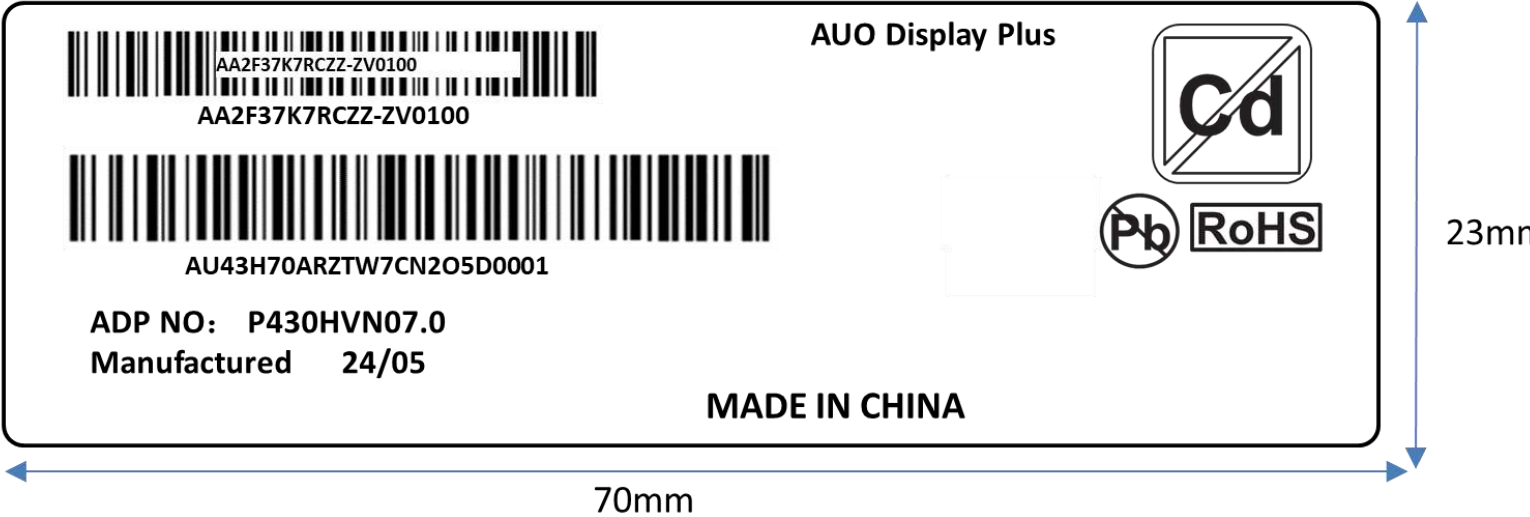
9.1 Safety

- (1) UL 62368-1 and CAN/CSA C22.2 No. 62368-1-14 - Audio/video, information and communication technology equipment Part 1: Safety requirements
- (2) IEC 62368-1:2014 (Second Edition)

10. Packing

10.1 Definition of Label

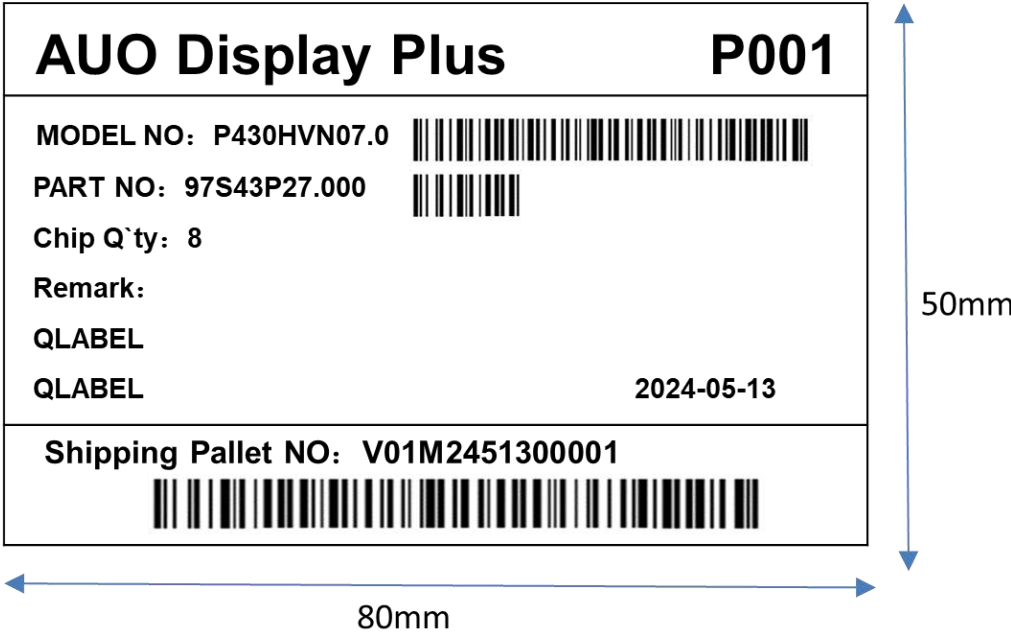
A. Panel Label:



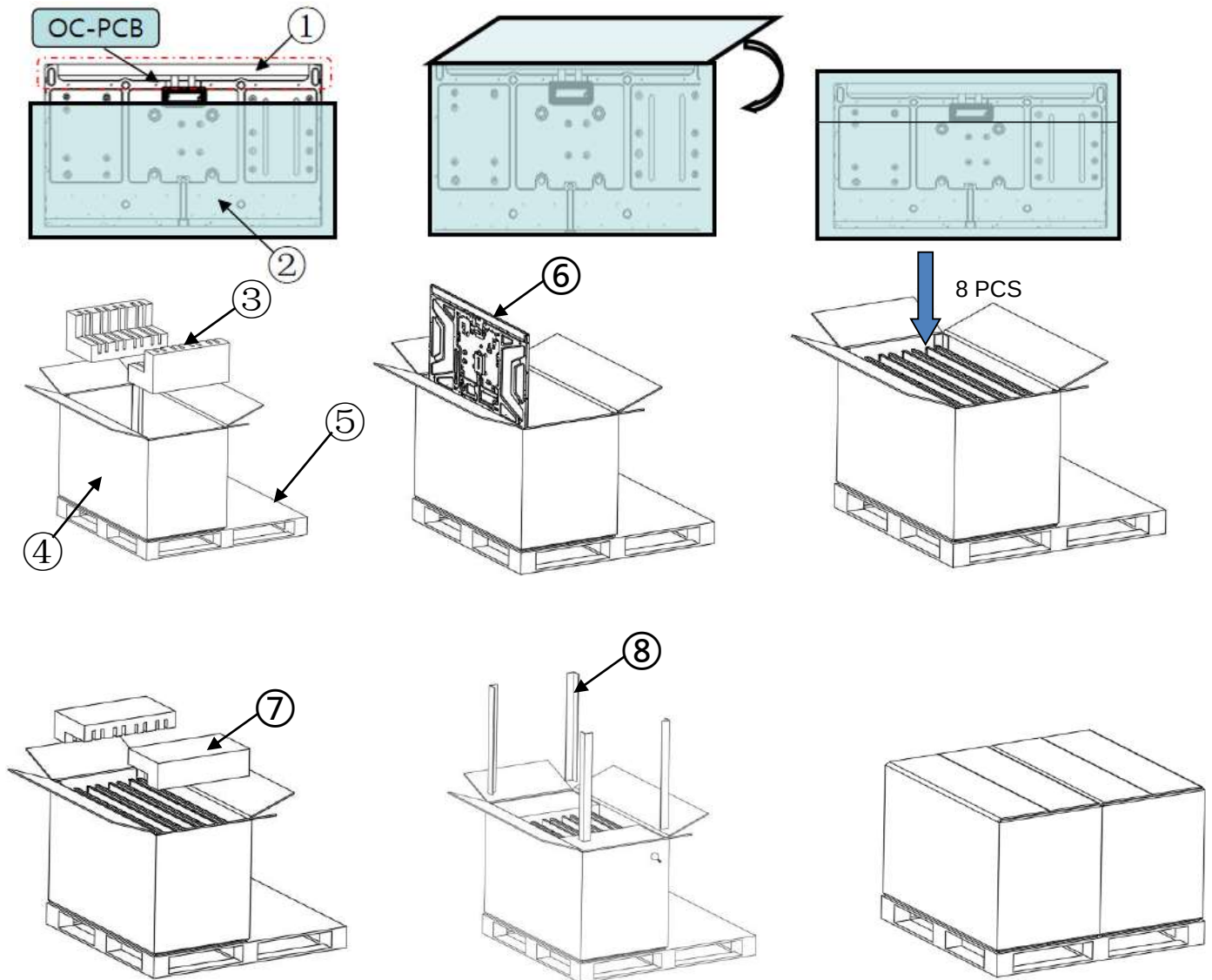
Green mark description

- (1) For Pb Free Product, AUO will add for identification.
 - (2) For RoHS compatible products, AUO will add RoHS for identification.
- Note: The green Mark will be present only when the green documents have been ready by AUO internal green team. (definition of green design follows the AUO green design checklist.)

B. Carton Label:



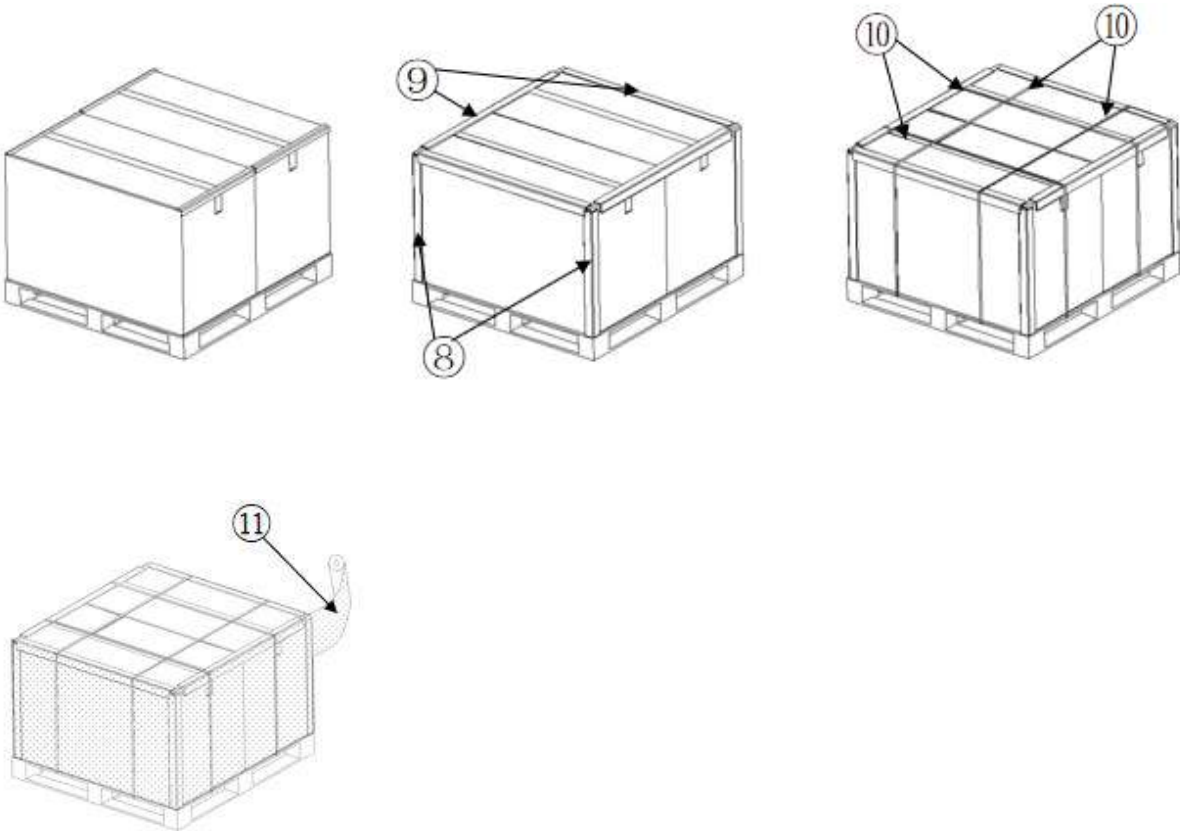
10.2 Packing Methods



NO	DESCRIPTION	MATERIAL	Qty.
1	LCD Module	/	16
2	Bag	PE	16
3	Pallet		1
4	Buffer foam (Down)	EPE	4
5	Buffer foam (Top)	EPE	4
6	Corner Protector (1000mm)	paper	4
7	Corner Protector (635mm)	paper	4
8	Corner Protector (650mm)	paper	4

10.3 Pallet and Shipment Information

	Item	Specification			Packing Remark
		Qty.	Dimension	Weight (kg)	
1	Packing BOX	8pcs/box	1065mm*570mm*650mm	67.8	
2	Pallet	1	1150mm*1080mm*114mm	150.6	
3	Boxes per Pallet	2 boxes/pallet			
4	Panels per Pallet	16 pcs/pallet			
5	Pallet after packing	16	1150mm*1080mm*769mm		



11. Precautions

Please pay attention to the followings when you use this TFT LCD module.

1. Mounting Precautions

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter cause circuit broken by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizer with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front/ rear polarizer. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage:
 $V = \pm 200\text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it may become lower.) And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may

be important to minimize the interface.

- (7) The conductive material and signal cables are kept away from LED driver inductor to prevent abnormal display, sound noise and temperature rising.

3. Operating Condition for Public Information Display

The device listed in the product specification is designed and manufactured for PID (Public Information Display) application. To optimize module's lifetime and function, below operating usages are required.

(1) Normal operating condition

- A. Operating temperature: 0~50°C
- B. Operating humidity: 10~90%
- C. Display pattern: dynamic pattern (Real display).

Note) Long-term static display would cause image sticking.

(2) Operation usage to protect against image sticking due to long-term static display.

- A. Suitable operating time: under 24 hours a day
(* The moving picture can be allowed for 24 hours a day)
- B. Liquid Crystal refresh time is required. Cycling display between 5 minutes' information (static) display and 10 seconds' moving image.
- C. Periodically change background and character (image) color.
- D. Avoid combination of background and character with large different luminance.

(2) Periodically adopt one of the following actions after long time display.

- A. Running the screen saver (motion picture or black pattern)
- B. Power off the system for a while

(3) LCD system is required to place in well-ventilated environment. Adapting active cooling system is highly recommended.

(4) Product reliability and functions are only guaranteed when the product is used under right operation usages. If product will be used in extreme conditions, such as high temperature/ humidity, display stationary patterns, or long operation time etc..., it is strongly recommended to contact AUO for filed application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at airports, transit stations, banks, stock market and controlling systems.

4. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wristband etc. And don't touch interface pin directly.

5. Precautions for Strong Light Exposure

- (1) Strong light exposure causes degradation of polarizer and color filter.
- (2) To keep display function well as a digital signage application, especially the component of TFT is very sensitive to sunlight, it is necessary to set up blocking device protecting panel from radiation of ambient environment.

6. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature at 5°C to 35°C with humidity between 30% and 70%.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.
- (3) Storage condition is guaranteed under packing conditions.
- (4) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition.

7. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.